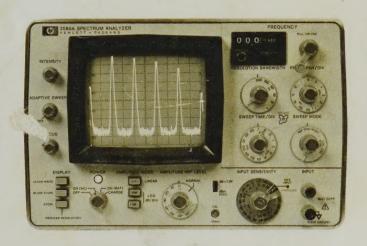
## OPERATING AND SERVICE MANUAL











## **OPERATING AND SERVICE MANUAL**

# MODEL 3580A SPECTRUM ANALYZER

Serial Number: 1415A-00741 (see note below)

#### IMPORTANT NOTICE

If the Serial Number of your instrument is lower than the one on this title page, the manual contains revisions that do not apply to your instrument. Backdating information given in the manual adapts it to these earlier instruments.

Where practical, backdating changes are given on the schematic diagrams. These changes are indicated by a dagger sign (†) which refers to the corresponding backdating note on the schematic or apron page. Backdating changes not given on the schematics are flagged by a numbered delta  $(\Delta_1)$  which refers to the corresponding numbered change in the Backdating Section (Section VIII).

## WARNING

To help minimize the possibility of electrical fire or shock hazards, do not expose this instrument to rain or excess moisture.

Manual Part No. 03580-90002

Microfiche No. 03580-90092

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Printed: November 1976



#### CERTIFICATION

Hewlett-Packard Company certifies that this instrument met its published specifications at the time of shipment from the factory. Hewlett-Packard Company further certifies that its calibration measurements are traceable to the United States National Bureau of Standards, to the extent allowed by the Bureau's calibration facility, and to the calibration facilities of other International Standards Organization members.

#### WARRANTY AND ASSISTANCE

This Hewlett-Packard product is warranted against defects in materials and workmanship for a period of one year from the date of shipment, except that in the case of certain components, if any, listed in Section I of this operating manual, the warranty shall be for the specified period. Hewlett-Packard will, at its option, repair or replace products which prove to be defective during the warranty period provided they are returned to Hewlett-Packard, and provided the proper preventive maintenance procedures as listed in this manual are followed. Repairs necessitated by misuse of the product are not covered by this warranty. NO OTHER WARRANTIES ARE EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE. HEWLETT-PACKARD IS NOT LIABLE FOR CONSEQUENTIAL DAMAGES.

If this product is sold as part of a Hewlett-Packard integrated instrument system, the above warranty shall not be applicable, and this product shall be covered only by the system warranty.

Service contracts or customer assistance agreements are available for Hewlett-Packard products.

For any assistance, contact your nearest Hewlett-Packard Sales and Service Office. Addresses are provided at the back of this manual.

#### MODEL 3580A

#### SPECTRUM ANALYZER

Manual Part Number 03580-90002

New or Revised Item

CHANGE NO. 1 applies to serial number 1415A02091 and greater.

Page 6-5. Change part numbers on:

A2U2,5,8 to 1820-1490 (TTL-CNTR 74LS90) A2U6 to 1820-0304 (TTL-FF SN7472N) A2U11 to 1820-1202 (TTL-GATE 74LSION)

Page 6-7. Change part numbers on:

A3U5 to 1820-1418 (IC-74LS42)
A3U6 to 1820-1574 (IC SN74LS73N)
A3U7 to 1820-0304 (TTL FF SN7472N)
A3U8 to 1820-1197 (TTL GATE 74LS00N)
A3U9,13,14 to1820-1204 (IC-SN74LS20N)
A3U11 to 1820-1144 (IC-SN74LS02N)
A3U12 to 1820-1202 (TTL GATE 74LS10N)

Page 6-7. Change A3R52 to part number 0683-1635 (R:Fxd 16 K ohm 5% 1/4 W).

Page 6-20. Change A7U15 to part number 1820-1601 (CMOSGTE CD4070BY).

Page 7-21/7-22, Figure 7-7. Change value of R52 on schematic no. 4 to 16 k $\Omega$ .

CHANGE NO. 2 applies to serial number 1415A02011 and greater.

Page 6-3. Change A2C2,3 to part number 0122-0089, manufacturer's part number MV109.

Page 6-16. Delete A6F1,F2 Fuse part number 2110-0343, qty. 2 Add F1 part number 2110-0490 (Fuse: 375A 125V) qty. 1 F2 part number 2110-0297 (Fuse: .5A 125V) qty. 1.

CHANGE NO. 3 applies to all serial numbers.

**Page 1-3.** Power requirements change to 100 V, 120 V, 220 V, or 240 V + 5%, -10%, 48 Hz to 440 Hz, 35 watts maximum.

CHANGE NO. 4 applies to serial number 1415A02051 and greater.

Page 6-17. Add part number 1400-0507 to 03580-66506 Hardware List.

Page 6-35. Change part number 0370-2473 to 0370-2994.

CHANGE NO. 5 applies to serial number 1415A02281 and greater.

Page 6-11. Change A4R71 to part number 0698-4482 (R:Fxd 17.4 K .01)

Change A4R109 to part number 0698-4429 (R:Fxd 1.87 K .01), Change A4R122B to part number 0698-4513 (R:Fxd 100 K .01),

Page 7-19/7-20, Figure 7-6. Change the values on schematic no. 3 R71 to 17.4 K R109 to 1.87 K

CHANGE NO. 6 applies to serial number 1415A02141 and greater.

Page 6-33. Add part number 7124-2308, Information Label, qty 1.

CHANGE NO. 7 applies to serial number 1415A01776 and greater.

Page 7-15/7-16, Figure 7-4. Change the ground of A9C26 from

ADDENDA.

Page 1-1, Paragraph 1-8. Change to read as follows:

There are three options available for the 3580A. Option 001 and Option 002 are listed in the following table. For further information concerning those options, refer to Table 1-2 or Section III in this manual or contact the nearest -hp- Sales and Service Office. Option 910 is an additional Operating and Service Manual.

Delete: Last two lines.

"Field Installation Kit -hp- 03580-80001 . . . Cover."

Add: Front Panel Cover Accessory -hp- 10101B.

Page 3-25, Paragraph 3-188. Continue last sentence in first paragraph.

...output configuration with an output level of 0 V to >1 vrms into 600 ohms (adjustable).

Page 5-5, Paragraph 5-20(b). Change -20 dBm to 0 dBm.

Page 5-17, Paragraph 5-57(c). Change -2915 volts  $\pm$  3 volts to about -2900 volts.

Page 6-34. Add -hp- Part No. 0340-0618, qty 1 Insulator Sheet under top cover part number 03580-04103.

Page 7-15/7-16, Figure 7-4. Change A9Q6 emitter voltage from  $-5.6\ V$  to  $-.56\ V.$ 



## CATHODE-RAY TUBE WARRANTY AND INSTRUCTIONS

The cathode-ray tube (CRT) supplied in your Hewlett-Packard Instrument and replacement CRT's purchased from HP are warranted by the Hewlett-Packard Company against electrical failure for a period of one year from the date of sale. Broken tubes and tubes with phosphor or mesh burns are not included under this warranty. No other warranty is expressed or implied.

#### **INSTRUCTION TO CUSTOMERS**

If the CRT is broken when received, a claim should be made with the responsible carrier. All warranty claims with Hewlett-Packard should be processed through your nearest Hewlett-Packard Sales/Service Office (listed at rear of instrument manual).

#### INSTRUCTIONS TO SALES/SERVICE OFFICE

Return defective CRT in the replacement CRT packaging material. If packaging material is not available, contact CRT Customer Serivce in Colorado Springs. The Colorado Springs Division must evaluate all CRT claims for customer warranty, Material Failure Report (MFR) credit, and Heart System credit. A CRT Failure Report form (see reverse side of this page) must be completely filled out and sent with the defective CRT to the following address:

#### HEWLETT-PACKARD COMPANY

1900 Garden of the Gods Road Colorado Springs, Colorado 80907

Attention: CRT Customer Service

Defective CRT's not covered by warranty may be returned to Colorado Springs for disposition. These CRT's, in some instances, will be inspected and evaluated for reliability information by our engineering staff to facilitate product improvements. The Colorado Springs Division is equipped to safely dispose of CRT's without the risks involved in disposal by customers or field offices. If the CRT is returned to Colorado Springs for disposal and no warranty claim is involved, write "Returned for Disposal Only" in item No. 5 on the form.

Do not use this form to accomplish CRT repairs. In order to have a CRT repaired, it must be accompanied by a customer service order (repair order) and the shipping container must be marked "Repair" on the exterior.

## **CATHODE-RAY TUBE FAILURE REPORT**

(This form must accompany all warranty claims and MFR/HEART credit claims.)

		Date
Submitted By (	Name)	
Name of Comp	any	
Address		
1. HP Instrun	nent Model No.	
2. HP instrum	nent Serial No.	
3. Defective C	RT Serial No.	Part No.
4. Replacemen	nt (New) CRT Serial No.	
5. Please desc below.	The the fairure and, if y	possible, show the trouble on the appropriate CRT face

8/74

6.

7.

#### **TABLE OF CONTENTS**

Section	Page	Section	n	Page
I. GENE 1-1. 1-4. 1-7. 1-9. 1-11. 1-13. 1-15.	CRAL INFORMATION1-1Description1-1Specifications1-1Options1-1Warranty Exceptions1-1Accessories Supplied1-1Accessories Available1-1Instrument and Manual Identification1-1		3-113. 3-132. 3-143. 3-147. 3-158. 3-163. 3-171. 3-175.	Sweep Modes.3-16Sweep Time and Sweep Rate.3-18External Triggering.3-19Adaptive Sweep.3-20Digitally-Stored Display.3-22Recorder Outputs.3-22Tracking Oscillator Output.3-23Tracking Oscillator Input.3-24L.O. Output.3-24
Section	Page		3-182.	Option 001
II. INSTA 2-1. 2-3. 2-5. 2-7. 2-9. 2-12. 2-13.	ALLATION		3-187. 3-191. 3-192. 3-195. 3-199. 3-203. 3-205. 3-207.	Option 002
2-14.	Operating and Storage			
0.15	Temperature (Option 001) 2-1	Section		Page
2-15. 2-17.	Installation		4-1. 4-3.	RY OF OPERATION
Section	Page		4-5.	Amplitude Section 4-1
III. OPER. 3-1. 3-3. 3-6. 3-8. 3-10. 3-11.	ATING INSTRUCTIONS		4-16. 4-22. 4-27. 4-28. 4-67. 4-112.	Frequency and Sweep Section
3-13.	Input Impedance			
3-16. 3-20. 3-24. 3-28. 3-32. 3-34. 3-37. 3-39. 3-41. 3-49. 3-51. 3-66. 3-72. 3-77. 3-80. 3-96. 3-103. 3-108.	Input Constraints 3-4 Grounding 3-4 Ground Loops 3-5 Measurement Configurations 3-5 Amplitude Modes 3-6 Absolute/Relative Measurements 3-6 Overload Indicator 3-7 Maximum Input Level 3-7 Sensitivity 3-8 Dynamic Range 3-8 Amplitude Measurements (Linear Mode) 3-8 Amplitude Measurements (Linear Mode) 3-8 Amplitude Measurements (Log Mode) 3-11 Internal Cal. Signal 3-12 Bandwidth Setting 3-15 Frequency Span Setting 3-15			TENANCE. 5-1 Introduction. 5-1 Recommended Test Equipment 5-1 Performance Tests 5-1 Test Card 5-1 Frequency Tests 5-1 Sweep Tests 5-1 Sweep Tests 5-3 Amplitude Tests 5-4 Bandwidth Tests 5-8 Dynamic Range Tests (Noise Tests) 5-9 IF Feedthru and Zero Beat Response Tests 5-12 Input Impedance Tests 5-12 Output Tests 5-12 Output Tests 5-13 Balanced Input Tests (Option 002 only) 5-14 Adjustment Procedure 5-16 Test Point and Adjustment Locations 5-16 Power Supply Tests and Adjustments 5-16

## TABLE OF CONTENTS (Cont'd)

Secti	on		Page	Section		Page
	5-58.	Display Adjustments	5-17	7-4.	General Troubleshooting Procedures.	7-1
	5-63.	Sweep Alignment and		7-8.	A2 Board VTO Troubleshooting	
		Dial Calibration	5-17	7-10.	A3 Board Troubleshooting	7-1
	5-68.			7-12.	A7 Board	
	5-70.	I.F. Filter Alignment		7-19.	High Voltage Power Supply	
	5-74.	Amplitude Calibration		7-25.	Crystal Replacement	
	5-81.	Mixer Balance Adjustments		7-28.	CRT Replacement	
	5-84.	Adaptive Sweep Marker Adjustment		7-30.	Battery Replacement	
		110000000000000000000000000000000000000			(Option 001 only)	7-6
Secti	on		Page	7-34.		
VI.		ACEABLE PARTS			Rotary Switches	.7-6(a)
	6-1.	Introduction.		7-37.	Factory Selected Components	
	6-4.	Ordering Information		7-40.	Schematic Diagrams	7-7
	6-6.	Non-Listed Parts				
	6-8.	Proprietary Parts		Section		Page
				VIII. BACK	DATING	8-1
Secti	on		Page	8-1.	Introduction	8-1
		JBLESHOOTING AND				
		RCUIT DIAGRAMS	7-1			
	7-1.	Introduction		APPENDICE	ES	
	7-3.	Troubleshooting and Preventive			Code List of Manufacturers	
		Maintenance	7-1		Sales and Service Offices	

### LIST OF TABLES

Table		Table		Page
1-1.	Specifications	5-4.	Log Amplitude Tests	. 56
1-2.	General Information 1-2		Linear Amplitude Tests	
	Operating Features		Amplitude Ref Level Tests (Linear Mode)	
	Maximum Input Levels	5-7.	Amplitude Ref Level Tests (Log Mode)	. 5-7
	Frequency Resolution		First Input Attenuator Test	
	Recommended Bandwidths (Log Sweep) 3-18		Second Input Attenuator Test	
	Optimum Sweep Time Settings		Frequency Response Tests	
	Control Settings (Amplitude Calibration) 3-28		300 Hz thru 10 Hz Bandwidth Tests	
	Linear Amplifier Gain 4-6	5-12.	3 Hz and 1 Hz Bandwidth Tests	. 5-9
	Video Output Circuits (Log 1 dB mode) 4-8	5-13.	Balanced Input Frequency Response Tests	.5-15
4-3.	Qualifier Inputs		Conversion Table	
4-4.	Ramp Generator Instructions		Replaceable Parts	
	Dial Mixing Amplifier Gain		Conditions for Single Stepping A3 Logic	
5-1.		7-2.	Excitation Table for J-K Flip-Flop	7-3
5-2.	Frequency Span Test 5-4		Factory Selected Components	
5-3.	Conversion Table		Assembly Cross Reference	

## LIST OF ILLUSTRATIONS

Figure	e	Page	Figure		Page
2-1.	Power Receptacles	. 2-1	4-19.	Storing A Trace	.4-17
3-1.	Front Panel	. 3-2		Displaying a Stored Trace	
3-2.	Rear Panel	. 3-3		Digital Storage And Display Sections	
3-3.	Equivalent Input Circuit	. 3-4		Digital Storage Section (Store Mode)	
3-4.	Graph Zt vs. Frequency	. 3-4		Store Function ASM Chart	
3-5.	Power Line Ground Loop	. 3-5		Equivalent Circuit (State 1)	
3-6.	Measurement Configurations	. 3-6		Equivalent Circuit (State 3)	
3-7.	Input Section	. 3-7		Store Button Released	
3-8.	Maximum Input Index	. 3-7		10 kHz CAL Signal	
3-9.	Noise vs. Frequency	. 3-8		Proper Hookup	
3-10.	Amplitude Section (Linear Mode)	. 3-9		Spurious Response Test	
3-11.	Amplitude Section (Log Mode)	.3-11		Common Mode Rejection Test	
3-12.	Cal Signal	.3-12		Symmetry Adjustment	
3-13.	Frequency Tuning	.3-12		Proper Hookup	
3-14.	IF Filter Response	.3-13		Adaptive Sweep Marker Adjustment	
3-15.	Response Of CW Signals	.3-13		Frequency Control Component Locator	
3-16.	Two Signals In Passband	.3-13		Mother Board (A1) Component	
	Large Signal Hides Small Signal			Location Diagram	.7-10
3-18.	Small Signal Resolved	.3-14	7-2.	Functional Block Diagram	
3-19.	Noise Sidebands (1 Hz BW)	.3-14		Amplitude Section Detailed	
3-20.	Zero Response (300 Hz BW)	.3-15		Block Diagram	7-14
3-21.	Amplitude vs. Time	.3-16	7-4.	Input Assembly (A9) and Balanced Input	
3-22.	Frequency Out Of Range	.3-16		Assembly (A18) Schematics and	
3-23.	Margin Below 0 Hz	.3-16		Component Location Diagrams 7-15/	/7-16
	Frequency vs. Time (Log Sweep)		7-5.	IF Filter Assembly (A5) Schematic and	
3-25.	Log Sweep (300 Hz BW)	.3-18		Component Location Diagram 7-17/	/7-18
3-26.	Log Amplitude vs. Log Freq. Plot of 20 kHz		7-6.	Detector Assembly (A4) Schematic and	
	Notch Filter (30 Hz BW)	.3-19		Component Location Diagram7-19/	/7-20
	Plot Using Conventional Sweep (15 minutes)		7-7.	Sweep Generator (A3) Schematic and	
3-28.	Plot Using Adaptive Sweep	.3-21		Component Location Diagram7-21/	/7-22
3-29.	Stored Trace and Current Trace		7-8.	Frequency Control Circuits	
	Displayed Simultaneously	.3-22		(A14, A15, A16) Schematic and	
	Store Button Released			Component Location Diagrams 7-23/	/7-24
	Tracking Oscillator		7-9.	VTO and Tracking Oscillator Assembly	
3-32.	Input Configurations (Option 002)	.3-25		(A2) Schematic and Component	
3-33.	Voltage Selection	.3-26		Location Diagram	.7-25
3-34.	Typical Turn On Display	.3-26	7-10.	Digital Storage Assembly (A7)	
	Probe Compensation			Schematic and Component Location	
	Simplified Block Diagram			Diagram	.7-27
	Typical Crystal Filter Stage		7-11.	Control Assembly (A8), Deflection Amplifier	
	Crystal Filter			(A13) and High Voltage Power Supply	
4-4.	IF Amplifier	. 4-5		(A11) Schematics and Component	17 30
4-5.	Linear Amplifier	. 4-5	<b>-</b> 10	Location Diagrams	//-30
	Log Amplifier		7-12.	Low Voltage Power Supply (A6)	
	Typical Log Amplifier Stage			Schematic and Component Location	
	Log Amplifier Input And Output Levels			Diagram, and Power Input Module	17 22
	Video Output		0.1	(A20) Schematic	17-32
4-10.	Frequency and Sweep Section	. 4-9	8-1.	Balanced-Terminated Input	0 1
4-11.	Adaptive Sweep Routine	4.10	0.2	Configuration	
	Linear Sweep Generator			Balanced Input Assembly	
	ASM Chart (Adaptive Sweep)			Control Board Circuit Change	
4-14.	Frequency Out Of Range	4-14	8-4.	Old Style Crystal	ν Ω Λ
	VTO And Tracking Oscillator (AS)			Control, Deflection, (A8, A11, A13) and	. 0-4
	Precision Monostable		0-0.	Schematics and Component Location	
	Basic Log Sweep Generator			Diagrams	7/8-8
T-10.	Overall Log Sweep Generator	4-10		Diagrailis	7 7 0-0

#### Table 1-1. Specifications.

#### **FREQUENCY**

Frequency Dial Accuracy: ± 100 Hz, 20°C to 30°C; ± 300 Hz, 0°C to 55°C

Display Accuracy: Frequency error between any two points is less than ± 2% of their indicated separation.

Bandwidths: (accuracy ± 15%)

1 Hz 3 Hz 10 Hz	30 Hz 100 Hz	300 Hz
(25°C ± 5°C)		

#### **AMPLITUDE**

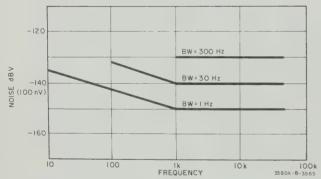
Amplitude Accuracy:	Log	Linear
Frequency Response:*		
20 Hz-20 kHz	± 0.3 dB	± 3%
5 Hz50 kHz	± 0.5 dB	± 5%
Switching Between Bandwic	iths (25°C):	
3 Hz-300 Hz	± 0.5 dB	± 5%
1 Hz-300 Hz	± 1 dB	± 10%
Amplitude Display:	± 2 dB	± 2%
Input Attenuator:	± 0.3 dB	± 3%
Amplitude Reference Level:		
(IF attenuator)		
most sensitive range	± 1 dB	± 10%
all other ranges	± 1 dB	± 3%
The state of the s		

<sup>\*</sup>Standard 3580A and Option 002 unbalanced input.

#### Dynamic Range:

Display Range (Log 10 dB mode): > 80 dB

Noise Level: "Noise level is measured with 50 ohms placed across the input terminals. On the 30 to 300 Hz bandwidth use maximum display smoothing. The noise level as a function of frequency is:" (Refer to noise vs frequency graph).



Distortion (THD and IM):

Std 3580A: > 80 dB below input reference level.

Option 002: > 80 dB below input reference level for signals below 0 dBm and above 100 Hz.

Spurious Responses: > 80 dB below input reference level Line Related Spurious: > 80 dB below input reference level or - 140 dBV (0.1  $\mu$ V)

Below - 90 dBm for Option 002 Balanced-Terminated Input.

#### IF Feedthru:

Input	Feedthru
> 10 V	- 60 dB or lower
< 10 V	- 70 dB or lower

Zero Response: > 30 dB below input reference level

Noise Sidebands (1 Hz Bandwidth): more than 70 dB below peak of CW signal ± 10 Hz away from center of response.

#### **SWEEP**

Sweep Times: 0,1 sec to 2,000 sec

Accuracy: ± 5%

Log Sweep: 20 Hz to 43 kHz

Accuracy: ± 20% after 3 continuous sweeps

#### **BALANCED INPUT (Option 002 only)**

Frequency Response  $\Delta_1$ :  $\pm$  0.5 dB, 40 Hz to 20 kHz for signals

below + 20 dBm.

Common Mode Rejection: > 70 dB at 60 Hz

#### **OUTPUTS**

#### **Recorder Outputs:**

X-Axis: 0 V to + 5 V  $\pm$  2.5% Y-Axis: 0 V to + 5 V  $\pm$  2.5%

#### **Tracking Oscillator Output:**

Frequency Response:

Std 3580A: ± 3%, 5 Hz to 50 kHz

Opt. 002:  $\pm$  0.5 dB, 100 Hz to 20 kHz, 10 kHz Reference, 600  $\Omega$  load.

Frequency Accuracy: ± 2.5 Hz relative to center of passband

L.O. Output: Frequency of output signal varies from 1.0 MHz to 1.5 MHz as analyzer frequency is tuned from 0 Hz to 50 kHz

Frequency Accuracy: The tuned frequency can be read to an accuracy of ± 5 Hz using an external counter.

Δ<sub>1</sub> Serial No. 1312A-00465 and below: Change Frequency Response Specification to ± 0.5 dB, 300 Hz to 20 kHz.

## SECTION I GENERAL INFORMATION

#### 1-1. DESCRIPTION.

- 1-2. The Hewlett-Packard Model 3580A Spectrum Analyzer is a low frequency instrument that has been optimized for use in the 5 Hz to 50 kHz range. The 3580A functions as a signal analyzer or as a network analyzer. When used as a signal analyzer, the 3580A provides a graphical display of the spectral components of an input signal. When used as a network analyzer, the 3580A plots the amplitude vs. frequency characteristics of 2-port networks such as amplifiers, attenuators and filters.
- 1-3. The major features of the 3580A include a digitally stored display, adaptive sweep, six selectable bandwidths (1 Hz 300 Hz), 30 nV sensitivity and 80 dB dynamic range. These standard features, along with optional balanced inputs and an internal rechargeable battery pack, make the 3580A ideally suited for communications, geophysical, oceanography and metrology applications.

#### 1-4. SPECIFICATIONS.

- 1-5. Table 1-1 is a complete list of the Model 3580A critical specifications that are controlled by tolerances. Table 1-2 contains general information describing the operating characteristics of the 3580A.
- 1-6. Any changes in specifications due to manufacturing, design, or traceability to the U.S. National Bureau of Standards are included in Table 1-1 in this manual. Specifications listed in this manual supersede all previous specifications for the Model 3580A.

#### 1-7. OPTIONS.

1-8. There are two options available for the 3580A. These options are listed in the following table. For further information concerning options, refer to Table 1-2 or Section III in this manual or contact the nearest -hp- Sales and Service Office.

3580A Option (Factory Installed)	Description
001*	Internal rechargeable battery pack and front panel cover for complete portability
002	Balanced inputs; balanced tracking oscillator output

<sup>\*</sup>Field Installation Kit -hp- 11195A Battery Pack only. Field Installation Kit -hp- 03580-80001 includes battery pack and front panel cover.

#### 1-9. Warranty Exceptions.

1-10. Batteries in Option 001 instruments are warranted for 90 days.

#### 1-11. ACCESSORIES SUPPLIED.

1-12. The following is a list of accessories supplied with the 3580A:

Item	Qty.	-hp- Part No.
Accessory Kit Includes the following:	1 ea.	03580-84401
PC Board Extender (15 pin)	2 ea.	5060-0049
PC Board Extender (10 pin)	2 ea.	5060-5917
Fuse: 0,25 A, 250 V Normal Blo (for 220 V/240 V operation)	1 ea.	2110-0004

#### 1-13. ACCESSORIES AVAILABLE.

1-14. The following is a list of Hewlett-Packard accessories available for use with the Model 3580A:

-hp- Model	Description
7035B Opt. 020	Voltage Divider Probe Front Panel Cover Assembly X/Y Recorder Oscilloscope Camera

### 1-15. INSTRUMENT AND MANUAL IDENTIFICATION.

- 1-16. The instrument serial number is located on the rear panel. Hewlett-Packard uses a two-section serial number consisting of a four-digit prefix and a five-digit suffix. A letter between the suffix and prefix identifies the country in which the instrument was manufactured (A = USA, G = West Germany, J = Japan, U = United Kingdom). All correspondence with Hewlett-Packard should include the complete serial number.
- 1-17. If the serial number of your instrument is lower than the one on the title page of this manual, refer to Section VIII for backdating information that will adapt this manual to your instrument.

#### Table 1-2. General Information.

Maximum

#### INPUT CHARACTERISTICS (Standard 3580A)

Connector: female banana plug
Impedance: 1 megohm, 30 pF

Maximum (ac) Input Level:

 Sensitivity
 Input

 + 30 dB (20 V) to - 10 dB (0.2 V)
 100 V rms

 - 20 dB (0.1 V) to - 70 dB (0.2 mV)
 50 V rms

Maximum (dc) Input Voltage: ± 100 V dc

Input

Coupling: capacitive

DC Isolation: none (input common referenced to frame ground)

#### INPUT CHARACTERISTICS (Option 002)

#### Selectable Input Configurations:

Unbalanced Balanced Bridged Balanced Terminated

Connector: female banana plug

#### Impedance:

Unbalanced: 1 megohm, 40 pF

Greater than 12 K (typically 14 K at 1 kHz) Terminated: 600 ohms or 900 ohms

#### Maximum Input Levels:

Unbalanced: same as Standard 3580A. Bridged: 100 V dc max, 35 V rms ac max.

Terminated: + 27 dBm at 0 V dc. (See Paragraph 3-187).

#### DC Isolation:

Unbalanced: none (input common referenced to frame

Bridged and Terminated: floating input

#### AMPLITUDE CHARACTERISTICS:

#### Amplitude Modes:

Linear: Absolute measurements in rms volts (average responding): relative measurements in percent of full scale.

Log 10 dB/div.: Absolute measurements in dBV (1 V rms = 0 dBV) or dBm/600 ohms; relative measurements in dB. Display sensitivity is 10 dB per division; display range is > 80 dB.

Log 1 dB/div.: Display sensitivity is 1 dB per division; display range is 10 dB. Any 10 dB portion of 80 dB range can be displayed by changing the AMPLITUDE REF LEVEL control setting.

#### Full-Scale Sensitivity:

#### Linear Mode:

Calibrated: 20 V rms to 0.1  $\mu$ V rms (18 ranges) Uncalibrated: 100 V rms to 0.2  $\mu$ V rms

#### Log 10 dB Mode:

Calibrated: + 30 dBV/dBm to - 70 dBV/dBm (11 ranges) Uncalibrated: + 40 dBV/dBm to - 60 dBV/dBm

Overload Indicator: An LED Overload indicator on the front panel lights to indicate that the input signal exceeds the maximum (full scale) input level set by the INPUT SENSITIVITY switch and amplitude VERNIER.

Internal Calibration Signal: An internally generated calibration signal can be used to calibrate the amplitude section (following input attenuator) to an accuracy of ± 1.5% at 10 kHz. The calibration signal can also be used to verify the frequency accuracy of the instrument.

#### FREQUENCY CHARACTERISTICS:

Frequency Range: 5 Hz to 50 kHz

Frequency Control: The front panel FREQUENCY control tunes the frequency of the analyzer over the 0 Hz to 50 kHz range. The control can be used to set either the start or center frequency of linear or manual sweeps.

Coarse or Fine Tuning: Coarse tuning is selected by pushing the crank toward the front panel; fine tuning is selected by pulling the crank outward. In the coarse position, one revolution of the crank changes the frequency by approximately 2.7 kHz. In the fine position, one revolution of the crank changes the frequency by approximately 73 Hz.

Frequency Dial: Indicates start or center frequency in kHz.

Range: 00.0 kHz to approximately 50.8 kHz.

Resolution: 20 Hz (one minor division)

Typical Frequency Stability: ± 10 Hz/hr. after 1 hour; ± 5 Hz/°C

Bandwidth Settings: 1 Hz, 3 Hz, 10 Hz, 30 Hz, 100 Hz, 300 Hz

Bandpass Characteristic: closely approximates a gaussian response.

Shape Factor: 10:1 on 1 Hz thru 100 Hz bandwidths; 8:1 on 300 Hz bandwidth

Equivalent Noise Bandwidth: Typically 12% wider than absolute 3 dB bandwidth.

#### Display Smoothing (noise filtering):

3 Settings: min, med max

Response: determined by Bandwidth setting.

#### SWEEP CHARACTERISTICS:

#### Sweep Modes:

Repetitive: The instrument sweeps continuously over the selected frequency range,

Single: The instrument sweeps one time over the selected frequency range and stops at the end frequency.

Reset: Sweep is reset to left-hand side of screen; instrument remains at start frequency of sweep.

Manual: The electronic sweep is disabled and a front panel potentiometer is used to manually sweep the frequency

#### Table 1-2. General Information (Cont'd).

and the refresh trace on the CRT. The manual sweep fully duplicates the span of the electronic sweep.

Log Zero: Used to set the correct starting point for log sweep.

Log: Front panel frequency and sweep controls are disabled. The instrument sweeps logarithmically from 20 Hz to 43 kHz. The log sweep is repetitive; sweep time is approximately 5 seconds.

Typical Sweep Linearity: ± 1%

Frequency Span Settings: 0 Hz\*, 5 Hz/div to 5 kHz/div

\*When the 0 Hz span setting is selected, the frequency sweep is disabled and the instrument remains at the frequency indicated on the frequency dial. The display continues to sweep at the panel-selected rate. This provides a graphical display of amplitude vs. time.

Overall Span: 50 Hz to 50 kHz (10 span settings)

Sweep Time Settings: 0.01 sec/div to 200 sec/div (14 settings)

Overall Sweep Time: 0.1 sec. to 2,000 sec.

Sweep Error Light: A front panel LED indicator lights when sweep rate is too fast,

Out of Range Indication: The CRT display is cleared in areas where the sweep goes below 0 Hz or above 50 kHz.

Adaptive Sweep: The front panel Adaptive Sweep control is used to set a baseline threshold on the CRT. In areas where responses are below the baseline threshold, the instrument sweeps 20 to 25 times faster than the panel-selected rate. When the sweep reaches a response that rises above the baseline threshold, it backs up slightly, pauses to allow the IF Filter to settle and then sweeps slowly over the response at the panel-selected rate. By sweeping rapidly through unused portions of the spectrum, the Adaptive Sweep greatly reduces the measurement time for certain applications.

External Triggering: A rear panel External Trigger Input connector is provided to allow the frequency sweep to be remotely triggered by a contact closure or TTL logic levels. External triggering can be used in the Repetitive, Single or Log sweep mode.

#### **OUTPUTS**:

#### **Recorder Outputs:**

X-Axis: Supplies dc voltage corresponding to position of frequency sweep on CRT.

Output Voltage: 0 V (left-hand edge) to +5 V (right-hand edge)

Output Resistance: 1 kilohm

Y-Axis: Supplies dc voltage proportional to amplitude.
Output Voltage: 0 V (bottom of screen) to + 5 V (top

of screen).

Output Resistance: 1 kilohm

Pen Lift: Provides a contact closure during single sweeps. If Adaptive Sweep is used, closure is present only when instrument is sweeping slowly over a response.

#### **Tracking Oscillator Output:**

Frequency: 5 Hz to 50 kHz; tracks tuned or swept frequency of instrument.

Output Level: 0 V to > 1 V rms into 600  $\Omega$  (adjustable)

Output Impedance: 600 ohms

Tracking Oscillator Input: The tracking oscillator output signal can be offset or frequency modulated by applying an external reference signal (about 100 kHz) to the rear panel Tracking Oscillator Input connector.

#### L.O. Output:

Frequency: Varies from 1.0 MHz to 1.5 MHz as 3580A frequency is tuned from 0 Hz to 50 kHz.

Output Level: Varies from about 300 mV p-p to 600 mV p-p depending on frequency.

Output Impedance: 1 kilohm

#### **GENERAL:**

#### **Operating Temperature Range:**

Standard 3580A: 0°C to + 55°C

Option 001:  $0^{\circ}$ C to  $+40^{\circ}$ C

#### Storage Temperature Range:

Standard 3580A:  $-40^{\circ}$ C to  $+75^{\circ}$ C

Option 001:  $-40^{\circ}$ C to  $+50^{\circ}$ C

Charge Temperature Range (Option 001): 0°C to + 40°C

Power Requirements: 100 V, 120 V, 220 V or 240 V + 5% - 10%, 48 Hz to 66 Hz, 35 watts maximum

#### Battery Characteristics (Option 001):

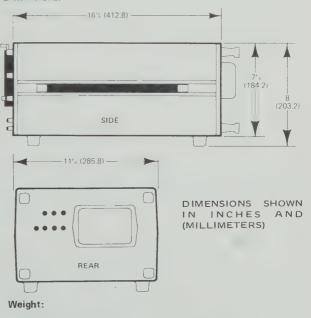
Operating Time: 5 hours from full charge

Charge Time: 14 hours to recharge fully discharged battery pack

Battery Life: more than 100 charge/discharge cycles

Protection: The batteries are protected from excessive discharge by an automatic cut out.

#### Dimensions:



Standard 3580A: Net 27 lbs.

Option 001: Net 35 lbs.



## SECTION II INSTALLATION

#### 2-1. INTRODUCTION.

2-2. This section contains information and instructions necessary for installing and shipping the Model 3580A Spectrum Analyzer. Included are initial inspection procedures, power and grounding requirements, environmental information, installation instructions and instructions for repackaging for shipment.

#### 2-3. INITIAL INSPECTION.

2-4. This instrument was carefully inspected both mechanically and electrically before shipment. It should be free of mars or scratches and in perfect electrical order upon receipt. To confirm this, the instrument should be inspected for physical damage incurred in transit. If the instrument was damaged in transit, file a claim with the carrier. Check for supplied accessories (Paragraph 1-11) and test the electrical performance of the instrument using the performance test procedures outlined in Section V. If there is damage or deficiency, see the warranty in the front of this manual.

#### 2-5. POWER REQUIREMENTS.

2-6. The Model 3580A can be operated from any power source supplying 100 V, 120 V, 220 V or 240 V (+ 5% -10%), 48 Hz to 440 Hz. Power dissipation is 35 watts, maximum. Refer to Paragraph 3-192 (Section III) for the Instrument Turn On procedure.

#### 2-7. Power Cords And Receptacles.

2-8. Figure 2-1 illustrates the standard power receptacle (wall outlet) configurations that are used throughout the United States and in other countries. The -hp- part number shown directly below each receptacle drawing is the part number for a 3580A power cord equipped with the appropriate mating plug for that receptacle. If the appropriate power cord is not included with the instrument, notify the nearest -hp- Sales and Service Office and a replacement cord will be provided.

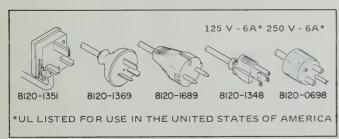


Figure 2-1. Power Receptacles.

#### 2-9. GROUNDING REQUIREMENTS.

2-10. To protect operating personnel, the National Electrical Manufacturer's Association (NEMA) recommends that the instrument panel and cabinet be grounded. The Model 3580A is equipped with a three conductor power cable which, when plugged into an appropriate receptacle, grounds the instrument. The offset pin on the power plug is the ground connection.

2-11. For battery powered instruments (Option 001), the common binding post of the INPUT connector (Case Ground  $\Rightarrow$ ) should be connected to earth ground or to an appropriate system ground. If a system ground is used, extra care should be taken to ensure that it is actually at ground potential and is not a voltage source.

#### 2-12. ENVIRONMENTAL REQUIREMENTS.

2-13. Operating and Storage Temperature (Standard 3580A).

Operating Temperature Range: 0°C to + 55°C Storage Temperature Range: - 40°C to + 75°C

### 2-14. Operating and Storage Temperature (Option 001).

Operating Temperature Range: 0°C to + 40°C

Storage Temperature Range: - 40° C to + 50° C

Charge Temperature Range: 0°C to +40°C

#### 2-15. INSTALLATION.

2-16. The Model 3580A is a portable instrument and does not require installation. The instrument is shipped with rubber feet and tilt stand in place, ready for use as a bench instrument.

#### 2-17. REPACKAGING FOR SHIPMENT.

2-18. The following paragraphs contain a general guide for repackaging the instrument for shipment. Refer to Paragraph 2-21 if the original container is to be used; 2-22 if it is not. If you have any questions, contact the nearest -hp-Sales and Service Office (See Appendix B for office locations).

#### NOTE

If the instrument is to be shipped to Hewlett-Packard for service, or repair, attach a tag to the instrument identifying the owner and indicating the service or repair to be accomplished. Include the model number and full serial number of the instrument. In any correspondence, identify the instrument by model number and full serial number.

2-19. Place instrument in original container with appropriate packing material and seal well with strong tape or metal bands. If original container is not available, one can be purchased from your nearest -hp- Sales and Service Office.

- 2-20. If original container is not to be used, proceed as follows:
- a. Wrap instrument in heavy paper, or plastic before placing in an inner container.
- b. Place packing material around all sides of instrument and protect panel face with cardboard strips.
- c. Place instrument and inner container in a heavy carton or wooden box and seal with strong tape or metal bands.
- d. Mark shipping container "DELICATE INSTRU-MENT," FRAGILE," etc.
- 2-21. Option: Option 910 is an additional Operating and Service Manual -hp- Part Number 03580-90002.

## SECTION III OPERATING INSTRUCTIONS

#### 3-1. INTRODUCTION.

3-2. This section contains complete operating instructions for the Model 3580A Spectrum Analyzer. Included is a brief description of the instrument, a description of controls, general operating information and basic operating procedures.

#### 3-3. ABOUT THE SPECTRUM ANALYZER.

3-4. The first spectrum analyzers were introduced during World War II for use in the development of pulse radar systems. Early spectrum analyzers were difficult to operate and interpret since they lacked such refinements as calibrated controls. They were, however, adequate tools which enabled scientists to observe the spectra of radar pulses and subsequently optimize the gain and bandwidth of radar receivers. Since that time, spectrum analyzers have evolved into general purpose instruments with unlimited applications in the RF and audio frequency ranges.

3-5. The 3580A is a low frequency spectrum analyzer designed specifically for use in the audio frequency range. It can be used as a signal analyzer or as a network analyzer. When used as a signal analyzer, the 3580A measures the amplitudes and frequencies of the spectral components of an input signal. When used as a network analyzer, the 3580A plots the amplitude vs. frequency characteristics of 2-port networks such as amplifiers, attenuators and filters.

#### 3-6. Operating Features.

3-7. The 3580A has many unique operating features that make it versatile, easy to use and ideally suited for low-frequency work. The three most significant features are its digitally stored display, Adaptive Sweep and 1 Hz bandwidth. Details of these and other features outlined in Table 3-1 are given in the General Operating section (Paragraph 3-10).

#### 3-8. CONTROLS, CONNECTORS AND INDICATORS.

3-9. Figures 3-1 and 3-2 illustrate and describe the function of all front and rear panel controls, connectors and indicators. The description of each item is keyed to the drawing within the figure.

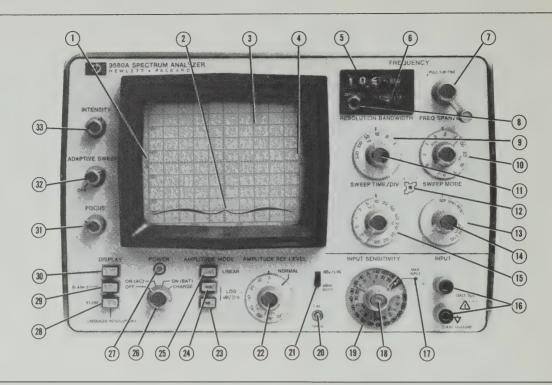
#### 3-10. GENERAL OPERATING INFORMATION.

#### 3-11. Input Cable Requirements.

3-12. The input signal can be applied to the 3580A through a twisted pair, a shielded cable equipped with banana-plug connectors (-hp- 11000A Cable Assy.) or a 10:1 Voltage Divider Probe (-hp- 10004B). Input leads should be kept as short as possible to minimize extraneous pickup. When using a 10:1 Voltage Divider Probe, the probe must be compensated as outlined in Paragraph 3-203.

Table 3-1. Operating Features.

FEATURE	PARAGRAPH	FEATURE	PARAGRAPH
High Input Impedance: 1 M $\Omega$ , 30 pF Frequency Range: 5 Hz to 50 kHz	3-13	<ol> <li>Log 10 dB: scale 10 dB/div; absolute measurements in dBV or dBm/600 ohms; relative measurements in dB; 80 dB dynamic range</li> </ol>	3-66
Six Selectable Bandwidths: 1 Hz – 300 Hz	3-80	3. Log 1 dB: scale 1 dB/div; 10 dB display range  Measurement Range:	
Calibrated Frequency Dial:	3-96	Weasurement Hange.	
Selects start or center frequency of sweep		1. Calibrated: 0.1 $\mu$ V rms (-140 dBV/dBm) full-scale to 20 V rms (+ 30 dBV/dBm) full-scale	
2. Coarse or fine tuning		2. Uncalibrated: 0.1 $\mu V$ rms (- 140 dBV/dBm) full-scale to 100 V rms (+ 40 dBV/dBm) full-scale.	
Eleven Frequency Span Settings: 0 Hz, 50 Hz — 50 kHz	3-103	80 dB Dynamic Range	3.49
Sweep Modes:	3-113	Digitally Stored Display	3-158
1. Single or repetitive linear sweep		Internal Calibration Signal Recorder Outputs:	3-77
2. Manual Sweep		1. X-AXIS	3-165
3. Log sweep		2. Y-AXIS	3-168
Fourteen Sweep Time Settings: 0.1 sec - 2,000 sec.	3-133		
Optimum Sweep Rate Indicator	3-137	3. PEN LIFT	3-170
	3-108	Tracking Oscillator Output	3-171
Frequency Out-Of-Range Indication On CRT	3-108	Tracking Oscillator Input	3-175
Adaptive Sweep	3-147	L.O. Output	3-178
Three Amplitude Modes:	3-32		
Linear: absolute measurements in rms volts; relative measure-	3-51	Portability, Battery Operation (Option 001)	3-182
ments in percent of full-scale.		Balanced Inputs, Balanced Tracking Oscillator Output (Option 002)	3-187

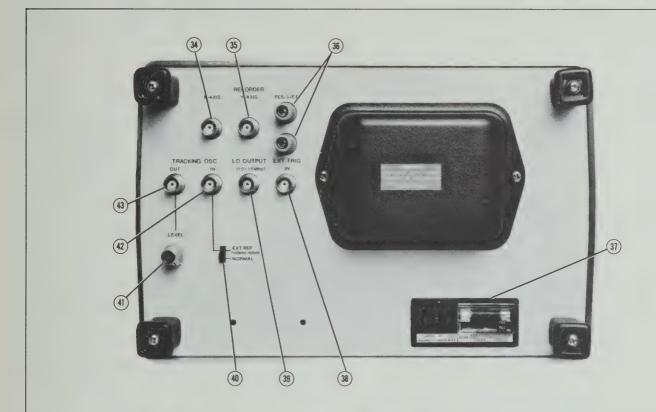


- LOG Markings: In the LOG 10 dB mode, these markings indicate signal amplitude in dB below full scale.
- Frequency Markings: These markings indicate 20 Hz, 200 Hz, 2 kHz and 20 kHz decade frequencies of log sweep. (Paragraph 3-125)
- (3) CRT Display: (Paragraph 3-158)
- LIN Markings: In the LIN mode, these markings indicate signal amplitude in percent of full scale (1.0 = 100%, 0.4 = 40%, etc.).
- FREQUENCY Dial: Indicates start or center frequency of linear or manual sweep. (Paragraph 3-99)
- 6 START/CTR Switch: When set to START position, FRE-QUENCY dial indicates start frequency of linear or manual sweep; when set to CTR position, FREQUENCY dial indicates center frequency of linear or manual sweeps. (Paragraph 3-100)
- FREQUENCY Control: Tunes frequency of instrument over 0 Hz to 50 Hz range. Is used to set start or center frequency of linear or manual sweeps, Push in for coarse tuning; pull out for fine tuning, (Paragraph 3-96)
- (8) ZERO CAL Potentiometer: Used to calibrate FREQUENCY dial for linear or manual sweeps and to set the correct starting point for log sweep. (Paragraph 3-102)
- BANDWIDTH Control: Controls 3 dB bandwidth of IF Filter. Is used to select the desired frequency resolution. The six BANDWIDTH settings are: 300 Hz, 100 Hz, 30 Hz, 10 Hz, 3 Hz and 1 Hz. (Paragraph 3-80)
- FREQ SPAN Control: Determines width of spectrum to be observed. Span settings range from 5 Hz per division (50 Hz) to 5 kHz per division (50 kHz). (Paragraph 3-103)
- $\begin{tabular}{ll} \hline \textbf{(I)} & \textbf{DISPLAY SMOOTHING Switch:} & \textbf{Provides three levels of noise} \\ \hline \textbf{filtering for video presentation.} \\ \hline \end{tabular}$
- (12) ADJUST Indicator: Lights to indicate that sweep rate is too fast. Will go out when SWEEP TIME is increased, BAND-WIDTH is widened or when FREQUENCY SPAN is narrowed. (Paragraph 3-137)
- (3) SWEEP MODE Switch: Permits selection of six sweep modes: REP (Repetitive), SING (Single), RESET, MAN (Manual), LOG ZERO and LOG. (Paragraph 3-113)
- MANUAL VERNIER: Tunes analyzer frequency and positions horizontal trace when SWEEP MODE switch is set to MAN position. (Paragraph 3-121)
- (5) SWEEP TIME Control: Sets duration of single and repetitive sweeps. Settings range from 0.01 second per division (0.1 sec.) to 200 seconds per division (2,000 sec.). (Paragraph 3-133)
- (6) INPUT Connector: Accepts male, banana-plug connector; input impedance is 1 megohm, 30 pF.(Paragraph 3-13)

- OVERLOAD Indicator: Lights to indicate that input signal exceeds maximum input level set by INPUT SENSITIVITY and amplitude VERNIER controls. (Paragraph 3-37)
- Amplitude VERNIER: For absolute measurements VERNIER must be set to CAL (fully CW) position. For relative measurements, VERNIER adjusts gain of analyzer to establish a full-scale reference. As the VERNIER is rotated counterclockwise, the gain decreases and the full-scale input level increases. (Paragraph 3-36, 3-39)
- (9) INPUT SENSITIVITY Switch: Selects maximum (full scale) input level and measurement range. For absolute measurements, full-scale settings range from + 30 dBV/dBm to -70 dBV/dBm in Log 10 dB mode or from 20 V rms to 0.2 mV rms in the Linear mode, In the Linear mode, seven additional ranges (0.1 mV to 0.1 μV) can be selected by the AMPLITUDE REF LEVEL switch (Paragraph 3-39, 3-53 and 3-68). With the switch in the CAL position, the INPUT terminals are disconnected and an internally generated calibration signal is applied to the input circuits (Paragraph 3-77).
- (20) CAL 10 kHz Potentiometer: Adjusts gain of amplitude circuits to compensate for slight variations in amplitude accuracy caused by temperature changes or changes in bandwidth (Paragraph 3-199).
- dBV/LIN dBm Switch: Set to dBV/LIN position for measurements in dBV or rms volts; set to dBm 600 OHM position for measurements in dBm 600 ohms. For measurements in dBm/600 ohms, an external termination is required.
- AMPLITUDE REF LEVEL Switch: Operates in conjunction with INPUT SENSITIVITY switch to establish full-scale sensitivity and measurement range. In Linear mode it controls the IF attenuation. When rotated in a clockwise direction, full-scale sensitivity increases in a 20 V, 10 V, 2 V, 1 V sequence (Paragraph 3-55). In the Log 10 dB mode, changing the Amplitude Ref Level setting offsets the entire display in 10 dB increments (Paragraph 3-69). In Log 1 dB mode, the Amplitude Ref Level control offsets the display to select any 10 dB portion of the 80 dB range (Paragraph 3-71).
- LOG 1 dB Button: (push to set; push LIN or LOG 10 dB to release) Selects Log 1 dB amplitude mode. Display sensitivity is 1 dB per division; display range is 10 dB. Any 10 dB portion of the 80 dB range can be displayed by changing the AMPLITUDE REF LEVEL setting. (Paragraph 3-71)
- LOG 10 dB Button: (push to set; push LIN or LOG 1 dB to release) Selects Log 10 dB amplitude mode for absolute measurements in dBV or dBm/600 ohms or relative measurements in dB. Display sensitivity is 10 dB per division; display range is 80 dB. (Paragraph 3-66)

- (25) LINEAR Button: (push to set; push LOG 1 dB or LOG 10 dB to release) Selects Linear amplitude mode for absolute measurements in rms volts or relative measurements in percent of full scale. (Paragraph 3-51)
- (26) POWER Switch: Applies line voltage to instrument when set to ON (AC) position; applies battery power to Option 001 instruments when set to ON (BAT) position; applies line voltage to Option 001 instruments to recharge batteries when set to CHARGE position. (Paragraph 3-192)
- (1) POWER Light: Lights when POWER switch is set to ON (AC), ON (BAT) or CHARGE.
- STORE Button: (push to set; push to release) When initially pressed, trace currently being displayed is permanently stored in memory. When released, permanently stored trace is cleared from memory. (Paragraph 3-160)
- BLANK STORE Button: (push to set; push to release) When pressed, permanently stored trace is blanked from the display. When released, stored trace returns to display. (Paragraph 3-160)
- 30 CLEAR WRITE Button: (momentary pushbutton) Clears display and resets sweep.
- (31) FOCUS Control: Focuses CRT trace. (Paragraph 3-158)
- ADAPTIVE SWEEP Control: Turns Adaptive Sweep on or off; is used to set baseline threshold on CRT display. (Paragraph 3-147)
- 33 INTENSITY Control: Adjusts brightness of CRT trace. Intensity can be set to any level without danger of burning the CRT face. (Paragraph 3-158)

Figure 3-1. Front Panel (Cont'd).



- X-AXIS Output: Female BNC connector supplies dc voltage corresponding to position of frequency sweep on CRT. Output voltage ranges from 0 V (left-hand edge) to +5 V (right-hand edge). Output resistance is 1 kilohm, nominal. (Paragraph 3-165)
- Y-AXIS Output: Female BNC connector supplies dc voltage proportional to amplitude. Output voltage ranges from 0 V (bottom of screen) to +5 V (top of screen). Output resistance is 1 kilohm, nominal. (Paragraph 3-168)
- (36) PEN LIFT Output: A contact closure is present across these terminals during single sweeps. If Adaptive Sweep is used, the closure is present only when the instrument is sweeping slowly over a response. (Paragraph 3-170)
- Power Input Module: Accepts power cord supplied with instrument. Contains line fuse and PC board for selecting line voltage. (Paragraph 3-193)
- (38) EXT TRIG IN Connector: Female BNC connector accepts contact closure or TTL logic levels to remotely trigger the frequency sweep. (Paragraph 3-143)

- (39) L.O. OUTPUT: Female BNC connector supplies a 100 mV rms signal whose frequency varies from 1 MHz to 1.5 MHz as the analyzer frequency is tuned from 0 Hz to 50 kHz. Output impedance is approximately 1 kilohm. (Paragraph 3-178)
- EXT REF/NORMAL Switch: In the NORMAL position, the tracking oscillator receives its reference from an internal 100 kHz crystal oscillator. In the EXT REF position, the tracking oscillator reference is an external signal applied to the TRACKING OSC IN connector. With the switch in the EXT REF position, the tracking oscillator will be inoperative unless an external reference signal is applied. (Paragraph 3-176)
- (1) LEVEL Control: Sets the amplitude of the Tracking Oscillator Output signal (0 V to 2 V rms).
- TRACKING OSC IN: Female BNC connector. An external reference signal can be applied to this connector to offset or frequency-modulate the Tracking Oscillator Output signal. (Paragraph 3-175)
- TRACKING OSC OUT: Female BNC connector supplies 0 Hz to 50 kHz signal that tracks the tuned or swept frequency of the instrument. Output level can be adjusted from 0 V to 2 V rms using the rear panel LEVEL control. Output impedance is 600 ohms, nominal. (Paragraph 3-171)

Figure 3-2. Rear Panel.

#### 3-13. Input Impedance.

3-14. The input impedance of the 3580A is 1 megohm shunted by 30 pF (28 pF nominal). This high input impedance has a minimum loading effect on the input signal and further permits the use of a 10 megohm, 10 pF Voltage Divider Probe (-hp-10004B).

3-15. Figure 3-3 shows the equivalent circuit for the 3580A Input. The resistor,  $R_{in}$ , represents the 1 megohm input resistance and the capacitor,  $C_{s}$ , represents the 28 pF shunt capacitance. Figure 3-4 shows the input impedance,  $Z_{t}$ , as a function of frequency. At low frequencies the reactance of  $C_{s}$  is very high, making  $Z_{t}$  nearly equal to  $R_{in}.$  As frequency increases, the decreasing reactance of  $C_{s}$  becomes more and more significant, causing  $Z_{t}$  to decrease. At 50 kHz,  $Z_{t}$  is approximately 100 kilohms.

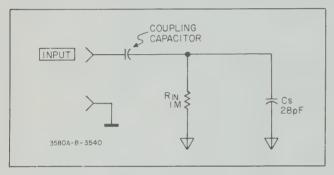


Figure 3-3. Equivalent Input Circuit.

#### 3-16. Input Constraints.

3-17. The maximum ac voltage that can be safely applied to the 3580A INPUT is determined by the INPUT SENSITIVITY switch setting (Paragraph 3-39). Maximum input levels are listed in Table 3-2. The 3580A input circuits are well protected and can withstand momentary (<5 second) overloads up to 100 V rms on all input ranges. The instrument can withstand continuous overloads up to 100 V rms on the +30 dB through -10 dB ranges and overloads up to 50 V rms on the -20 dB through -70 dB

ranges. Overloads greater than this may damage the instrument.



3580A STD Input Levels exceeding 100 V rms on the +30 dB through -10 dB ranges, 50 V rms on the -20 dB through -70 dB ranges or  $\pm$  100 V dc may damage the instrument. See Paragraph 3-187 for option 002.

3-18. DC Isolation. The STD 3580A INPUT is capacitively coupled to provide dc isolation. The maximum dc voltage that can be safely applied to the INPUT is ± 100 V dc. Exceeding this limit can cause breakdown of the input capacitor resulting in damage to the input amplifier circuitry.

3-19. The 3580A cannot be operated in a floating condition. All input and output commons are connected directly to outer-chassis (frame) ground which connects to earth ground through the offset pin of the power cord connector or the common side of the INPUT connector. The 3580A option 002, when operated in the unbalanced mode, has the same input restrictions as the 3580A standard. However, when the 3580A option 002 is used in the bridged mode or the terminated mode, there is no input connection to chassis ground.

#### 3-20. Grounding.

3-21. To protect operating personnel, the 3580A chassis must be grounded. The 3580A is equipped with a three conductor power cable which, when plugged into an appropriate receptacle, grounds the instrument. The offset pin on the power plug is the ground connection.

3-22. To preserve the protection feature when operating the instrument from a two contact outlet, use a three-prong to two-prong adapter and connect the lead on the adapter to earth ground.

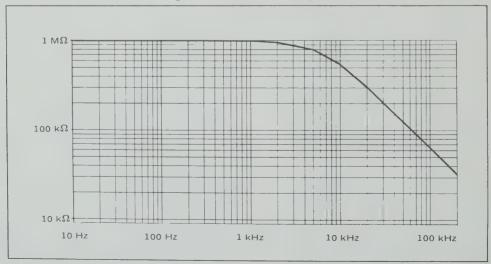


Figure 3-4. Graph Z<sub>t</sub> vs. Frequency.

3-23. For battery powered instruments (Option 001), the common binding post of the INPUT connector (Case Ground ) should be connected to earth ground or to an appropriate system ground. If a system ground is used, extra care should be taken to ensure that it is actually at ground potential and is not a voltage source.

#### 3-24. Ground Loops.

3-25. In the design of the 3580A, extra care has been taken to control internal ground currents that could produce undesirable responses or degrade the accuracy of low level measurements. Due to its wide dynamic range and high sensitivity, however, the 3580A can be affected by external ground currents or "ground loops" which are normally caused by poor grounding. The following paragraphs briefly describe the common power-line ground loop and outline the steps that can be taken to minimize ground loop problems.

3-26. Figure 3-5A shows the input arrangement for a simple grounded measurement. Ein represents the source being measured along with any noise associated with it and is generally called the "normal-mode source". Rs represents the source resistance and the resistance of the high lead; R<sub>a</sub> represents the resistance of the ground lead. Current from Ein (normal-mode current) flows through Rs, Z1 and Rq and the instrument responds to the drop across Z<sub>1</sub>. As long as the grounds on both sides of Rq are identical, extraneous currents cannot circulate between the source ground and the instrument ground. If, however, the grounds are different due to voltage drops in the ground lead or currents induced into it, a new source is developed and the measurement appears as shown in Figure 3-5B. The new source, E<sub>cm</sub> (the difference between grounds), is called the "common-mode source" because it is common to both the high and ground lines. Common-mode current can flow through  $R_g$  or through  $R_s$  and  $Z_l$ . Since  $Z_l$  is usually much larger than  $R_s$  and since they are both in parallel with  $R_g$ , most of the voltage across  $R_g$  will appear across  $Z_l$  causing an error in the amplitude reading.

- 3-27. To minimize power-line ground loops, the following guidelines should be observed:
  - a. Keep input leads as short as possible.
  - b. Provide good ground connections to minimize Rq.
- c. Connect the signal source and the 3580A to the same power bus.
- d. If a removable ground strap is provided on the signal source, float the source to break the common-mode current path.
- e. Option 001: Battery operate the 3580A; connect a separate ground lead between the common terminal of the 3580A INPUT connector and the ground terminal of the signal source.

#### 3-28. Measurement Configurations.

3-29. The 3580A can be used in either of two measurement configurations: open loop or closed loop. These configurations are illustrated in Figure 3-6.

**3-30. Open Loop.** In the open-loop configuration, the 3580A functions as a *signal analyzer* which divides the input signal into its various frequency components. The amplitudes of these components are displayed as a function of frequency on the CRT. The amplitude vs. frequency display shows how energy is distributed as a function of

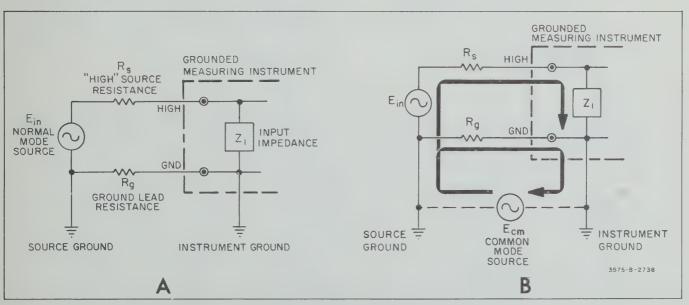


Figure 3-5. Power Line Ground Loop.

frequency and, in effect, is the Fourier spectrum of the input signal. Some of the more common measurements that can be made using the open-loop configuration include harmonic distortion, intermodulation distortion, spurious, square-wave symmetry and noise.

3-31. Closed Loop. In the closed-loop configuration, the 3580A functions as a network analyzer for characterizing two-port devices such as amplifiers, attenuators and filters. For closed-loop measurements the network to be tested is inserted between the rear panel TRACKING OSC OUT and the front panel INPUT. the tracking oscillator supplies the stimulus to the network and the 3580A measures the response. As the frequency is swept over the band of interest, the instrument responds to the amplitude variations introduced by the network. The resulting display is an amplitude vs. frequency plot of the network.

#### 3-32. Amplitude Modes.

- POSHBURTON 3-33. The front panel AMPLITUDE MODE switch permits selection of three amplitude modes: Linear (LIN), Log 10 dB and Log 1 dB. When the Linear mode is selected and the amplitude VERNIER is in the CAL position, the vertical axis of the display is calibrated in rms volts (average responding). The bottom line of the display graticule represents 0 volts while the top line represents the full scale input voltage determined by the INPUT SENSITIVITY and AMPLITUDE REF LEVEL control settings (Paragraph 3-53). When either of the Log modes is selected, the vertical axis of the display is calibrated in dBV (1 V rms = 0 dBV) or dBm/600 ohms, depending on the position of the dBv/LIN - dBm slide switch. In the Log 10 dB mode, the vertical scale is 10 dB per division and the maximum display range is greater than 80 dB (Paragraph 3-67). In the Log 1 dB mode, the vertical scale is expanded to 1 dB per division with a maximum display range of 10 dB. Any

10 dB portion of the 80 dB display range can be displayed by changing the AMPLITUDE REF LEVEL setting (Paragraph 3-71).

#### 3-34. Absolute/Relative Measurements.

3-35. Absolute Measurements. Absolute measurements reveal the actual amplitude of responses appearing on the CRT display. The 3580A can be calibrated for absolute measurements in rms volts, dBV (1 V rms = 0 dBV) or dBm/600 ohms. For absolute measurements with the 3580A, the front panel amplitude VERNIER control must be set to the CAL (fully clockwise) position and the instrument must be calibrated as outlined in Paragraph 3-199.

3-36. Relative Measurements. In signal analysis, relative measurements are used for comparing the amplitudes of two or more frequency components of a signal. In network analysis, relative measurements are used to compare the amplitude variations of a response curve at two or more frequencies. Relative measurements do not require a calibrated scale. That is, using the amplitude VERNIER and other amplitude controls, the gain of the analyzer can be adjusted so that any input level within the range of 100 V rms to 0.1 µV rms will produce full scale deflection on the CRT display. This arbitrary full scale input level then serves as a reference for measuring signals that are lower in amplitude. In the Linear mode with the VERNIER not in the CAL position, the vertical scale on the CRT is no longer calibrated in volts per division. Thus, the unit of measure becomes "percent of full scale" where the reference is 100% and one vertical division is 10%. In the Log modes the vertical scale is always 10 dB per division or 1 dB per division even though the full scale reference is arbitrary. For relative measurements in the Log 10 dB mode, the top line of the display graticule (full scale) represents 0 dB and signals are measured in dB below the 0 dB reference level.

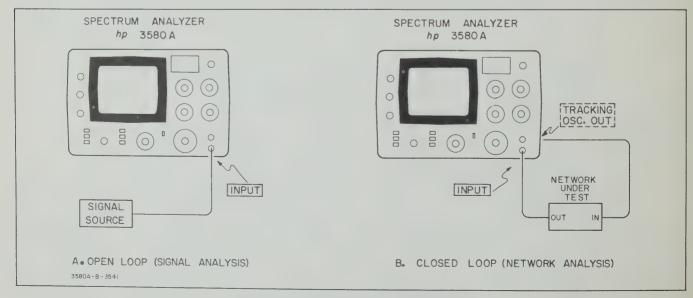


Figure 3-6. Measurement Configurations.

#### 3-37. Overload Indicator.

3-38. Figure 3-7 is a simplified block diagram showing the 3580A Input Section. The INPUT SENSITIVITY switch and its associated VERNIER potentiometer control the input attenuation and gain of the Input Circuits to maintain the proper signal level at the input of the Mixer. This is an important function since signals that overdrive the Mixer can produce harmonic and spurious mixing products which ultimately appear on the display. The Overload Detector at the input of the Mixer senses when the signal level exceeds the design limits and, in turn, lights the front panel OVERLOAD indicator. As indicated in Paragraph 3-17, the 3580A Input Circuits are well protected and continuous overloads up to 100 V rms on the + 30 dB through - 10 dB ranges or up to 50 V rms on the - 20 dB through - 70 dB ranges will not damage the instrument. In most cases, an OVERLOAD indication simply means that the input signal is overdriving the Mixer and unwanted responses may appear on the display. Generally, any time the OVER-LOAD light is off instrument-induced distortion and spurious is more than 80 dB below the input reference level.

#### 3-39. Maximum Input Level.

3-40. The maximum input level is the maximum level that can be applied to the INPUT without overloading the instrument. The maximum input level is determined only by the INPUT SENSITIVITY and amplitude VERNIER settings and is not affected by the AMPLITUDE REF LEVEL setting. With the amplitude VERNIER control in the CAL (fully CW) position, the maximum input level is indicated by a black panel index adjoining the INPUT SENSITIVITY switch dial and the OVERLOAD indicator (Figure 3-8). In both Linear and Log modes, the maximum input level is determined by the black (dB) markings on the INPUT SENSITIVITY switch dial. These markings represent either dBV or dBm/600 ohms, depending on the position of the dBv/LIN-dBm slide switch. When the amplitude VERNIER control is rotated counterclockwise

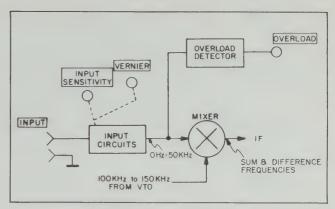


Figure 3-7. Input Section.

away from the CAL position, the gain of the input circuit decreases, the maximum input level *increases* and the markings on the INPUT SENSITIVITY switch dial no longer apply. Table 3-2 lists the maximum input levels for each INPUT SENSITIVITY setting with the amplitude VERNIER in the CAL and fully counterclockwise positions. The maximum levels listed in the table are, in some cases, considerably lower than the absolute maximum levels that will produce an OVERLOAD indication. Observing these maximum levels will ensure optimum performance on all ranges.

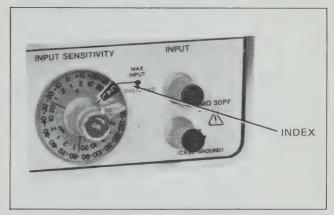


Figure 3-8. Maximum Input Index.

Table 3-2. Maximum Input Levels.

INPUT SENSITIVITY SETTING	(VERNIER in CAL) LINEAR MODE   LOG MODE		(VERNIER fully CCW) LINEAR MODE LOG MODE		POTENTIAL DAMAGE LEVEL (Continuous Overload)
+ 30 dB/20 V	31.6 V	+ 30 dBV/dBm	100 V*	+ 40 dBV/dBm	100 V*
+ 20 dB/10 V	10 V	+ 20 dBV/dBm		+ 30 dBV/dBm	
+ 10 dB/2 V	3.16 V	+ 10 dBV/dBm	10 V	+ 20 dBV/dBm	
0 dB/1 V	1 V	0 dBV/dBm	2 V	±10 dBV/dBm	V
- 10 dB/0.2 V	0.32 V	- 10 dBV/dBm	1 V	0 dBV/dBm	100 V
- 20 dB/0.1 V	0.1 V	- 20 dBV/dBm	0.2 V	- 10 dBV/dBm	50 V
- 30 dB/20 mV	32 mV	- 30 dBV/dBm	0.1 V	- 20 dBV/dBm	
- 40 dB/10 mV	10 mV	- 40 dBV/dBm	20 mV	- 30 dBV/dBm	
- 50 dB/2 mV	3,2 mV	- 50 dBV/dBm	10 mV	- 40 dBV/dBm	1
- 60 dB/1 mV	1 mV	- 60 dBV/dBm	2 mV	- 50 dBV/dBm	V
- 70 dB/0,2 mV	0.32 mV	- 70 dBV/dBm	1 mV	- 60 dBV/dBm	50 V

<sup>\*</sup>Absolute maximum input voltage.

#### 3-41. Sensitivity.

- 3-42. Sensitivity is a figure of merit that defines the analyzer's ability to detect or respond to a given input level. There are three types of sensitivity that are of interest when operating the 3580A:
  - a. Maximum Sensitivity
  - b. Full Scale Sensitivity
  - c. Display Sensitivity
- **3-43.** Maximum Sensitivity. Maximum Sensitivity refers to the smallest signal that can be detected by the analyzer. The maximum sensitivity of the analyzer is limited by its own internally generated noise and is commonly defined as the point where the signal level is equal to the noise level. This is sometimes called "tangential sensitivity".
- 3-44. Nyquist's Noise Equation<sup>1</sup> reveals two important things about noise that apply to the 3580A:
- a. Noise is proportional to the square root of bandwidth... Noise level decreases and sensitivity increases as the BANDWIDTH setting is narrowed.
- b. Noise is proportional to the square root of input resistance... The 3580A has a high (1 Megohm) input resistance. This means that noise is largely dependent on the source resistance placed at the INPUT terminals. Signal sources having low output resistances will produce a lower noise level than those having high output resistances.
- 3-45. Noise level is also dependent on the tuned frequency of the instrument. Semiconductors in the input stages of the instrument exhibit surface noise which has a 1/f frequency spectrum. This surface noise is predominate at frequencies below 1 kHz. When the 3580A is tuned below 1 kHz, the noise level increases and sensitivity decreases.
- 3-46. Figure 3-9 is a family of curves showing the specified noise levels vs. frequency for the 300 Hz, 30 Hz and 1 Hz BANDWIDTH settings. Typically, if the source resistance is less than 10 kilohms, the noise levels will be below those indicated by the curves.
- **3-47. Full Scale Sensitivity.** Full scale sensitivity defines the input level that will produce full scale deflection on any given range. For absolute measurements, full scale sensitivity ranges from 20 V rms to 0.1  $\mu$ V rms in the Linear mode and from + 30 dBV/dBm to 140 dBV/dBm in the Log (10 dB) mode. With the amplitude VERNIER control set fully counterclockwise, full scale sensitivity ranges from approximately 100 V rms to 0.2  $\mu$ V rms in the Linear mode and from + 40 dBV/dBm to 130 dBV/dBm in the Log mode.
- 3-48. Display Sensitivity. Display Sensitivity or "scale calibration" expresses the analyzer's response in units per

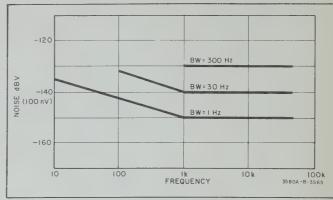


Figure 3-9. Noise vs. Frequency.

vertical division. For absolute measurements in the Linear mode, display sensitivity ranges from 2 V per division to 10 nV per division. For absolute or relative logarithmic measurements, display sensitivity is 10 dB per division in the Log 10 dB mode and 1 dB per division in the Log 1 dB mode.

#### 3-49. Dynamic Range.

3-50. The dynamic range of a spectrum analyzer defines its ability to detect large and small signals and display them simultaneously. For operating purposes, dynamic range can be expressed as the ratio of the largest to smallest signals that can be simultaneously displayed on the CRT. In both the Linear and Log modes, the largest signal that can be displayed (full scale sensitivity) is determined by the INPUT SENSITIVITY, amplitude VERNIER and AMPLI-TUDE REF LEVEL control settings. The smallest signal that can be displayed is determined by the display range or by the internal noise floor (maximum sensitivity). In the Linear mode the smallest signal that can be displayed is approximately 1% of full scale. Thus, the dynamic range is approximately 40 dB as long as the internal noise floor is more than 40 dB below full scale. With the AMPLITUDE REF LEVEL switch in the NORMAL position, the display range in the Log 10 dB mode is greater than 80 dB. The dynamic range is, therefore, at least 80 dB as long as the noise floor is more than 80 dB below full scale. In the Log 1 dB mode, the display sensitivity is increased to 1 dB per division and the dynamic range, determined by the display range, is 10 dB.

#### 3-51. Amplitude Measurements (Linear Mode).

3-52. Figure 3-10 is a simplified block diagram showing a portion of the 3580A amplitude section in the Linear mode. The INPUT SENSITIVITY switch and amplitude VERNIER potentiometer control the input attenuation and gain of the Input Circuits and establish the maximum input level as outlined in Paragraph 3-40. In addition, the INPUT SENSITIVITY switch operates in conjunction with the AMPLITUDE REF LEVEL switch to establish the full-scale sensitivity and measurement range.

 $<sup>^{1}</sup>E_{n} = (4 \text{ kTBR})^{1/2}$ Where  $E_{n} = \text{noise level}$ ; k = Boltzmann's constant;  $T = \text{temperature (}^{\circ}K)$ ; B = bandwidth (Hz); R = input resistance.

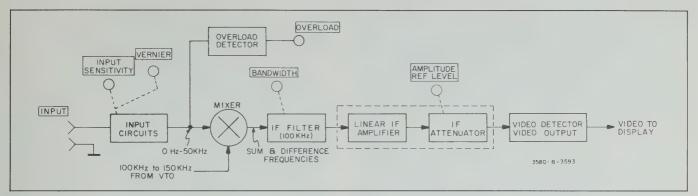


Figure 3-10. Amplitude Section (Linear Mode).

- 3-53. The INPUT SENSITIVITY switch has 12 positions: a CAL position and 11 voltage range settings. With the amplitude VERNIER in the CAL position and the AMPLITUDE REF LEVEL switch in the NORMAL (X1) position, the full-scale sensitivity, as determined by the INPUT SENSITIVITY switch setting, ranges from 20 V rms to 0.2 mV rms.
- 3-54. For any given INPUT SENSITIVITY setting, the dynamic range of the Input Circuits, Mixer and IF Filter is at least 80 dB as long as the noise floor is more than 80 dB below full scale. Thus, with the INPUT SENSITIVITY switch in the 0.2 mV position, an input signal as low as 0.1  $\mu V$  rms could be detected at the output of the IF Filter. In the Linear mode, however, the dynamic range of the display is limited to approximately 40 dB. This means that on the 0.2 mV range the smallest signal that can be displayed is approximately  $2\,\mu V$  or 1% of full scale. Moreover, the  $2\,\mu V$  signal might be visible on the display but it would be too small to be measured accurately. For all practical purposes, then, the dynamic display range is limited to approximately 20 dB.
- 3-55. To utilize the full measurement range of the instrument in the Linear mode, it is necessary to increase the display sensitivity. To accomplish this a variable IF Attenuator, controlled by the AMPLITUDE REF LEVEL switch, is inserted between the Linear IF Amplifier and Video Detector. With the AMPLITUDE REF LEVEL switch set to the NORMAL (X1) position, the IF attenuation is maximum. As the AMPLITUDE REF LEVEL switch is rotated in a clockwise direction, the IF attenuation decreases, the effective IF gain increases and the display sensitivity increases. The IF Attenuator provides seven additional ranges which allow the full-scale sensitivity to be varied from 0.1 mV rms to 0.1  $\mu$ V rms.
- 3-56. By observing the INPUT SENSITIVITY and AMPLITUDE REF LEVEL controls, it can be noted that the full-scale (blue) markings on the INPUT SENSITIVITY switch dial are indicated by a white window that is mechanically linked to the AMPLITUDE REF LEVEL switch. Changing the position of either switch changes the full-scale sensitivity in a 20 V, 10 V, 2 V, 1 V sequence. Changing the AMPLITUDE REF LEVEL setting, however, does not change the maximum input level. For example,

- with the INPUT SENSITIVITY switch set for a maximum input of 1 V rms and the AMPLITUDE REF LEVEL switch set to the X0.1 position, the full-scale sensitivity is 0.1 V rms, the display sensitivity is 10 mV per division but the maximum input level is still 1 V rms. Input signals greater than 0.1 V rms but less than or equal to 1 V rms will not overdrive the mixer or produce an OVERLOAD indication. They will, however, peak the display when the analyzer is tuned to their specific frequency. This does not damage the instrument or hinder its ability to measure signals within the display range.
- 3-57. Using the AMPLITUDE REF. LEVEL Control. Whenever possible, the AMPLITUDE REF LEVEL switch should be left in the NORMAL (X1) position and the INPUT SENSITIVITY switch should be used to set the full-scale sensitivity. This is because the Amplitude Calibration Procedure (Paragraph 3-199) is performed with the AMPLITUDE REF LEVEL switch in the NORMAL position and any error introduced by the IF Attenuator is adjusted out. When the AMPLITUDE REF LEVEL setting is changed from the NORMAL position, the accuracy of the IF Attenuator must be considered. This means that a possible worst-case error of ± 3% of full scale must be added to the amplitude accuracy specification. Amplitude accuracy is discussed in Paragraph 3-72.
- 3-58. There are commonly two occasions when it is necessary to change the AMPLITUDE REF LEVEL setting:
- a. When the required full-scale sensitivity is within the range of 0.1 mV rms to 0.1  $\mu$ V rms and the amplitude of the input signal is less than or equal to 0.1 mV rms. In this case, the INPUT SENSITIVITY switch is set to the 0.2 mV range (fully clockwise) and the appropriate range is selected using the AMPLITUDE REF LEVEL switch.
- b. For expanded-scale measurements where the amplitude of the input signal is 0.2 mV rms or greater and the signal or signals of interest are less than 10% of full scale with the INPUT SENSITIVITY switch set to the lowest range that does not produce an OVERLOAD indication. In this case, the AMPLITUDE REF LEVEL switch is initially set to the X1 position and the INPUT SENSITIVITY switch is set to the lowest range that does not produce an

OVERLOAD indication. The AMPLITUDE REF LEVEL switch is then set so that the low-level signals of interest can be measured. Signals greater than the full-scale level indicated by the white window on the INPUT SENSITIVITY switch dial will peak the display but will not damage the instrument or introduce harmonic or spurious responses.

**3-59.** Scale Factor. The blue markings on the AMPLITUDE REF LEVEL switch dial indicate the scale factor which, for *absolute* measurements is the factor by which the INPUT SENSITIVITY (Max. Input) setting must be multiplied to determine the full-scale sensitivity. For example, if the INPUT SENSITIVITY switch is set to the 2 V range and the AMPLITUDE REF LEVEL switch is set to the X0.01 position, the full-scale sensitivity is: 2 V X 0.01 = 0.02 V or 20 mV.

3-60. For absolute measurements the full-scale sensitivity is conveniently indicated by the white window on the INPUT SENSITIVITY switch dial and the scale factor can generally be ignored. If, for some reason, the scale factor is to be used, note that the even numbered positions on the AMPLITUDE REF LEVEL dial are not marked. This is because the scale factor in these positions depends on the INPUT SENSITIVITY switch setting. If the INPUT SENSITIVITY switch is set to the 20 V, 2 V, 0.2 V, etc. position, the unmarked positions on the AMPLITUDE REF LEVEL switch dial represent X0.5, X0.05, X0.005 and X0.0005. If the INPUT SENSITIVITY switch is set to 10 V, 1 V, 0.1 V, etc., the unmarked positions represent X0.2, X0.02, X0.002 and X0.0002. This applies only when the amplitude VERNIER is in the CAL position.

3-61. For relative measurements where the amplitude VERNIER is not in the CAL position, the full-scale markings on the INPUT SENSITIVITY switch dial do not apply and, for expanded-scale measurements, a scale factor must be used. In relative measurements the scale factor is the factor by which a relative amplitude reading must be multiplied to obtain the correct reading in percent of full scale.

3-62. When making relative measurements it is important to remember that any time the VERNIER is not in the CAL position, the relationship between the marked and unmarked positions of the AMPLITUDE REF LEVEL switch varies as a function of both the INPUT SENSITIVITY and amplitude VERNIER settings. There is always a X1, X0.1, X0.01, X0.001 relationship between the marked positions and this same relationship exists between the unmarked positions. However, there is no longer a X1, X0.5, X0.1 or X1, X0.2, X0.1 relationship between the marked and unmarked positions. To obtain the correct scale factor the following guidelines must be observed:

a. If the full-scale reference is set with the AMPLITUDE REF LEVEL switch in a marked position, all measurements must be made using marked positions.

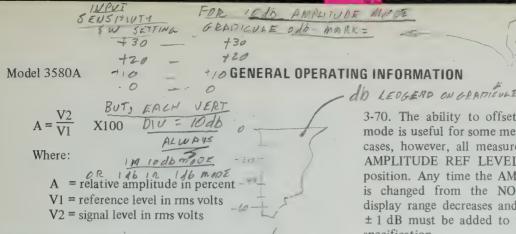
b. If the full-scale reference is set with the AMPLITUDE REF LEVEL switch in an unmarked position, all measurements must be made using unmarked positions.

c. The AMPLITUDE REF LEVEL setting on which the full-scale reference level is established becomes the X1 setting. If the X1 setting is a marked position, the scale factors for the remaining marked positions become X0.1, X0.01, etc. Similarly, if the X1 setting is an unmarked position the remaining unmarked positions become X0.1, X0.01, etc.

3-63. Examples. Consider the case where the fundamental frequency component of an input signal is 0.75 V and it is necessary to measure the second harmonic component whose relative amplitude is 1%. With the AMPLITUDE REF LEVEL control initially set to the NORMAL (X1) position and the amplitude VERNIER fully counterclockwise, the INPUT SENSITIVITY switch can be set to the 0.2 V position without overloading the instrument, The amplitude VERNIER can then be adjusted so that the amplitude of the fundamental frequency component is 100% of full scale. The 1% second harmonic will perhaps be visible on the display but an expanded scale will be required to measure it accurately. In this case, the full-scale reference was established with the AMPLITUDE REF LEVEL switch in the X1 position. Thus, the unmarked positions cannot be used and the scale factors of the marked positions are as indicated on the switch dial. By setting the AMPLITUDE REF LEVEL control to the X0.01 position, the 1% second harmonic can be expanded to 100% of full scale. It will then be necessary to multiply the 100% reading by the X0.01 scale factor to obtain the correct reading:  $100 \times 0.01 = 1\%$ .

3-64. Next, consider the case where the amplitude of the fundamental frequency component is 1.8 mV and it is necessary to measure a harmonic component whose relative amplitude is 4%. With the AMPLITUDE REF LEVEL switch in the NORMAL (X1) position and the amplitude VERNIER fully counterclockwise, the INPUT SENSITI-VITY switch can be set to the 0.2 mV (lowest) range. With a fundamental frequency component of less than 0.2 mV, a full-scale reference cannot be obtained on the 0.2 mV range. It is, therefore, necessary to go to the 0.1 mV range using the AMPLITUDE REF LEVEL switch. In this case, the full-scale reference will be established with the AMPLI-TUDE REF LEVEL switch in an unmarked position. This unmarked position becomes the X1 position. To expand the harmonic to a measurable level, it will be necessary to rotate the AMPLITUDE REF LEVEL control clockwise to the next unmarked position. This unmarked position has a scale factor of X0.1 and will expand the 4% harmonic to 40% of full scale. The correct reading can then be obtained by multiplying the 40% reading by the X0.1 scale factor:  $40 \times 0.1 = 4\%$ .

**3-65.** Alternative Method. An alternative method for determining the relative amplitude of two signals is to first measure the absolute voltage levels and then calculate their relative amplitude using the following formula:



3-66. Amplitude Measurements (Log Mode).

3-67. Figure 3-11 is a simplified block diagram showing a portion of the 3580A amplitude section in the Log mode. By comparing Figures 3-10 and 3-11, it can be noted that in the Log mode, the IF Amplifier/Attenuator is replaced by a Log Amplifier. The Log Amplifier provides an 80 dB display range.

3-68. With a dynamic display range of 80 dB, only eleven full-scale ranges are needed to utilize the full measurement range of the instrument. These eleven ranges are selected by the INPUT SENSITIVITY switch. With the amplitude VERNIER in the CAL position and the AMPLITUDE REF LEVEL control in the NORMAL (0 dB) position, the full-scale sensitivity, as determined by the INPUT SENSITIVITY switch setting, ranges from +30 dBV/dBm to -70 dBV/dBm.

3-69. As in the Linear mode, the maximum input level is determined by the INPUT SENSITIVITY and amplitude VERNIER settings. Likewise, the full-scale sensitivity is indicated on the INPUT SENSITIVITY switch dial by the white window that is linked to the AMPLITUDE REF LEVEL switch. In the Log mode, however, the AMPLI-TUDE REF LEVEL switch controls the dc operating point of the Video Output circuits and cannot be used to extend the measurement range. In the Log 10 dB mode, rotating the AMPLITUDE REF LEVEL switch in a clockwise direction offsets the entire display in 10 dB increments. Each time the display is offset, the value of the top line of the display graticule (full scale) becomes 10 dB lower as indicated by the white window. At the same time, however, the dynamic range of the display decreases by 10 dB. With the AMPLITUDE REF LEVEL switch set to the - 70 dB position, the full-scale sensitivity is 70 dB below its original value but the dynamic display range is only about 10 dB.

3-70. The ability to offset the display in the Log 10 dB mode is useful for some measurement applications. In most cases, however, all measurements can be made with the AMPLITUDE REF LEVEL switch set to the NORMAL position. Any time the AMPLITUDE REF LEVEL setting is changed from the NORMAL position, the dynamic display range decreases and a possible worst-case error of ± 1 dB must be added to the overall amplitude accuracy specification.

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3-71. Expanded-Scale Measurements. When the Log 1 dB mode is selected, the display sensitivity is increased to 1 dB per division and, with 10 vertical divisions, the maximum display range is 10 dB. The display in the Log 1 dB mode corresponds to the top 10 dB of the display in the Log 10 dB mode. Thus, by offsetting the display using the AMPLITUDE REF LEVEL control, any 10 dB portion of the 80 dB range can be displayed. In the Log 1 dB mode, the black (dB) markings on the AMPLITUDE REF LEVEL switch dial indicate the value of the top line of the display graticule with respect to the 0 dB (full scale) reference. For example, with the switch in the - 10 dB position the top line of the display graticule represents - 10 dB and the display ranges from -10 dB to -20 dB. Similarly, with the switch in the - 60 dB position the top line of the display graticule represents - 60 dB and the display ranges from -60 dB to - 70 dB.

#### 3-72. Amplitude Accuracy.

3-73. The Amplitude Accuracy Specification listed in Table 1-1 is as follows:

Amplitude Accuracy:	Log	Linear
Frequency Response: 20 Hz-20 kHz 5 Hz-50 kHz	± ,3 dB ± .5 dB	
Switching between bandwidths (25°C):		
3 Hz-300 Hz 1 Hz-300 Hz	± .5 dB ± 1 dB	
Amplitude display:	± 2 dB	± 2%
Input attenuator:	± .3 dB	± 3%
Amplitude reference level: (IF attenuator)		
most sensitive range all other ranges	± 1 dB ± 1 dB	± 10% ± 3%

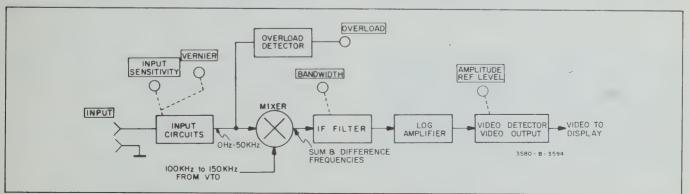


Figure 3-11. Amplitude Section (Log Mode).

3-74. The Amplitude Accuracy specification is broken down so that portions of the specification that do not apply to a particular measurement can be eliminated. All applicable portions of the specification must be added together to obtain the overall accuracy specification. It should be noted that the overall accuracy specification reflects the absolute worst-case error that could possibly be encountered. Typically, all parameters are well within their specified tolerances and the probability of having a worst-case condition is very slight. As more parameters are added to the specification, the magnitude of the possible worst-case error increases but the probability of having a worst-case condition greatly decreases.

3-75. The Frequency Response, Amplitude Display and Input Attenuator specifications must always be taken into account when calculating the overall accuracy specification. Excluding the Switching Between Bandwidths and Amplitude Ref. Level specifications, the worst case error is  $\pm$  2.8 dB in the Log mode or  $\pm$  10% of reading in the Linear mode.

3-76. The Switching Between Bandwidths specification can be disregarded as long as the Amplitude Calibration Procedure is performed on the BANDWIDTH setting that is used for measurements. If the BANDWIDTH setting is changed, the Switching Between Bandwidths specification must be added to the overall accuracy specification. Similarly, the Amplitude Ref. Level specification can be disregarded as long as the AMPLITUDE REF LEVEL control is in the NORMAL position. If the AMPLITUDE REF LEVEL setting is changed, the Amplitude Ref. Level specification must also be added to the overall accuracy specification.

#### 3-77. Internal Cal. Signal.

3-78. With the INPUT SENSITIVITY switch set to the CAL position, the high INPUT terminal on the front panel is disconnected and an internally generated calibration signal is applied to the Input Amplifier. The calibration signal is a highly accurate 15/85 duty cycle pulse train which provides a 10 kHz fundamental frequency component along with odd and even harmonic components spaced at 10 kHz intervals (Figure 3-12). The magnitude of the pulse is such that the fundamental frequency component produces full scale deflection when the instrument is properly calibrated. The amplitudes of the harmonic components are not meaningful. The calibration signal can be used for amplitude calibration or to verify the frequency accuracy of the instrument.

3-79. In the Amplitude Calibration Procedure (Paragraph 3-199), the front panel 10 kHz CAL potentiometer is adjusted so that the 10 kHz fundamental frequency component of the cal. signal produces full scale deflection. This calibrates all circuitry following the input attenuator to a full scale accuracy of  $\pm$  1.5% at 10 kHz.

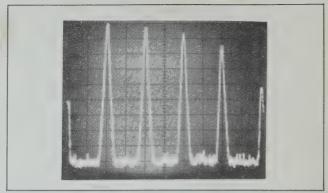


Figure 3-12. Cal Signal.

#### 3-80. Bandwidth Setting.

3-81. Refer to Figure 3-13 for the following discussion. The 3580A uses a hetrodyne technique where the 0 Hz to 50 kHz input signal is mixed with a 100 kHz to 150 kHz signal from a Voltage-Tuned Local Oscillator (VTO). To select a given frequency present at the input of the Mixer, the VTO frequency is tuned so that the difference between it and the frequency of interest is 100 kHz. The 100 kHz intermediate frequency (IF) is fed through the IF Filter, detected and applied to the vertical axis of the CRT display. Signals outside the passband of the IF Filter are rejected. The BANDWIDTH setting determines the bandwidth of the IF Filter and thus, the selectivity of the instrument.

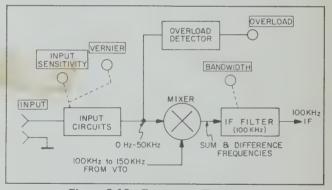


Figure 3-13. Frequency Tuning.

3-82. For operating purposes, the 3580A input channel can be pictured as a bandpass filter than can be manually tuned or swept over the 0 Hz to 50 kHz frequency range. The instrument responds only to signals passing through the filter and thereby sorts out the various frequency components present at the input. The BANDWIDTH setting determines the width of the filter skirts at the - 3 dB points above and below the tuned frequency:

Lower 3 dB Point = 
$$f_0 - \frac{BW}{2}$$

Upper 3 dB Point = 
$$f_o + \frac{BW}{2}$$

Where:

f<sub>o</sub> = Tuned Frequency (0 Hz to 50 kHz) BW = BANDWIDTH Setting (1 Hz-300 Hz **3-83.** IF Bandpass Characteristic. Many signal analyzers use active filters that have very steep skirts and a square-shaped bandpass characteristic that approaches the ideal "window filter". This type of filtering provides a high degree of selectivity, but because of its long transient response time, is not well suited for swept frequency applications. The 3580A IF Filter consists of 5 synchronously-tuned crystal filter stages. The bandpass characteristic of the synchronously-tuned filter (Figure 3-14) closely approximates a gaussian response. The gaussian filter provides good selectivity and, because of its relatively short transient response time, is considered optimum for sweeping.

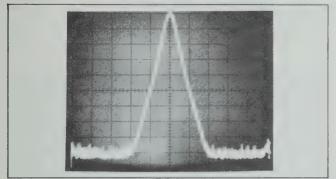


Figure 3-14. IF Filter Response.

**3-84.** Shape Factor. The shape factor of the 3580A IF Filter is approximately 10:1 on the 1 Hz through 100 Hz bandwidths and 8:1 on the 300 Hz bandwidth. A shape factor of 10:1 means that the filter skirts are 10 times wider at the -60 dB points than at the -3 dB points. Similarly, a shape factor of 8:1 means that the skirts are 8 times wider at the -60 dB points than at the -3 dB points. On the 10 Hz bandwidth, for example, the -3 dB points are 10 Hz apart and the -60 dB points are 10 x 10 or 100 Hz apart. The filter is, in effect, centered on the tuned frequency,  $f_0$ , and exhibits 3 dB of rejection to signals that are  $\pm$  5 Hz away from  $f_0$  and 60 dB of rejection to signals that are  $\pm$  50 Hz away from  $f_0$ .

**3-85.** Equivalent Noise Bandwidth. When making noise measurements with the 3580A, it is necessary to use the "equivalent noise bandwidth" rather than the 3 dB bandwidth indicated by the BANDWIDTH setting. In the 3580A, the equivalent noise bandwidth is 12% wider than the absolute 3 dB bandwidth. Note that the specified bandwidth tolerance is ± 15%. This means that the absolute 3 dB bandwidth can be 15% wider or narrower than the BANDWIDTH setting. For optimum accuracy, measure the absolute 3 dB bandwidth of your instrument and use that figure to calculate the equivalent noise bandwidth.

**3-86.** Bandwidth Selection. There are 4 things to consider when selecting a BANDWIDTH setting:

- 1) Resolution
- 2) Low Frequency Limit
- 3) Response Time
- 4) Noise Rejection

**3-87.** Resolution. Resolution is the ability of the analyzer to separate signals that are closely spaced in frequency. An important point here is that the response of the analyzer to a CW signal is an amplitude vs. frequency plot of the IF Filter (Figure 3-15). The width and shape of the filter skirts are, therefore, the major limitations of resolution. If two CW signals appear in the passband (± 3 dB points) simultaneously, they cannot be separated (Figure 3-16). If two signals differing widely in amplitude are both inside the filter skirts, the response of the larger signal can hide or obscure that of the smaller signal (Figure 3-17). If the amplitude of the smaller signal is greater than that of the skirt produced by the larger signal, the peak of the smaller signal can be resolved (Figure 3-18). For optimum resolution, the bandwidth should be narrowed to the point where only one signal is inside the filter skirts at any given time. Generally, the width of the filter skirts at the - 80 dB point does not exceed 15 times the 3 dB bandwidth. Thus, optimum resolution can always be obtained when the frequency separation between signals is at least 15 times the BANDWIDTH setting.

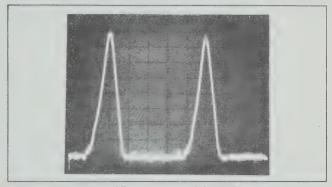


Figure 3-15. Response Of CW Signals.

3-88. Table 3-3 lists the *approximate* maximum resolution for two signals whose relative amplitude is within the range of 0 dB to 70 dB. For example, on the 100 Hz Bandwidth, it is possible to resolve two signals that are equal in amplitude and 2 X BW or 200 Hz apart. Similarly, it is possible to resolve two signals that differ in amplitude by 40 dB and are 5 X BW or 500 Hz apart.

3-89. In some analyzers resolution is further limited by noise sidebands caused by residual FM in the local oscillator. In the 3580A, however, the 1 Hz bandwidth is

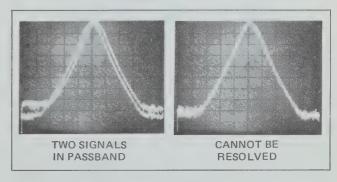


Figure 3-16. Two Signals In Passband.

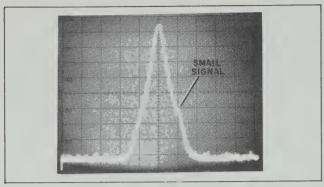


Figure 3-17. Large Signal Hides Small Signal.

the only bandwidth on which the noise sidebands can be resolved. On the 1 Hz bandwidth the noise sidebands are more than 70 dB below the peak of a CW response  $\pm$  10 Hz away from the center frequency,  $f_o$  (Figure 3-19). In some isolated cases, the noise sidebands may slightly degrade the resolution on the 1 Hz bandwidth. For the most part, however, noise sidebands can be ignored.

Table 3-3. Frequency Resolution.

AMPL DIFFERENCE	MAX. RESOLUTION		
<b>0</b> dB	2 X BW		
10 dB	2 X BW		
20 dB	5 X BW		
30 dB	5 X BW		
40 dB	5 X BW		
50 dB	10 X BW		
60 dB	10 X BW		
70 dB	10 X BW		

BW = BANDWIDTH setting

**3-90.** Low Frequency Limit. To utilize the full dynamic range of the instrument at low frequencies, the lowest frequency to be resolved must be at least 5 times the selected BANDWIDTH. This low frequency limit is due to the zero response described in the following paragraphs.

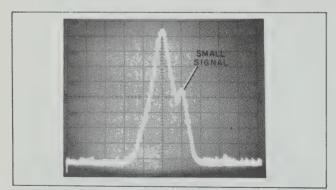


Figure 3-18. Small Signal Resolved.

3-91. As the 3580A frequency is tuned toward 0 Hz, the VTO frequency approaches the 100 kHz IF. Although the VTO signal is suppressed by the use of a double balanced mixer, part of the VTO signal feeds through the 100 kHz IF Filter and appears on the display. The response produced by the VTO signal peaks at 0 Hz and is appropriately called the "zero response". As with any other CW signal, the zero

response on the display is an amplitude vs. frequency plot of the IF Filter (Figure 3-20). The wider the bandwidth, the wider the zero response.

3-92. The amplitude and bandwidth of the zero response determines the lowest frequency that can be resolved. On any BANDWIDTH setting, the peak amplitude of the zero response is more than 30 dB below the full scale reference set by the INPUT SENSITIVITY and amplitude VERNIER controls (AMPLITUDE REF LEVEL switch in NORMAL position). With the zero response more than 30 dB below full scale and a dynamic display range of 80 dB, the maximum difference between the peak of the zero response and any measureable input signal is between 40 dB and 50 dB. Table 3-3 indicates that the maximum resolution between two signals whose relative amplitude is between 40 dB and 50 dB is 5 times the BANDWIDTH setting.

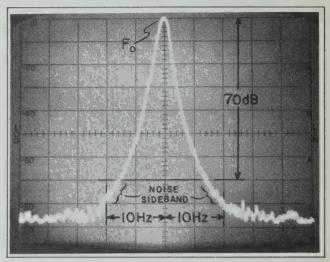


Figure 3-19. Noise Sidebands (1 Hz BW)

3-93. Response Time. Generally, when making swept frequency measurements, it is desirable to have good resolution and, at the same time, sweep as rapidly as possible. This involves a definite trade off since the narrower bandwidths provide the greatest resolution but require slower sweep rates. As the bandwidth is narrowed, the IF Filter takes longer to respond to electrical changes taking place at its input. Consequently, the sweep rate must be slow so that the signal remains in the passband long enough for the filter to fully respond. Optimum sweep rate is discussed in Paragraph 3-135.

3-94. For applications where narrow bandwidths and slow sweep rates are required, the 3580A Adaptive Sweep feature can often be used to substantially reduce the measurement time. Adaptive Sweep is discussed in Paragraph 3-147.

**3-95.** Noise Rejection. The maximum sensitivity of the analyzer is limited by its own internally generated noise. As outlined in Paragraph 3-44, internal noise is a function of bandwidth, input resistance and tuned frequency. The narrower bandwidths provide the greatest noise rejection.

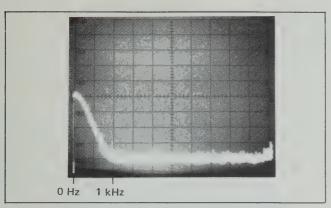


Figure 3-20. Zero Response (300 Hz BW).

#### 3-96. Frequency Setting.

3-97. The front panel FREQUENCY control tunes the frequency of the analyzer over the 0 Hz to 50 kHz range. The control can be used to set either the start or center frequency of a linear sweep. The start or center frequency selected by the FREQUENCY control is indicated on the FREQUENCY dial.

3-98. The FREQUENCY control has two selectable drive ratios to permit coarse or fine tuning. Coarse tuning is selected by pushing the crank toward the front panel; fine tuning is selected by pulling the crank outward. In the coarse position, one revolution of the crank changes the FREQUENCY dial setting by approximately 2.7 kHz. In the fine position, one revolution of the crank changes the frequency by approximately 73 Hz.

**3-99.** Frequency Dial. The FREQUENCY dial indicates the start or center frequency in kHz. Dial settings range from 00.0 kHz to approximately 50.5 kHz. The frequency dial resolution is 20 Hz represented by one minor division on the frequency scale. When the instrument is properly calibrated (Paragraph 3-195), the frequency dial accuracy is:

a. ± 100 Hz when the ambient temperature is within the range of 20°C (68°F) to 30°C (86°F).

b.  $\pm$  300 Hz when the ambient temperature is within the range of 0°C (32°F) to 20°C (68°F) or 30°C (86°F) to 55°C (131°F).

**3-100. Start/Center.** With the START/CENTER slide switch in the START position, the FREQUENCY dial setting indicates the frequency represented by the first vertical line on the left-hand side of the display graticule. This is the "start frequency" or frequency at which the sweep begins. With the switch in the CENTER position, the FREQUENCY dial setting indicates the frequency represented by the center vertical line on the display graticule. This is the "center frequency" of the sweep.

3-101. When surveying a spectrum containing two or more signals, it is generally convenient to leave the START/CENTER switch in the START position. The FRE-QUENCY control can then be used to set the start frequency and the FREQUENCY SPAN control can be used to set the spectrum width or "end frequency". To observe one frequency component in a spectrum, set the START/CENTER switch to the CENTER position and set the FREQUENCY dial to the frequency of interest. The frequency of interest will appear in the center of the display. The width of the center frequency response can be adjusted by changing the FREQUENCY SPAN or BAND-WIDTH setting.

**3-102. Zero Cal. Potentiometer.** The purpose of the ZERO CAL potentiometer is to enable the operator to compensate for slight variations in frequency dial accuracy that occur during warm-up or when the instrument is operated in an uncontrolled environment. The ZERO CAL potentiometer is also used in the Log Zero sweep mode to establish the correct starting point for the log sweep. Refer to Paragraph 3-195 for the Frequency Calibration Procedure.

## 3-103. Frequency Span Setting.

3-104. The FREQUENCY SPAN control sets the width of the spectrum to be observed during linear or manual sweeps. Excluding the 0 Hz position, there are ten FREQUENCY SPAN settings ranging from 5 Hz per division to 5 kHz per division. With ten horizontal divisions on the display, the overall spectrum width can be adjusted from 50 Hz to 50 kHz.

**3-105. O Hz Span.** With the FREQUENCY SPAN switch set to the O Hz position, the instrument remains at the start or center frequency indicated on the FREQUENCY dial. The display, however, continues to sweep at the rate selected by the SWEEP TIME setting. The result is a graphical display of amplitude vs. *time*.

3-106. The amplitude vs. time feature is useful for observing the amplitude variations of a signal that occur over relatively long periods of time. For example, the amplitude of the 10 kHz sine wave shown in Figure 3-21A appears stable on a conventional oscilloscope but is actually varying at a very slow rate. In Figure 3-21B, the 3580A was used to monitor the amplitude of the 10 kHz signal over a 2,000 second period. The 3580A amplitude vs. time display shows that the 10 kHz signal is amplitude modulated by a triangular-shaped signal whose frequency is 0.00166 Hz.

3-107. Because of its narrow bandwidth, the 3580A cannot respond to rapid changes in amplitude. The maximum modulating frequency that can be observed and measured with any accuracy is approximately 100 Hz on the 300 Hz BANDWIDTH setting.

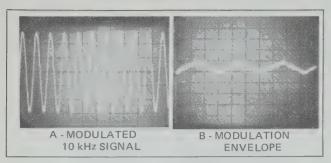


Figure 3-21. Amplitude vs. Time.

#### 3-108. Frequency Out of Range.

3-109. There are a number of cases where the FRE-QUENCY and FREQUENCY SPAN settings are such that the frequency sweep attempts to go below 0 Hz or above 50 kHz. For example, if the start frequency is set to 10 kHz and the FREQUENCY SPAN setting is 5 kHz/div (50 kHz), the end frequency of the sweep is 60 kHz which is 10 kHz above the 50 kHz limit. If the instrument is set for a center frequency of 0 Hz, the start frequency is a negative value and the area between the start frequency and the center frequency is not meaningful.

3-110. To minimize erroneous indications, an internal detector senses when the frequency sweep tries to go below 0 Hz or above 50 kHz and, in turn, clears the display. The result is a clean baseline in areas where the frequency limits are exceeded (Figure 3-22).

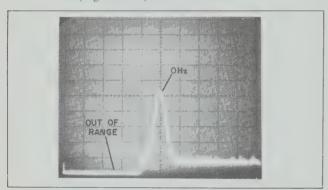


Figure 3-22. Frequency Out Of Range.

3-111. The frequency out-of-range detector is not exact. Consequently, there are margin areas below 0 Hz and above 50 kHz where signals can be displayed. Typically, the margin below 0 Hz is about 500 Hz wide. Signals displayed in this negative margin are the images of the 0 Hz to 500 Hz signals displayed on the positive side of 0 Hz (Figure 3-23). The margin above 50 kHz is about 800 Hz wide and signals up to 50.8 kHz can generally be displayed.

3-112. The frequency sweep will go out of range under any of the following conditions:

a. When: Fstart + 10 Fspan = > 50 kHz

b. When: Fcenter + 5 Fspan = > 50 kHz

c. When: Fcenter - 5 Fspan = < 0 Hz

Where: Fstart = .start frequency of sweep

Fspan = FREQUENCY SPAN setting

Fcenter = center frequency of sweep

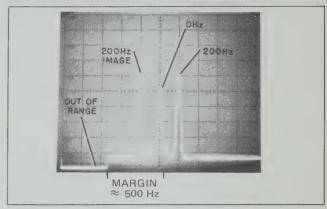


Figure 3-23. Margin Below 0 Hz.

#### 3-113. Sweep Modes.

3-114. The front panel SWEEP MODE switch permits selection of six sweep modes:

- 1) REP (Repetitive)
- 2) SING (Single)
- 3) RESET
- 4) MAN (Manual)
- 5) LOG ZERO
- 6) LOG

**3-115.** Repetitive Mode. In the Repetitive sweep mode the instrument sweeps continuously over the selected frequency range. The duration of each sweep is determined by the SWEEP TIME setting.

**3-116.** Single Mode. When the Single sweep mode is selected, the instrument sweeps one time over the selected frequency range and stops at the end frequency. The instrument remains at the end frequency until another sweep mode is selected or until a new sweep is initiated. A new sweep can be initiated by:

- a. Setting the SWEEP MODE switch to RESET and back to SING.
- b. Pressing the CLEAR WRITE button. This clears the display and simultaneously resets the sweep. Do not use clear-write when making x y recordings.
  - c. External triggering as outlined in Paragraph 3-143.

3-117. The Single sweep mode is particularly useful for making X-Y recordings using an external plotter connected to the rear panel RECORDER outputs. The operator can start the sweep, go about his business and return later to retrieve the completed recording.

3-118. It should be noted that the rear panel PEN LIFT output is operative *only* in the Single sweep mode. The PEN LIFT output is provided for use with X-Y recorders that have an electrically operated pen lift circuit enabling the pen to be remotely actuated by a contact closure (Paragraph 3-170).

**3-119. Reset Mode.** When the Reset mode is selected, the sweep is reset to the left-hand side of the screen and the instrument remains at the *start* frequency determined by the FREQUENCY dial setting.

3-120. The Reset mode is used primarily for calibrating the FREQUENCY dial. In the Frequency Calibration Procedure (Paragraph 3-195), the Reset mode is selected and the FREQUENCY dial is set for a start frequency of 00.0 kHz. The ZERO CAL potentiometer is then adjusted so that the zero response peaks at 0 Hz on the display.

**3-121. Manual Mode.** In the Manual sweep mode, the electronic frequency sweep is disabled and frequency control is transferred to the MANUAL VERNIER potentiometer. By adjusting the MANUAL VERNIER, the frequency can be set anywhere within the selected spectrum. With the MANUAL VERNIER set fully counterclockwise, the CRT sweep is at the left-hand side of the screen and the instrument is tuned to the start frequency determined by the FREQUENCY setting. As the vernier is rotated in a clockwise direction, the frequency increases and the video information is written (and retained) on the CRT just as it is when using the electronic sweep.

3-122. The Manual sweep is useful for applications where it is necessary to precisely measure the frequency of a signal within the spectrum. For precise frequency measurements, an electronic counter is connected to the rear panel TRACKING OSC OUT or LO OUTPUT to monitor the frequency. Using a narrow bandwidth such as 10 Hz or 30 Hz, the MANUAL VERNIER is adjusted so that the CRT sweep is at the peak of the signal to be measured. If the TRACKING OSC OUT is used, the frequency of the signal can then be read directly from the counter. If the LO OUTPUT is used, the frequency must be calculated by dividing the counter reading by ten and subtracting 100 kHz (Paragraph 3-178).

#### **NOTE**

When the SWEEP MODE setting is changed from LOG ZERO to MAN or from RESET to MAN, the frequency sweep jumps from the start frequency to the frequency set by the MANUAL VERNIER. Conversely, when the SWEEP MODE is changed from MAN to LOG

ZERO or from MAN to RESET, the frequency sweep jumps from the frequency set by the MANUAL VERNIER to 0 Hz or to the start frequency. In either case, the rapid change in frequency will distort the trace being displayed on the CRT. If it is desirable to retain a specific trace when switching to or from the Manual mode, set the MANUAL VERNIER fully counterclockwise before changing the SWEEP MODE setting.

3-123. Log Zero Mode. The Log Zero mode is used to establish the correct starting frequency for the log sweep. When the Log Zero mode is selected, the sweep is reset to the left-hand side of the screen, the FREQUENCY and FREQUENCY SPAN controls are disabled and the start frequency is internally set to 0 Hz. To calibrate the log sweep, the front panel ZERO CAL potentiometer is adjusted to peak the zero response at the left-hand edge of the display graticule. Peaking the zero response at 0 Hz in the Log Zero mode nulls out any dc offsets in the frequency control circuit. This ensures that the log sweep will start at 20 Hz.

**3-124.** Log Sweep. When the Log sweep mode is selected, the following things take place:

a. The FREQUENCY, FREQUENCY SPAN and SWEEP TIME controls are disabled and their settings do not effect the log sweep. The ZERO CAL potentiometer remains operative and, to ensure the proper starting point for the log sweep, must be adjusted for peak zero response in the Log Zero mode.

b. The instrument sweeps logarithmically over the 20 Hz to 43 kHz frequency range. The log sweep is repetitive and the duration of each sweep is approximately 5 seconds.

#### NOTE

When the Log sweep mode is first selected or when the log sweep is initiated by external triggering, optimum frequency accuracy will not be obtained until 3 or 4 continuous sweeps have been made. This peculiarity of the Log sweep is caused by dielectric absorption (soak effect) in the integrating capacitor of the Log sweep generator.

3-125. By observing the CRT display it can be noted that each decade frequency of the log sweep is marked at the bottom of the graticule. The first vertical line on the left-hand side of the graticule represents 20 Hz, the second line represents 43 Hz and the third line 98.2 Hz. This sequence is repeated for each decade of frequency.

3-126. Figure 3-24 is a plot of frequency vs. time during a log sweep. At the beginning of the sweep the slope of the curve is gradual. A gradual slope indicates a small change in

frequency for a given unit of time and thus, a slow sweep rate. As the sweep progresses the slope becomes steeper and the sweep rate increases exponentially.

3-127. Because the 3580A is a narrow band instrument, the continuously increasing sweep rate presents a problem. At low frequencies narrow bandwidths are required to obtain good resolution. Narrow bandwidths can be used at low frequencies because the sweep rate is slow. As the frequency and sweep rate increases, however, the bandwidth must be widened so that the instrument can respond properly.

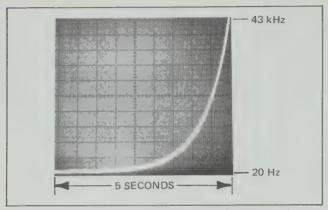


Figure 3-24. Frequency Vs. Time (Log Sweep).

3-128. The 300 Hz BANDWIDTH is the only bandwidth that allows the instrument to respond properly over the entire range of the log sweep. For this reason, the ADJUST light comes on when any bandwidth other than 300 Hz is selected. On the 300 Hz bandwidth, however, low frequency measurements are not possible because the resolution is poor and the skirt produced by the zero response covers nearly half of the display (Figure 3-25). For measurements at low frequencies a narrower bandwidth must be used. Table 3-4 lists the recommended bandwidths for measurements in given portions of the spectrum.

Table 3-4. Recommended Bandwidths (Log Sweep).

FREQUENCY RANGE	RECOMMENDED BANDWIDTH		
20 Hz-200 Hz	10 Hz		
200 Hz-982 Hz	30 Hz		
982 Hz-9.82 kHz	100 Hz		
9.82 kHz-43 kHz	300 Hz		

3-129. The log sweep is intended primarily for making log amplitude vs. log frequency plots of 2-port devices. For this application, the network to be tested is connected in the closed-loop configuration where the rear panel Tracking Oscillator Output supplies the stimulus and the 3580A measures the response.

#### NOTE

Because of the relatively fast sweep rates used in the Log sweep mode, conventional X-Y recorders connected to the rear panel RE-CORDER outputs cannot respond properly during log sweeps (see Paragraph 3-163).

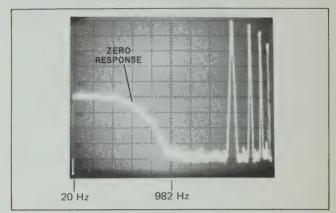


Figure 3-25. Log Sweep (300 Hz BW).

3-130. During closed loop measurements the bandwidth limitations are not quite as stringent as those previously described. This is because the input frequency, derived from the Tracking Oscillator Output, is always in or near the center of the passband. The only requirement is that the bandwidth be wide enough to permit the instrument to fully respond to amplitude variations introduced by the network under test. If the network under test does not have extremely steep skirts, a relatively narrow bandwidth can be used. For example, Figure 3-26 is a log amplitude vs. log frequency plot of a 20 kHz notch filter. The plot was made using a 30 Hz bandwidth.

3-131. The easiest way to select the proper bandwidth for the log sweep is to start with a wide bandwidth such as 100 Hz and then narrow the bandwidth until the amplitude or shape of the response curve begins to change. When the response curve starts to change, the bandwidth is too narrow.

#### 3-132. Sweep Time and Sweep Rate.

**3-133.** Sweep Time Control. The front panel SWEEP TIME control provides 14 sweep time settings ranging from 0.01 second per division to 200 seconds per division. With 10 horizontal divisions, total sweep time ranges from 0.1 second to 2,000 seconds.

**3-134.** Sweep Rate. The sweep rate in Hz per second is determined by the FREQ SPAN and SWEEP TIME settings:

$$R = \frac{F_s}{T}$$

Where:

R = sweep rate in Hz/sec F<sub>s</sub> = FREQ SPAN setting T = SWEEP TIME setting Increasing the frequency span or decreasing the sweep time increases the sweep rate.

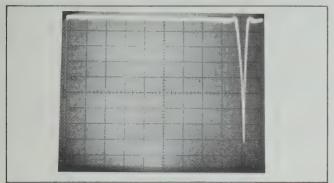


Figure 3-26. Log Amplitude Vs. Log Freq. Plot of 20 kHz Notch Filter (30 Hz BW).

**3-135.** Optimum Sweep Rate. The optimum sweep rate is the maximum rate at which the frequency can be swept without excessively compressing or skewing the amplitude response. When the 3580A is sweeping at what is considered to be the optimum rate, the amplitude compression is about 2%.

3-136. The optimum sweep rate is determined by the response time of the instrument. If the response time is long, the sweep rate must be slow so that the instrument can respond properly. The response time of the 3580A is determined by the BANDWIDTH and DISPLAY SMOOTH-ING settings. Narrowing the bandwidth or increasing the display smoothing increases the response time and, therefore, decreases the optimum sweep rate.

**3-137.** Optimum Sweep Indicator. The 3580A is equipped with an internal detector that monitors the BANDWIDTH, DISPLAY SMOOTHING, FREQUENCY SPAN and SWEEP TIME control settings. When these control settings are such that the sweep rate exceeds the optimum sweep rate, the front panel ADJUST indicator illuminates.

3-138. To sweep at the optimum rate, first set the FREQUENCY, FREQUENCY SPAN, BANDWIDTH and DISPLAY SMOOTHING controls to obtain the desired measurement parameters. Then, starting with a slow SWEEP TIME setting, increase the sweep rate until the ADJUST light first comes on. When the ADJUST light comes on, rotate the SWEEP TIME control one position counterclockwise. The ADJUST light will go out and the instrument will sweep at the optimum rate.

3-139. Table 3-5 lists the optimum SWEEP TIME settings for various FREQ SPAN, BANDWIDTH and DISPLAY SMOOTHING settings.

3-140. For closed-loop measurements where the 3580A is used as a network analyzer, the optimum sweep rate is determined by the 3580A BANDWIDTH and DISPLAY SMOOTHING control settings and by the bandwidth of the network under test. During closed-loop measurements, the input frequency is always near the center of the passband

and the IF Filter is required to respond only to amplitude variations introduced by the network. For this reason, the optimum sweep rate for closed-loop measurements is generally much faster than it is for open-loop measurements. In many closed-loop measurement applications the sweep rate can be set 20 to 25 times faster than the optimum rate indicated by the ADJUST light.

3-141. If the optimum sweep rate is not limited by the bandwidth of the 3580A, it may be limited by the bandwidth of the network under test. For bandpass and low pass filters, a rough approximation of optimum sweep rate can be made using the following formula:

$$R = \frac{BW^2}{2}$$

Where:

R = optimum sweep rate in Hz/sec BW = bandwidth of network under test

3-142. In practice it is often difficult to predict the optimum sweep rate. For this reason, the simplest approach is to start with the optimum rate set using the ADJUST light. Then, while observing the response curve, gradually increase the sweep rate until the amplitude or shape of the curve begins to change. When the curve begins to change the sweep rate is too fast.

### 3-143. External Triggering.

3-144. The EXT TRIG IN connector enables the frequency sweep to be remotely inhibited using a contact closure or TTL Logic Levels. This signal may be used to inhibit the sweep in the single, repetitive or Log Sweep Mode.

3-145. In order to inhibit the sweep, the externally applied signal into the EXT TRIG IN connector is kept low. To allow the 3580A to perform a single sweep, the inhibit signal is allowed to go high for greater than 1 msec, but for less than the total sweep time. If the inhibit signal is not returned to low within the specified time, additional sweeps may be initiated.

3-146. To remotely inhibit the frequency sweep apply the following levels to the center terminal of the EXT TRIG IN connector:

Sweep Inhibit: Ground (through < 10 K) or -0.5 V dc to 0.5 V dc.

Sweep Enable: Open or +2.5 V dc to +5 V dc.

### NOTE

The outer shield of the EXT TRIG IN connector is connected to case ground. The center terminal of the connector is the inhibit line.

### 3-147. Adaptive Sweep.

3-148. One of the inconveniences associated with low frequency spectrum analyzers is the extremely slow sweep rates required when using narrow bandwidths. For example,

to sweep over a 200 Hz spectrum using a 1 Hz bandwidth, the optimum sweep time setting is 50 seconds per division. This makes the overall measurement time 500 seconds or about 8 minutes. If a sweep time setting of 200 seconds per division is used, the total measurement time is 2,000 seconds or 33 minutes.

Table 3-5. Optimum Sweep Time Settings.

BANDWIDTH SETTING	FREQ SPAN/DIV	SPECTRUM WIDTH	OPTIMUM SWP. TIME (SMOOTHING MIN.)	OPTIMUM SWP. TIME (SMOOTHING MED.)	OPTIMUM SWP. TIME (SMOOTHING MAX.)
1 Hz	5 Hz	50 Hz	10 sec.	100 sec.	
1 Hz	10 Hz	100 Hz	20 sec.	200 sec.	
1 Hz	20 Hz	200 Hz	50 sec.		
1 Hz	50 Hz	500 Hz	100 sec.		
1 Hz	0.1 kHz	1 kHz	200 sec.*		
7 172	0.7 K/12	, KIIZ	200 300.		
3 Hz	5 Hz	50 Hz	1 sec.	10 sec.	100 sec.
3 Hz	10 Hz	100 Hz	2 sec.	20 sec.	200 sec.
3 Hz	20 Hz	200 Hz	5 sec.	50 sec.	
3 Hz	50 Hz	500 Hz	10 sec.	100 sec.	
3 Hz	0.1 kHz	1 kHz	20 sec.	200 sec.*	
3 Hz	0.2 kHz	2 kHz	50 sec.		
3 Hz	0.5 kHz	5 kHz	100 sec.		
3 Hz	1 kHz	10 kHz	200 sec.*		
10 Hz	5 Hz	50 Hz	0.1 sec.	1 sec.	10 sec.
10 Hz	10 Hz	100 Hz	0.1 sec.	2 sec.	
10 Hz	20 Hz	200 Hz	0.2 sec. 0.5 sec.		20 sec.
10 Hz	1			5 sec.	50 sec.
	50 Hz	500 Hz	1 sec.	10 sec.	100 sec.
10 Hz	0.1 kHz	1 kHz	2 sec.	20 sec.	200 sec.*
10 Hz	0.2 kHz	2 kHz	5 sec.	50 sec.	
10 Hz	0.5 kHz	5 kHz	10 sec.	100 sec.	
10 Hz	1 kHz	10 kHz	20 sec.	200 sec.*	
10 Hz	2 kHz	20 kHz	50 sec.		
10 Hz	5 kHz	50 kHz	100 sec.		
30 Hz	5 Hz	50 Hz	0.01 sec.**	0,1 sec.	1 sec.
30 Hz	10 Hz	100 Hz	0.02 sec.	0.2 sec.	2 sec.
30 Hz	20 Hz	200 Hz	0.05 sec.	0.5 sec.	5 sec.
30 Hz	50 Hz	500 Hz	0.1 sec.	1-sec.	
30 Hz	0,1 kHz	1 kHz	0.1 sec.		10 sec.
30 Hz	0.2 kHz	2 kHz		2 sec.	20 sec.
30 Hz	0.5 kHz	5 kHz	0.5 sec.	5 sec.	50 sec.
30 Hz			1 sec.	10 sec.	100 sec.
	1 kHz	10 kHz	2 sec.	20 sec.	200 sec.*
30 Hz	2 kHz	20 kHz	5 sec.	50 sec.	
30 Hz	5 kHz	50 kHz	10 sec.	100 sec.	
100 Hz	5 Hz	50 Hz	0.01 sec.**	0.01 sec.**	0.1 sec.
100 Hz	10 Hz	100 Hz	0.01 sec.**	0.02 sec.	0,2 sec.
100 Hz	20 Hz	200 Hz	0.01 sec.**	0.05 sec.	0.5 sec.
100 Hz	50 Hz	500 Hz	0.01 sec.**	0.1 sec.	0.5 sec.
100 Hz	0.1 kHz	1 kHz	0.02 sec.	0.2 sec.	1 sec.
100 Hz	0.2 kHz	2 kHz	0,05 sec.	0.5 sec.	2 sec.
100 Hz	0.5 kHz	5 kHz	0,1 sec.	1 sec.	
100 Hz	1 kHz	10 kHz	0.1 sec.		10 sec.
100 Hz	2 kHz	20 kHz		2 sec.	20 sec.
100 Hz		L L	0.5 sec.	5 sec.	50 sec.
100 112	5 kHz	50 kHz	1 sec.	10 sec.	100 sec.
300 Hz	5 Hz	50 Hz	0.01 sec.**	0.01 sec.**	0.01 sec.**
300 Hz	10 Hz	100 Hz	0.01 sec.**	0.01 sec.**	0.02 sec.
300 Hz	20 Hz	200 Hz	0.01 sec.**	0.01 sec.**	0.05 sec.
300 Hz	50 Hz	500 Hz	0.01 sec.**	0.01 sec.**	
300 Hz	0.1 kHz	1 kHz	0.01 sec.**		0.1 sec.
300 Hz	0.2 kHz	2 kHz	0.01 sec.**	0.02 sec.	0.2 sec.
300 Hz	0.5 kHz	5 kHz	0.01 sec. **	0.05 sec.	0.5 sec.
300 Hz	1 kHz		0.01 sec.**	0,1 sec.	1 sec.
300 Hz	2 kHz	10 kHz	0.02 sec.	0.2 sec.	2 sec.
300 Hz		20 kHz	0.05 sec.	0.5 sec.	5 sec.
000 112	5 kHz	50 kHz	0.1 sec.	1 sec.	10 sec.

<sup>\*</sup>Slowest SWEEP TIME Available.

<sup>\*\*</sup>Fastest SWEEP TIME Available.

3-149. In many applications relatively wide portions of the spectrum being swept do not contain useful information. The plot shown in Figure 3-27, for example, has a number of narrow spectral components but more than 98% of the display is nothing but noise floor. Using a conventional sweep at the optimum sweep rate, it took more than 15 minutes to trace the plot shown in Figure 3-27. Using the 3580A Adaptive Sweep feature, however, the same plot (minus the noise floor) was traced in about 1.5 minutes (Figure 3-28).

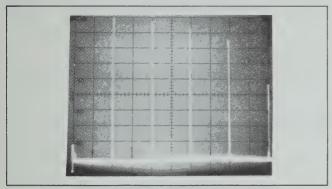


Figure 3-27. Plot Using Conventional Sweep (15 minutes).

3-150. To use the Adaptive Sweep feature, the operator sets a baseline threshold using the front panel ADAPTIVE SWEEP control. The baseline threshold can be adjusted anywhere from the bottom of the screen to approximately 70% of full scale. For the plot shown in Figure 3-28, the baseline threshold was set about 10 dB above the noise floor.

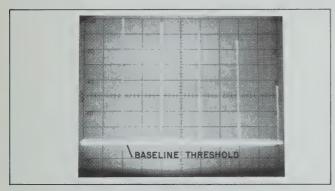


Figure 3-28. Plot Using Adaptive Sweep.

3-151. At the beginning of the Adaptive Sweep, the instrument sweeps at the rate selected by the SWEEP TIME setting. This ensures that the zero response or any other signal on or near the start frequency will be properly detected. After the sweep passes through any initial responses, the sweep rate is automatically increased to 20 or 25 times the selected rate. When the sweep reaches a response that rises above the baseline threshold, it backs up slightly, pauses to allow the IF Filter to settle and then sweeps slowly over the response at the panel-selected rate. When the response has been completely traced, the sweep is again speeded up until another response is encountered. As a result, the portions of the spectrum below the threshold

level are not displayed, but the spectral responses above the threshold level are displayed just as they are using a conventional sweep. By sweeping rapidly through unused portions of the spectrum, the Adaptive Sweep greatly reduces the overall measurement time.

**3-152. Setting the Baseline Threshold.** When setting the baseline threshold for the Adaptive Sweep, the following guidelines must be observed:

a. In the Linear amplitude mode the threshold must be at least 60% below the peak of the smallest signal to be displayed. For example, if the peak of the smallest signal to be displayed is 4 vertical divisions, the threshold must be at least 2.4 divisions (0.6 X 4) below it. Similarly, if the peak of the smallest signal to be displayed is 1 vertical division, the threshold must be at least 0.6 of a division below it.

b. In the Log amplitude mode, the threshold must be at least 8 dB below the peak of the smallest signal to be displayed.

3-153. The reason for setting the baseline threshold below the peak of the smallest signal to be displayed is that the responses are detected when the instrument is sweeping 20 to 25 times faster than the panel-selected rate. During these fast sweeps the IF Filter does not have time to fully respond. As a result, the signals applied to the internal threshold detector are about 6 dB (50%) lower in amplitude than they are when sweeping at the optimum rate. If the threshold is not more than 6 dB below the peak of a signal, that signal will not be detected and consequently, will not be displayed.

### NOTE

Adaptive Sweep cannot be used on the 0.05 Sec., 0.02 Sec. and 0.01 Sec. SWEEP TIME settings.

3-154. With the SWEEP TIME control set to one position slower than the optimum rate, the signal compression during fast sweeps is approximately 3 dB or 30%. This allows the baseline threshold to be set 4 dB or 45% below the peak of the smallest signal to be displayed. The trade off here is that the measurement time is considerably longer than it is when sweeping at the optimum rate.

3-155. Adaptive Sweep, Log 1 dB Mode. The Adaptive Sweep is difficult to use in the Log 1 dB amplitude mode. This is because the display range is only 10 dB and, when sweeping at the optimum rate, the baseline threshold must be at least 8 dB below the peak of the smallest signal to be displayed. With the baseline threshold at the bottom of the screen, signals more than 2 dB below full scale will not be displayed. If the Adaptive Sweep is to be used in the Log 1 dB mode, set the SWEEP TIME control one or two positions slower than the optimum sweep rate. This will reduce the amplitude compression during fast sweeps and

allow at least 50% of the display range to be used. If the Adaptive Sweep is not to be used in the Log mode, be sure the ADAPTIVE SWEEP control is in the OFF position.

**3-156.** Adaptive Sweep Marker. When the ADAPTIVE SWEEP control is set to the ON position, a sweep marker appears on the display. The sweep marker is a blank spot or gap in the trace that indicates the position of the frequency sweep. The sweep marker is provided because the digital memory that generates the display does not track the fast-forward and fast-backward excursions of the Adaptive Sweep. The sweep marker enables the operator to observe these excursions, making it easy to verify that the Adaptive Sweep is operating properly.

3-157. In some cases it may be desirable to display the sweep marker without using the Adaptive Sweep. This can be done in the Linear and Log 10 dB modes by setting the ADAPTIVE SWEEP control to the ON position and leaving the baseline threshold at the bottom of the display. With the baseline threshold at the bottom of the display, the video level exceeds the threshold level causing the instrument to continually sweep at the panel-selected rate.

### 3-158. Digitally-Stored Display.

3-159. A unique feature of the 3580A is its digitally-stored display. The digitally-stored display provides a number of unusual operating conveniences. For example, display adjustments are not required when the sweep parameters are changed. The digitally-stored trace is automatically cleared and updated at the correct rate. The INTENSITY and FOCUS controls have the same effect as those of a regular oscilloscope. Once they are set, they do not need to be readjusted. Moreover, the INTENSITY control can be set to any level without danger of burning the CRT face. Digital storage provides a bright, crips, flicker-free presentation. There is no blooming of display ambiguity.

3-160. One of the major advantages of digital storage is its ability to retain a trace indefinitely, i.e., as long as power is applied to the instrument. When a single sweep is made, the trace that is generated will continue to be displayed until the CLEAR WRITE button is pressed or until it is replaced by a new sweep. If a trace is needed for future reference, it can be permanently stored in memory by simply pressing the STORE pushbutton. The "stored trace" and a current or "refresh trace" can then be displayed simultaneously (Figure 3-29). If desired, the stored trace can be blanked from the display by pressing the BLANK STORE button. Releasing the BLANK STORE button returns the stored trace to the display.

3-161. A permanently stored trace is not effected by changing the control settings or by pressing the CLEAR WRITE button. The only way the stored trace can be cleared from memory is by releasing the STORE button or turning the power off. When the STORE button is initially released, the stored trace disappears and a series of dots appear on the display (Figure 3-30). The dots are automatically cleared when the display is updated by a new sweep.

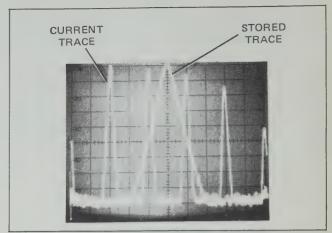


Figure 3-29. Stored Trace and Current Trace Displayed Simultaneously.

3-162. Reduced Resolution. The digital memory in the 3580A has 1024 addresses where the Y-axis amplitude information is stored. When the STORE button is not pressed, each address corresponds to a given position of the frequency sweep and the X-axis of the display is divided into 1024 discreet segments. When the STORE button is pressed, the memory is split in half. One half (512 addresses) is used for the stored trace and the other half is for the refresh trace. Since only 512 addresses are used for each trace, the display resolution is decreased. This means that the display is not quite as detailed as it is with a single trace stored in 1024 addresses. The techniques used for storing information and splitting the memory are such that the peaks of the responses are always retained. Thus, the reduced resolution does not normally obscure any useful information.

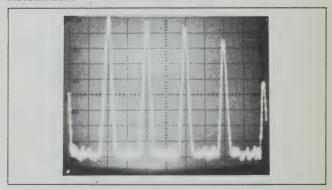


Figure 3-30. Store Button Released.

### 3-163. Recorder Outputs.

3-164. Recorder outputs are provided on the rear panel of the 3580A to permit the use of an external X-Y recorder/plotter. The -hp- Model 7035B Option 020 X-Y Recorder is recommended. Although the Standard Model 7035B and other X-Y recorders can be used, the 7035B Option 020 is preferable because it has some special features that simplify scale calibration. In addition, the Model 7035B Option 020 is equipped with an X-axis log converter which can be used to scale the 3580A linear sweep to obtain a full log sweep over a 3-decade (10 Hz to 10 kHz) range.

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**3-165.** X-Axis Output. The X-AXIS output supplies a dc voltage proportional to the position of the frequency sweep on the CRT display. When the sweep is at the start frequency, the X-Axis output is 0 V dc; when the sweep is at the end frequency, the output is + 5 V dc. The output resistance is 1 Kilohm, nominal.

3-166. In the Repetitive and Single sweep modes when Adaptive Sweep is not used, the X-Axis output is a 0 V to +5 V linear ramp. When Adaptive Sweep is used, the output voltage tracks the forward and reverse excursions of the sweep. In the Manual sweep mode, the X-Axis output voltage corresponds to the sweep position set by the MANUAL VERNIER control. When the Reset or Log Zero mode is selected, the X-Axis output remains at 0 V dc.

3-167. In the Log sweep mode, the frequency is swept logarithmically but the X-Axis output is still a 0 V to +5 V *linear* ramp. An output of 0 V dc corresponds to the 20 Hz start frequency, an output of +2.5 V dc corresponds to 982 Hz at the center of the display and an output of +5 V dc corresponds to the 43 kHz end frequency.

### **NOTE**

Because of the relatively fast sweep rates used in the Log sweep mode, conventional X-Y recorders connected to the X-AXIS output cannot respond properly. To make amplitude vs. log-frequency recordings, use an X-Y recorder that has a built-in log converter for the X-axis input (-hp- 7035B Opt. 020). Connect the 3580A X-AXIS output to the X-axis input of the recorder. With the recorder set to the log mode, sweep the 3580A at a slow linear rate using the Single or Repetitive sweep mode.

**3-168.** Y-Axis Output. The Y-AXIS output supplies a dc voltage proportional to the amplitude of the responses appearing on the display. An output of 0 V dc corresponds to the bottom of the screen; an output of +5 V dc corresponds to the top of the screen. The Y-Axis output voltage is 0.5 V per division in the Linear amplitude mode, 0.05 V per dB in the Log 10 dB mode and 0.5 V per dB in the Log 1 dB mode. Output resistance is 1 kilohm nominal.

3-169. There are several things about the Y-AXIS output that should be noted:

a. In the Log 10 dB mode, rotating the AMPLITUDE REF LEVEL control in a clockwise direction offsets the display in steps of 10 dB. This also offsets the Y-Axis output in steps of + 0.5 V.

b. In the Log 1 dB mode, the display ranges from 0 dB (+5 V) to -10 dB (0 V). The Y-Axis output, however, extends from approximately +1 dB (+5.5 V) to -13 dB (-1.5 V).

c. Changing the baseline threshold using the ADAP-TIVE SWEEP control does not effect the Y-Axis output voltage.

**3-170.** Pen Lift Output. The PEN LIFT output is provided for use with X-Y recorders having electrically operated pen lift circuits that allow the pen to be remotely actuated by a contact closure. The PEN LIFT output is operative only in the Single sweep mode. If Adaptive Sweep is not used, a contact closure is present between the PEN LIFT output terminals for the duration of the single sweep. If Adaptive Sweep is used, the contact closure is present only when the instrument is sweeping slowly over a response. This prevents the fast-forward and fast-backward excursions of the sweep from being recorded. The PEN LIFT output terminals are isolated from case ground. Do not use clear-write to reset sweep.

### 3-171. Tracking Oscillator Output.

3-172. The rear panel TRACKING OSC OUT connector supplies a 5 Hz to 50 kHz sinusodial output signal that tracks the tuned or swept frequency of the instrument. The specified frequency response of the tracking oscillator output signal is  $\pm$  3% over the 5 Hz to 50 kHz frequency range. Total harmonic distortion and spurious is more than 40 dB below a 1 V rms signal level. The output impedance is 600 ohms, nominal. When the output is terminated in 600 ohms, the LEVEL control may be used to adjust the output from 0 V to 1 V rms.

3-173. The frequency accuracy of the tracking oscillator output signal is specified at ± 2.5 Hz relative to the center of the instrument's passband. On the 1 Hz and 3 Hz bandwidths, the passband is less than 2.5 Hz above and below the center frequency. Thus, the tracking oscillator output frequency may be slightly outside of the passband. This is of little consequence except during closed-loop measurements where the tracking oscillator signal is fed into the INPUT through a network under test. If the tracking oscillator frequency is outside the passband, insertion loss will be encountered. Under worst case conditions, maximum insertion loss is approximately 30 dB on the 1 Hz bandwidth and 8 dB on the 3 Hz bandwidth. Typically, the insertion loss is about 5 dB on the 1 Hz bandwidth and 2 dB on the 3 Hz bandwidth.

3-174. For most closed-loop measurements optimum results will be obtained using the 10 Hz or 30 Hz bandwidth. If, for some reason, the 1 Hz or 3 Hz bandwidth is used, insertion loss can be minimized by removing the top cover and adjusting A2C4 (100 kHz ADJ) so that the tracking oscillator frequency is in the center of the passband. An alternative approach is to apply an external reference signal to the TRACKING OSC IN connector and adjust the frequency of the reference so that the tracking oscillator frequency is in the center of the passband (see Paragraph 3-176).

### 3-175. Tracking Oscillator Input.

3-176. Figure 3-31 is a simplified block diagram of the Tracking Oscillator circuit. With the rear panel slide switch in the NORMAL position, the 100 kHz to 150 kHz signal from the VTO is mixed with a 100 kHz signal from a Crystal Oscillator. The 0 Hz to 50 kHz difference frequency is fed through a 50 kHz Low-Pass Filter and applied to the TRACKING OSC OUT connector. With the slide switch in the EXT REF position, the 100 kHz Crystal Oscillator is disconnected and an external reference signal can be applied to the Mixer through the TRACKING OSC IN connector. The frequency of the external reference signal can be varied about 100 kHz to offset or frequency modulate the tracking oscillator output signal. Increasing the frequency of the external reference signal decreases the tracking oscillator output frequency; decreasing the external reference frequency increases the tracking oscillator output frequency.

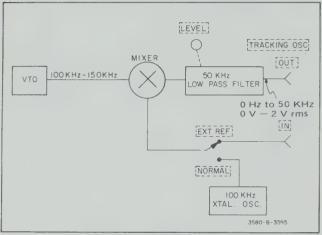


Figure 3-31. Tracking Oscillator.

3-177. The signal level applied to the TRACKING OSC IN connector should be 100 mV rms  $\pm$  10%. Use a highly stable signal source such as an -hp- Model 3320A/B or 3330A/B Frequency Synthesizer. The impedance of the tracking oscillator input is approximately 3.6 kilohms.

### 3-178. L.O. Output.

3-179. The VTO in the 3580A generates a 1 MHz signal which is divided in frequency to obtain the 100 kHz to 150 kHz VTO signal that is applied to the Input Mixer and Tracking Oscillator. The 1 MHz to 1.5 MHz signal from the VTO is available at the rear panel LO OUTPUT connector. The signal level at the LO OUTPUT is 100 mV rms; output impedance is 1 kilohm, nominal.

3-180. The tuned frequency of the instrument can be measured to an accuracy of  $\pm$  5 Hz with an electronic frequency counter connected to the LO OUTPUT. The following formula can be used to calculate the tuned frequency from the counter reading:

$$F_t = \frac{F_c}{10} - 100 \text{ kHz}$$

Where:

 $F_t$  = tuned frequency  $F_c$  = counter reading

3-181. The tuned frequency of the instrument can be measured using either the L.O. Output or the Tracking Oscillator Output. It is generally preferable to use the L.O. Output because it provides greater frequency resolution. Also, the L.O. Output frequency can be measured using a 0.1 second gate time for fast response.

### 3-182. Option 001.

3-183. The 3580A Option 001 is equipped with an internal rechargeable battery pack and a protective front panel cover for complete portability.

### WARNING

To protect operating personnel, the 3580A Option 001 chassis must be grounded. For power line operation connect the power cord to a three-prong grounded receptacle. For battery operation connect the common (black) input terminal to earth ground or to an appropriate system ground. If a system ground is used be sure it is actually at ground potential and is not a voltage source.

3-184. The 3580A Option 001 can be operated from the ac power line or from its own internal battery pack. With the POWER switch set to the ON (AC) position, the instrument receives its power from the ac power line and a trickle charge is applied to the batteries. The trickle charge prevents the batteries from discharging, but is not sufficient to recharge the batteries in a reasonable time. With the POWER switch in the ON (BAT) position, the ac power is turned off and the instrument receives its power solely from the internal battery pack. A fully charged battery pack will operate the instrument for more than 5 hours. When the batteries are discharged to the point where they cannot operate the instrument properly, the power is automatically shut off. This eliminates erroneous measurements caused by weak batteries and further prevents the batteries from being damaged due to excessive discharge.

3-185. To recharge the batteries, connect the instrument to an appropriate ac power source and set the POWER switch to the CHARGE position. The POWER light will illuminate. The instrument cannot be operated while the batteries are being charged. Recharge time for completely discharged batteries is 14 hours. The useful life of the batteries is more than 100 charge/discharge cycles.

## ECAUTION 3

The instrument should not be left in the CHARGE mode for prolonged periods. A charge period of 14 hours is sufficient to recharge a fully discharged battery pack. Extended periods of overcharge in ambient temperatures exceeding 30°C (86°F) will severely degrade battery life and capacity by causing the cells to overheat.

- **3-186.** Temperature Limits. To prevent battery damage, the following temperature limits must be observed:
- a. Operating Temperature:  $0^{\circ}$ C (+ 32°F) to + 40°C (+ 104°F)
- b. Charge Temperature Range:  $0^{\circ}$ C (+  $32^{\circ}$ F) to +  $40^{\circ}$ C (+  $104^{\circ}$ F)
- c. Storage Temperature Range:  $-40^{\circ}$ C ( $-40^{\circ}$ F) to  $+50^{\circ}$ C ( $+122^{\circ}$ F)

### 3-187. Option 002.

3-188. The 3580A Option 002 is equipped with a front panel slide switch which permits selection of three input configurations: Unbalanced, Balanced Bridged, and Balanced Terminated. These input configurations are illustrated in Figure 3-32. In addition, the 3580A Option 002 TRACKING OSC OUT is transformer coupled to provide a 600-ohm balanced output configuration.



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The differential signal level applied to the Option 002 Balanced Terminated input must not exceed + 27 dBm at 0 V dc. The combined ac and dc levels must be such that the power dissipated by the terminating resistor is less than 0.5 watt.

3-189. The 3580A Option 002 can be calibrated for absolute measurements in rms volts, dBm/600 ohms or

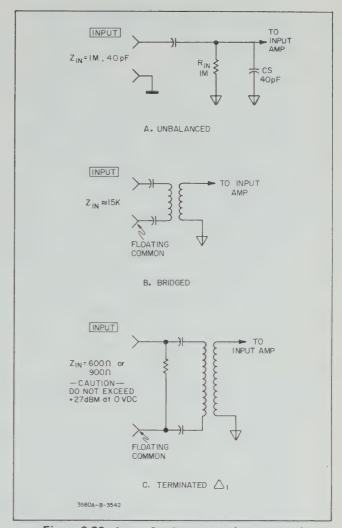


Figure 3-32. Input Configurations (Option 002).

dBm/900 ohms. The selection is made using the front panel dBm 900 ohm/LIN - - dBm 600 ohm slide switch. Relative measurements can be made in dB or percent of full scale.

3-190 It should be noted that in the unbalanced input configuration, the input shunt capacitance is 40 pF, nominal. This differs from the 30 pF shunt capacitance of the standard Model 3580A. If a 10:1 divider probe is used, it must have sufficient adjustment range to compensate for the 40 pF shunt capacitance. An -hp- Model 10003A Voltage Divider Probe is recommended.

### 3-191. BASIC OPERATING PROCEDURES.

### 3-192. Instrument Turn On.

### 3-193. Power Line Operation.

- a. Check the line voltage at the point of installation.
- b. Refer to Figure 3-33 and set the 3580A for the line voltage to be used (100 V, 120 V, 220 V or 240 V). Line voltage must be within +5% to -10% of voltage setting.
- c. Verify that the proper fuse is installed in the fuse holder:

Line Setting	Fuse Type	-hp- Part No.
100 V/120 V	0.5 A, 250 V Normal Blow	2110-0012
220 V/240 V	0.25 A, 250 V Normal Blow	2110-0004

- d. Connect the detachable ac power cord to the rear panel power receptacle and to the power source.
- e. Set the POWER switch to the ON (AC) position. The POWER light will illuminate.

- f. Allow approximately 15 seconds for the CRT to warm up. Adjust the INTENSITY and FOCUS controls for a bright, clear presentation on the CRT. When the instrument is initially turned on, the display may be similar to the one shown in Figure 3-34. This display reflects the preferred states of the storage elements in the digital memory and is not meaningful. To clear the display, press the CLEAR WRITE button.
- g. Allow a warm-up period of at least 1 hour before using the 3580A in a critical measurement application.

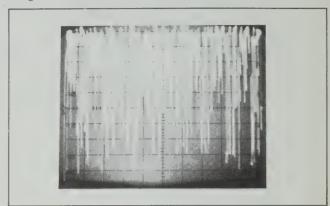


Figure 3-34. Typical Turn On Display.

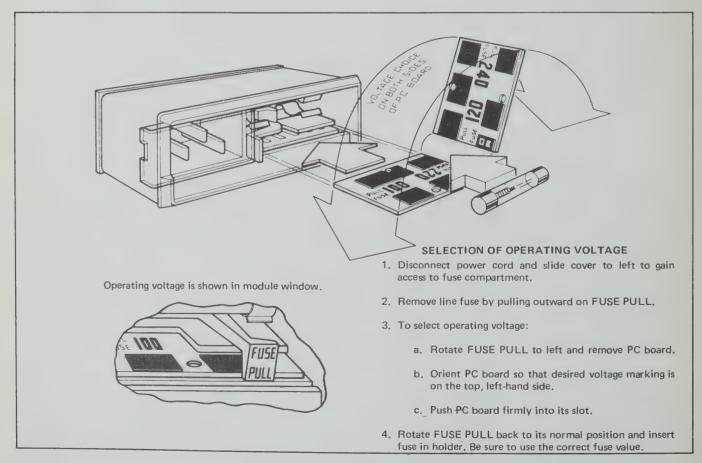


Figure 3-33. Voltage Selection.

### 3-194. Battery Operation (Option 001).

- a. Connect the low (black) terminal of the front panel INPUT connector to earth ground or to an appropriate system ground.
- b. Set the POWER switch to the ON (BAT) position. The POWER light will illuminate.
- c. Allow approximately 15 seconds for the CRT to warm up. Adjust the INTENSITY and FOCUS controls for a bright, clear presentation on the CRT. When the instrument is initially turned on, the display may be similar to the one shown in Figure 3-34. This display reflects the preferred states of the storage elements in the digital memory and is not meaningful. To clear the display, press the CLEAR WRITE button.
- d. Allow a warm-up period of at least 1 hour before using the 3580A in a critical measurement application.
- e. To recharge the batteries, perform Steps a through d of the power-line turn on procedure (Paragraph 3-193). Set the POWER switch to the CHARGE position. The POWER light will illuminate. The instrument cannot be used while the batteries are being charged.

## ECAUTION 3

The instrument should not be left in the CHARGE mode for prolonged periods. A charge period of 14 hours is sufficient to recharge a fully discharged battery pack. Extended periods of overcharge in ambient temperatures exceeding 30°C (86°F) will severely degrade battery life and capacity by causing the cells to overheat.

#### 3-195. Frequency Calibration Procedure.

3-196. The Frequency Calibration Procedure should be performed after warm-up each time the instrument is turned on. It should also be performed before and after using the log sweep.

3-197. For operation in the Repetitive, Single or Manual sweep mode, proceed as follows:

- a. Turn the instrument on as outlined in Paragraph 3-192.
- b. Set the 3580A controls as follows:

ADAPTIVE SWEEP	OFF
DISPLAY STORE and BLANK ST	ORE
Rele	eased
AMPLITUDE MODE LOG 10 dB	/DIV
AMPLITUDE REF LEVELNOR	MAL
- dBv/LIN - dBm SwitchdBv	/LIN

INPUT SENSITIVITYCAL
VERNIERCAL
(Fully CW)
FREQUENCY 00.0 kHz
START CTRSTART
BANDWIDTH30 Hz
DISPLAY SMOOTHINGMIN
FREQ. SPAN/DIV N/A
SWEEP TIME/DIVN/A
SWEEP MODE

c. Clear the display by pressing the CLEAR WRITE button.

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d. Adjust the front panel ZERO CAL potentiometer for peak zero response. (The zero response will appear on the first line on the left-hand side of the display graticule.)

P3 (e. Set the BANDWIDTH to 10 Hz. Repeat Step d.

3-198. For operation in the Log sweep mode, proceed as follows:

a. Set the 3580A controls as follows:

DISPLAY STORE and BLANK STORE Released AMPLITUDE MODE LOG 10 dB/DIV AMPLITUDE REF LEVEL NORMAL dBv/LIN - dBm Switch dBv/LIN INPUT SENSITIVITY CAL VERNIER CAL (Fully CW) FREQUENCY N/A START CTR N/A BANDWIDTH 30 Hz DISPLAY SMOOTHING MIN FREQ. SPAN/DIV N/A SWEEP TIME/DIV N/A SWEEP MODE LOG ZERO	ADAPTIVE SWEEP	OFF
AMPLITUDE MODE LOG 10 dB/DIV AMPLITUDE REF LEVEL NORMAL dBv/LIN - dBm Switch dBv/LIN INPUT SENSITIVITY CAL VERNIER CAL (Fully CW) FREQUENCY N/A START CTR N/A BANDWIDTH 30 Hz DISPLAY SMOOTHING MIN FREQ. SPAN/DIV N/A SWEEP TIME/DIV N/A	DISPLAYSTORE and	d BLANK STORE
AMPLITUDE REF LEVEL NORMAL dBv/LIN - dBm Switch dBv/LIN INPUT SENSITIVITY CAL VERNIER CAL (Fully CW) FREQUENCY N/A START CTR N/A BANDWIDTH 30 Hz DISPLAY SMOOTHING MIN FREQ. SPAN/DIV N/A SWEEP TIME/DIV N/A		Released
dBv/LIN - dBm Switch dBv/LIN INPUT SENSITIVITY CAL VERNIER CAL (Fully CW) FREQUENCY N/A START CTR N/A BANDWIDTH 30 Hz DISPLAY SMOOTHING MIN FREQ. SPAN/DIV N/A SWEEP TIME/DIV N/A	AMPLITUDE MODE	LOG 10 dB/DIV
INPUT SENSITIVITY CAL VERNIER CAL (Fully CW) FREQUENCY N/A START CTR N/A BANDWIDTH 30 Hz DISPLAY SMOOTHING MIN FREQ. SPAN/DIV N/A SWEEP TIME/DIV N/A	AMPLITUDE REF LEVEL .	NORMAL
VERNIER CAL (Fully CW) FREQUENCY N/A START CTR N/A BANDWIDTH 30 Hz DISPLAY SMOOTHING MIN FREQ. SPAN/DIV N/A SWEEP TIME/DIV N/A	dBv/LIN - dBm Switch	dBv/LIN
FREQUENCY N/A START CTR N/A BANDWIDTH 30 Hz DISPLAY SMOOTHING MIN FREQ. SPAN/DIV N/A SWEEP TIME/DIV N/A	INPUT SENSITIVITY	CAL
FREQUENCY N/A START CTR N/A BANDWIDTH 30 Hz DISPLAY SMOOTHING MIN FREQ. SPAN/DIV N/A SWEEP TIME/DIV N/A	VERNIER	
START CTR N/A BANDWIDTH 30 Hz DISPLAY SMOOTHING MIN FREQ. SPAN/DIV N/A SWEEP TIME/DIV N/A		(Fully CW)
BANDWIDTH		
DISPLAY SMOOTHING		
FREQ. SPAN/DIV N/A SWEEP TIME/DIV N/A	BANDWIDTH	30 Hz
SWEEP TIME/DIVN/A	DISPLAY SMOOTHING	MIN
SWEEP MODE LOG ZERO	SWEEP TIME/DIV	N/A
	SWEEP MODE	LOG ZERO

b. Clear the display by pressing the CLEAR WRITE button.

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c. Adjust the front panel ZERO CAL potentiometer for peak zero response. (The zero response will appear on the first line on the left-hand side of the display graticule.)

### 3-199. Amplitude Calibration Procedure.

3-200. The Amplitude Calibration Procedure should be performed initially after warm-up and each time the BANDWIDTH setting is changed.

3-201. For operation on the 1 Hz or 3 Hz BANDWIDTH, proceed as follows:

a. Turn the instrument on (Paragraph 3-192) and perform the Frequency Calibration Procedure (Paragraph 3-195).

b. Set the 3580A controls as follows:

DISPLAYSTORE and BLANK STORE
Released
AMPLITUDE MODE LOG 10 dB/DIV
AMPLITUDE REF LEVELNORMAL
dBv/LIN - dBm SwitchdBv/LIN
INPUT SENSITIVITYCAL
VERNIER
(Fully CW)
FREQUENCY 10.0 kHz
START CTRCTR
BANDWIDTH 1 Hz or 3 Hz
(whichever is to be used)
DISPLAY SMOOTHINGMIN
FREQ. SPAN/DIV 0 Hz
SWEEP TIME/DIVN/A
SWEEP MODE MAN

- c. Turn the ADAPTIVE SWEEP control to the on position so the sweep marker (gap) appears on the horizontal trace. Leave the baseline threshold at the bottom of the screen.
- d. While pressing the CLEAR WRITE button, adjust the MANUAL VERNIER so that the sweep marker is in the center of the display. Release the CLEAR WRITE button and set the ADAPTIVE SWEEP control to the OFF position.
- e. Pull out on the FREQUENCY control for fine tuning. Carefully adjust the FREQUENCY control for a peak 10 kHz response in the center of the display.
- f. Using a small screwdriver, adjust the front panel <u>CAL</u> 10 KHz potentiometer so that the peak of the 10 kHz response is exactly full scale.
- g. Set the AMPLITUDE MODE to LOG 1 dB/DIV. Repeat Step f.

### 3-202. For operation on the 10 Hz, 30 Hz, 100 Hz or 300 Hz BANDWIDTH proceed as follows:

- a. Turn the instrument on (Paragraph 3-192) and perform the Frequency Calibration Procedure (Paragraph 3-195).
  - b. Set the 3580A controls as follows:

DISPLAYSTORE and BLANK STORE
Released
AMPLITUDE MODE LOG 10 dB/DIV
AMPLITUDE REF LEVELNORMAL
dBv/LIN - dBm SwitchdBv/LIN
INPUT SENSITIVITY
VERNIERCAL
(Fully CW)
FREQUENCY 10.0 kHz
START CTRCTR
BANDWIDTH 10 Hz-300 Hz
(whichever is to be used)

DISPLAY SMOOTHING	MIN
FREQ. SPAN/DIV See Table	3-6
SWEEP TIME/DIV See Table	
SWEEP MODE I	REP

c. Using the ADAPTIVE SWEEP control, set the baseline threshold to - 60 dB on the display.

d. Using a small screwdriver, adjust the front panel CAL 10 KHz potentiometer so that the peak of the 10 kHz response is exactly full scale.

e. Set the AMPLITUDE MODE to LOG 1 dB/DIV. Using the ADAPTIVE SWEEP control, set the baseline threshold to the bottom of the display. Repeat Step d.

Table 3-6. Control Settings.
(Amplitude Calibration)

BANDWIDTH SETTING	FREQ SPAN/DIV	SWEEP TIME/DIV
10 Hz	20 Hz	0.5 SEC
30 Hz	0.1 kHz	0.2 SEC
100 Hz	0.5 kHz	0.1 SEC
300 Hz	1 kHz	0.02 SEC

### 3-203. Input Probe Compensation.

3-204. Before using a 10:1 voltage divider probe it is necessary to adjust the probe for optimum frequency response. Once the probe is properly adjusted, it should not require further attention. It is good practice, however, to perform periodic verification tests to ensure that optimum adjustment is maintained.

- a. Turn the instrument on as outlined in Paragraph 3-192.
- b. Connect the probe to the 3580A INPUT using a BNC to bananna-plug adapter (-hp- Part No. 1251-2277).
  - c. Set the 3580A controls as follows:

ADAPTIVE SWEEP OFF
DISPLAYSTORE and BLANK STORE
Released
AMPLITUDE MODELOG 10 dB/DIV
AMPLITUDE REF LEVELNORMAL
INPUT SENSITIVITY 10 dB
FREQUENCY 00.0 kHz
START CTRSTART
BANDWIDTH 300 Hz
DISPLAY SMOOTHINGMIN
FREQ. SPAN/DIV
SWEEP TIME/DIV0.05 SEC
SWEEP MODE REP

d. Set the rear panel LEVEL control fully clockwise (facing rear panel).

- e. Connect the probe tip to the rear panel TRACKING OSC OUT connector. Connect the ground lead of the probe to case ground.
- f. Adjust the front panel amplitude VERNIER so that the horizontal trace is between 0 dB and 10 dB on the display.
  - g. Set the AMPLITUDE MODE to LOG 1 dB/DIV.
- h. Adjust the probe so that its response is flat over the entire frequency range (Figure 3-35).

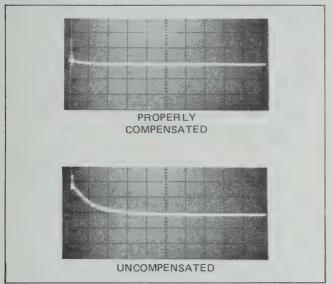


Figure 3-35. Probe Compensation.

### 3-205. Familiarization Exercise.

3-206. The following procedure demonstrates the Digital Storage, Adaptive Sweep and other operating features of the 3580A.

- a. Turn the instrument on as outlined in Paragraph 3-92. Perform the Frequency Calibration Procedure (Paragraph 3-195) and the Amplitude Calibration Procedure (Paragraph 3-199). Perform the amplitude calibration using the 100 Hz BANDWIDTH and the LOG 10 dB AMPLITUDE MODE.
  - b. Set the 3580A controls as follows:

ADAPTIVE SWEEPOFF
DISPLAYSTORE and BLANK STORE
Released
AMPLITUDE MODE LOG 10 dB/DIV
AMPLITUDE REF LEVELNORMAL
dBv/LIN - dBm SwitchdBv/LIN
INPUT SENSITIVITYCAL
VERNIERCAL
(Fully CW)
FREQUENCY 00.0 kHz
START CTRSTART
BANDWIDTH 100 Hz

DISPLAY SMOOTHING					MIN
FREQ. SPAN/DIV			a	5	KHz
SWEEP TIME/DIV		, 0		1	SEC
SWEEP MODE					<b>REP</b>

- c. The spectral components of the 10 kHz calibration signal will now appear on the display. If the instrument is properly calibrated, the peak of the 10 kHz fundamental frequency component will be at full scale and the zero response will coincide with the first line on the left-hand side of the display graticule.
- d. Set the BANDWIDTH switch to the 30 Hz position. The ADJUST light will illuminate to indicate that the sweep rate is too fast. As the trace is updated by a new sweep, the amplitudes of the various frequency components will be compressed because the IF Filter does not have time to fully respond.
- e. Rotate the SWEEP TIME control counterclockwise until the ADJUST light goes out (10 SEC). When the ADJUST light goes out, the instrument is sweeping at the optimum rate.
- f. Set the SWEEP MODE switch to the SING (Single) position. Press and release the CLEAR WRITE button. This will clear the display and initiate a new sweep. Allow 100 seconds for the display to be updated. The trace generated by the single sweep will continue to be displayed until it is cleared or replaced by a new sweep.
- g. Press the STORE button and then press the BLANK STORE button. The trace currently being displayed is now permanently stored in memory and can be recalled at any time by releasing the BLANK STORE button.
- h. Using the ADAPTIVE SWEEP control, set the baseline threshold about 10 dB above the noise floor.
- i. Press and release the CLEAR WRITE button to initiate a new sweep. Observe the fast and slow excursions of the Adaptive Sweep. Note that the penlift relay clicks each time the instrument begins to sweep slowly over a response. The Adaptive Sweep takes only about 15 seconds to trace the plot that previously took 100 seconds.
- j. Set the ADAPTIVE SWEEP control to the OFF position. Release the BLANK STORE button to compare the 15 second trace and the 100 second trace. The two traces will be identical except the 15 second trace obtained using the Adaptive Sweep will not have a noise floor. Again press the BLANK STORE button. The permanently stored trace will disappear.
- k. Set the SWEEP MODE switch to the REP (Repetitive) position.
- 1. To examine the 20 kHz frequency component in greater detail, set the START/CTR switch to CTR, set the FREQ. SPAN/DIV to 0.5 KHz and set the SWEEP TIME/DIV to 1 SEC. At this point, the center of the display is

0 Hz and the negative frequencies on the left-hand side of 0 Hz are blanked. Set the FREQUENCY dial to 20.0 kHz. When the trace is updated by a new sweep, the 20 kHz frequency component will appear in the center of the display.

- m. Set the BANDWIDTH switch to 300 Hz. This will make the 20 kHz component wider because the analyzer's response to a CW signal is an amplitude vs. frequency plot of the IF Filter.
- n. Release the BLANK STORE button. The permanently stored trace will reappear on the display. Even though the sweep parameters have been changed, the stored trace appears exactly as it did when the STORE button was initially pressed.
- o. Set the FREQ. SPAN/DIV to 5 KHz and allow 10 seconds for the display to be updated.
- p. Release the STORE button. The previously stored trace will disappear and a series of dots will appear on the current trace. The dots will be cleared when the display is updated by a new sweep.

### 3-207. Technique For Measuring Noise.

3-208. The 3580A uses peak detection on the swept spectrum. Therefore, the noise displayed is peak noise and can be several dB higher than average noise. Average noise measurements can be made if the following technique is used:

- a. Use display smoothing.
- b. Ignoring the adjust warning light, decrease Sweep Time/Div until the displayed noise level no longer

decreases. The spectrum shape of the noise should be gradually changing, not abrupt, allowing the spectrum analyzer to follow it well.

3-209. Average Detection Error. The video detector is an average responding full wave detector. This type of detector has an inherent error when detecting noise. In the 3580A, the error occurs in both the linear and log modes of operation. To correct for this error, multiply the displayed reading by 1.128 to get the rms value.

**3-210.** Log Conversion Error. In the log mode of operation, an additional correction must be made to compensate for log conversion error. Add 1.5 dB to the corrected display reading.

#### NOTES

- 1. Only "Gently" varying noise spectra can be accurately measured using this technique. Accurate measurement of both discrete lines and noise levels in the same spectrum is not generally possible.
- 2. To calculate the equivalent noise bandwidth, multiply the 3 dB bandwidth by 1.12. Remember that the 3 dB bandwidth has a tolerance of ± 15% and therefore should be measured if accurate results are desired.
- 3. The recorder Y Axis output is linear and continuous. Noise measurements can be made by connecting a true rms reading voltmeter to this output. See Paragraph 3-168 for operating information concerning the Y Axis output. The use of an X-Y recorder may also prove beneficial in making noise measurements.

Model 3580A Section IV

# SECTION IV THEORY OF OPERATION

### 4-1. INTRODUCTION.

4-2. This section contains a Simplified Block Diagram Description and a Functional Description of the 3580A Spectrum Analyzer.

### 4.3. SIMPLIFIED BLOCK DIAGRAM DESCRIPTION.

4-4. Refer to the Simplified Block Diagram (Figure 4-1) for the following discussion.

The 3580A can be divided into four major sections:

- 1) Amplitude Section
- 2) Frequency and Sweep Section
- 3) Digital Storage Section
- 4) Display

### 4-5. Amplitude Section.

- 4-6. The Amplitude Section consists of an Input Circuit, an Overload Detector, an Input Mixer, an IF Filter, Log and Linear IF Amplifiers, a Video Detector, a Video Filter and a Video Output Circuit.
- **4-7. Input Circuits.** The Input Circuits, controlled by the front panel INPUT SENSITIVITY switch, provide the gain or attenuation needed to maintain the proper signal level at the input of the Mixer. The Input Circuits also contain a 50 kHz low-pass filter which prevents image frequencies (200 kHz and above) from reaching the Mixer.
- **4-8. Overload Detector.** The Overload Detector at the input of the Mixer senses when the input level exceeds the design limits and, in turn, lights the front panel OVER-LOAD indicator. This is an important function since signals that overdrive the mixer can produce harmonic and spurious mixing products which ultimately appear on the display.
- **4-9. Input Mixer.** The Input Mixer is a double-balanced active mixer in which the 0 Hz to 50 kHz input signal is mixed with a 100 kHz to 150 kHz signal from the Voltage-Tuned Local Oscillator (VTO). The output of the Mixer is a composite signal containing the upper and lower side bands.
- 4-10. To select a given frequency component present at the input of the Mixer, the VTO frequency is tuned so that the difference between it and the frequency of interest is 100 kHz:

Fvto - Fin = 100 kHz Where:

> Fvto = 100 kHz to 150 kHz VTO frequency Fin = 0 Hz to 50 kHz input frequency

The 100 kHz intermediate frequency (IF) is fed through the IF Filter, detected and displayed on the CRT. Signals outside the passband of the IF Filter are rejected.

- **4-11. IF Filter.** The IF Filter contains five cascaded crystal filter stages. The center frequency of the filter is 100 kHz and the 3 dB bandwidth varies from 1 Hz to 300 Hz as a function of the front panel BANDWIDTH setting. Since the Input Circuits and Input Mixer are broadband through 50 kHz, the selectivity of the instrument is determined entirely by the bandwidth of the IF Filter.
- 4-12. Log and Linear Amplifiers. The 100 kHz output of the IF Filter is applied to the Video Detector through a Log Amplifier in the Log amplitude mode or through a Linear Amplifier in the Linear amplitude mode. The Log Amplifier converts the amplitude of the incoming IF signal to a logarithmic value, providing an 80 dB display range. The Linear Amplifier is a conventional amplifier circuit in which the gain is varied to provide the 20 V, 10 V, 2 V, 1 V ranging sequence used in the Linear mode. Also, the Linear Amplifier contains a variable attenuator which increases the overall gain as the AMPLITUDE REF LEVEL switch is changed from the X1 position.
- **4-13.** Video Detector. The Video Detector is an average-responding, full-wave detector circuit which produces a dc voltage proportional to the amplitude of the 100 kHz log or linear input signal.
- **4-14.** Video Filter. The Video Filter is an R/C filter network controlled by the BANDWIDTH and DISPLAY SMOOTHING controls. The purpose of the filter is to smooth-out the ripple and noise riding on the detected video signal.
- 4-15. Video Output Circuit. The Video Output Circuit functions as an output buffer in the Linear mode and as a variable gain amplifier in the Log 10 dB and Log 1 dB modes. In the Log 10 dB mode, a variable dc offset voltage, controlled by the AMPLITUDE REF LEVEL switch, is summed with the video input signal. This allows the entire display to be offset in steps of 10 dB as the AMPLITUDE REF LEVELsetting is changed from 0 dB to 70 dB. In the Log 1 dB mode, the gain of the Video Output Circuit is increased to provide an expanded scale of 1 dB per division. Changing the AMPLITUDE REF LEVEL setting then varies

the dc offset voltage to select any  $10\,dB$  portion of the  $80\,dB$  range. The output of the Video Output Circuit, ranging from  $0\,V$  to  $+\,5\,V$  dc, is applied to the rear panel Y-AXIS output connector and to the Digital Storage Section.

### 4-16. Frequency and Sweep Section.

- 4-17. The Frequency and Sweep Section consists basically of a Ramp Generator, a Dial Mixing Amplifier, a Voltage-Tuned Local Oscillator (VTO) and a Tracking Oscillator.
- **4-18.** Ramp Generator. The Ramp Generator produces a 0 V to +5 V linear ramp which is applied to the Dial Mixing Amplifier and to the Digital Storage Section. The frequency of the ramp is determined by the front panel SWEEP TIME setting. The FREQ SPAN control, located between the Ramp Generator and Dial Mixing Amplifier, determines the amplitude of the ramp applied to the VTO and thus, the overall change in frequency produced by the ramp.
- **4-19.** Dial Mixing Amplifier. In the Dial Mixing Amplifier, the ramp voltage is combined with a variable dc voltage from the front panel FREQUENCY control. This dc voltage establishes the low-frequency limit or "start frequency" of the VTO.
- **4-20. VTO.** The VTO generates a 100 kHz to 150 kHz square wave which is applied to the Input Mixer in the Amplitude Section and to the Tracking Oscillator.
- **4-21.** Tracking Oscillator. In the Tracking Oscillator, the 100 kHz to 150 kHz VTO signal is mixed with a 100 kHz signal from a crystal oscillator. This produces the 0 Hz to 50 kHz tracking signal which is available at the rear panel TRACKING OSC OUT connector.

### 4-22. Digital Storage Section.

- 4-23. Because of the extremely slow sweep rates used in the 3580A, some form of display storage is required. The most common method for obtaining display storage is to use a storage CRT in which the display is retained by the phosphor or by a "storage mesh" located behind the CRT face. Relatively recent advances in large-scale integrated circuits, however, have made it possible to use a digital storage technique in the 3580A. Digital storage permits the use of a standard oscilloscope CRT and further provides several operating conveniences not available with conventional displays.
- 4-24. In the Digital Storage Section, the 0 V to +5 V "frequency ramp" from the Frequency and Sweep Section is applied to an A to D converter where it is converted to binary and used to address a memory bank. At the same time, the detected video information from the Amplitude Section is converted to binary by an A to D converter and stored in the memory locations addressed by the ramp. The binary video data is then non-destructively read out of memory, converted to dc, processed and applied to the vertical deflection plates of the CRT.

- 4-25. During the read cycle, a "display ramp," generated in the Digital Storage Section, is used to address the memory and drive the horizontal deflection plates of the CRT. The display ramp scans the memory and sweeps the display approximately 50 times each second. This is a much faster rate than that of the frequency ramp used for storing data. The memory contents are, therefore, refreshed at the slow frequency-sweep rate, while data is read-out of memory at the rapid display-sweep rate. The result is a flicker-free, stored presentation.
- 4-26. When the front panel STORE button is pressed, the display currently in memory is processed and stored in one-half of the memory locations. This leaves the other half of the memory available for the refresh trace. During the read cycle, the display ramp first scans the memory locations containing the refresh trace. It then recycles and scans the locations containing the previously stored trace. Due to the rapid scan rate of the display ramp, the stored trace and the refresh trace appear simultaneously on the CRT.

### 4-27. FUNCTIONAL DESCRIPTION.

### 4-28. Amplitude Section.

- 4-29. Refer to the Amplitude Section Detailed Block Diagram (Figure 7-3) for the following discussion.
- 4-30. Input Attenuator. The Input Attenuator, controlled by the front panel INPUT SENSITIVITY switch, serves as an input voltage divider and coupling network between the INPUT connector and the Input Amplifier. The attenuator is comprised of 5 R/C divider networks. These networks provide the required signal attenuation for the +30 dB (20 V) through -10 dB (0.2 V) ranges. On the -20 dB (0.1 V) through -70 dB (0.2 mV) ranges, the Input Attenuator is bypassed by the Input Sensitivity switch and the input signal is applied directly to the Input Amplifier. Table 1 of the Detailed Block Diagram lists the maximum (full scale) input levels, input attenuation and resulting signal levels applied to the Input Amplifier for each INPUT SENSITIVITY setting.
- **4-31. Input Amplifier.** The Input Amplifier is a low noise, high input-impedance amplifier circuit which provides variable gain and impedance conversion between the Input Attenuator and the Post Attenuator. The Input Amplifier gain, controlled by the INPUT SENSITIVITY switch, is approximately X1.25 (+1.8 dB) on the +30 dB through -50 dB ranges and is increased to X12.5 (+21.8 dB) on the -60 dB and -70 dB ranges. Table 1 of the Detailed Block Diagram lists the full-scale input levels, Input Amplifier gain and full-scale output levels for each INPUT SENSITIVITY setting.
- 4-32. Post Attenuator. The Post Attenuator is a resistive divider network controlled by the INPUT SENSITIVTY switch and by the front panel slide switch that selects dBV/LIN or dBm/600 $\Omega$ . With the slide switch in the dBV/LIN position, the post attenuation is 5 dB or 15 dB. With the switch in the dBm/600 $\Omega$  position, the attenuation

Model 3580A Section IV

is - 2 dB or - 12 dB. Table 1 of the Detailed Block Diagram lists the full-scale input levels, post attenuation and output levels for each INPUT SENSITIVITY setting.

- **4-33.** Post Amplifier. The output of the Post Attenuator is applied to the Post Amplifier through the wiper of the front panel VERNIER potentiometer, R2. The Post Amplifier provides the final stage of gain and buffering before the signal is applied to the Input Mixer. The Post Amplifier gain, controlled by the INPUT SENSITIVITY switch, is approximately X4.5 (+13.2 dB) the +30 dB through 30 dB ranges and is increased to X45 (+33.2 dB) on the 40 dB through 70 dB ranges.
- 4-34. Table 1 of the Detailed Block Diagram lists the full-scale input levels, Post Amplifier gain and full-scale output levels for each INPUT SENSITIVITY setting. In the Log 10 dB or Log 1 dB amplitude mode, the normalized full-scale output of the Post Amplifier is 100 mV rms on all ranges. In the Linear mode, the full-scale output is 100 mV rms on the 10 V, 1 V, 0.1 V, etc. ranges and 62 mV rms on the 20 V, 2 V 0.2 V, etc. ranges. To compensate for this difference in full-scale levels in the Linear mode, the gain of the Linear Amplifier (following IF Section) is increased on the ranges having a lower output voltage.
- **4-35.** Overload Circuit. The Overload Circuit consists of an Overload Detector, an Overload Driver and an LED Overload Indicator. The Overload Detector is a full-wave peak detector designed to sense an over-voltage condition at the output of the Post Amplifier. During normal operation, the full-scale output of the Post Amplifier is 0.1 V rms or 0.14 V peak. If the signal level exceeds 0.14 V peak, the Overload Driver is gated on and the OVERLOAD indicator illuminates.
- 4-36. Note that the Overload Driver has one input labeled "Overload Inhibit." With the INPUT SENSITIVITY switch in the CAL position, -10 V dc is applied to the Overload Inhibit line causing the Overload Driver to remain cut off. This prevents the 10 kHz calibration signal from producing an OVERLOAD indication. The calibration signal is a pulse train in which the amplitude of the 10 kHz fundamental-frequency component is set to produce full-scale deflection on the CRT. Because of the rich harmonic content of the pulse train, its overall amplitude is slightly greater than 1.4 V peak.
- 4-37. Low-Pass Filter. To prevent image frequencies (200 kHz and above) from reaching the Input Mixer, the signal from the output of the Post Amplifier is fed through a 50 kHz Low-Pass Filter network. This "Cauer" filter is a 7-pole, passive, LCR filter network. The response of the filter is essentially flat over the 5 Hz to 50 kHz input frequency range. The filter provides 50 dB of rejection at 100 kHz and more than 90 dB of rejection at 200 kHz. Due to the 604-ohm series input resistance (R65) and the 604-ohm terminating resistance (R91), the filter introduces -6 dB of insertion loss. This makes the normalized full-scale input to the Mixer equal to 50 mV rms in the Log mode and 50 mV rms or 31 mV rms in the Linear mode.

- **4-38. Input Mixer.** The Input Mixer section consists of an active mixer (U2), a gain control circuit and an output buffer (Q14-Q16).
- 4-39. The mixer is a monolithic, double-balanced modulator circuit driven by a 0.8 V p-p, 100 kHz to 150 kHz square wave from the VTO. In the mixer, the square wave from the VTO alternately gates out positive and negative portions of the 5 Hz to 50 kHz input signal, resulting in full-wave balanced multiplication between the two signals. When the mixer is properly balanced, the VTO and input frequencies are supressed and the composite output signal is predominately the upper and lower sidebands.
- 4-40. The gain control circuit at the output of the mixer is a resistive attenuator controlled by transistor switches Q12 and Q13. Transistor switch Q12 is energized on the 1 Hz and 3 Hz BANDWIDTH settings and Q13 is energized on the 10 Hz and 30 Hz BANDWIDTH settings. The result is that the signal level is decreased as the bandwidth is narrowed. The reasons for this are:
- a. On the wider bandwidths, the noise floor in the IF Filter rises. A larger signal is, therefore, required to maintain the required signal-to-noise ratio.
- b. On the narrower bandwidths, the IF Filter becomes non-linear when high-level signals are applied. Since the noise floor is lower, the non-linearity can be minimized by lowering the signal level.
- 4-41. The output buffer is a 3-stage amplifier circuit which provides gain and isolation between the Mixer and the IF Filter. The gain of the output buffer can be varied by adjusting the front panel CAL 10 kHz potentiometer.
- **4-42. IF Filter.** The IF Filter consists of 5 synchronously-tuned crystal filter stages. Each stage (Figure 4-2) can be divided into 6 major sections:
  - 1. Crystal (Y1) and Pulling Capacitor (C1)
  - 2. Capacitive Compensating Network (T1, C2)
  - 3. Resistive Compensating Network (R1, R2, RT1)
  - 4. Parallel Resonant Circuit (L1, C3)
  - 5. Variable Q Switching (Rv, Q1)
  - 6. Output Buffer
- 4-43. Crystal. The crystals used in the IF Filter are pre-aged at the factory and are selected for a center frequency between 99,991 Hz and 99,993 Hz. The reason for selecting a frequency slightly lower than the required 100.00 kHz, is to allow the frequency to be adjusted by placing a "pulling" capacitor (C1) in series with the crystal (see Figure 4-3A).
- 4-44. Capacitive Compensating Network. The purpose of the capacitive compensating network is to neutralize the shunt capacitance (Cs) of the crystal and any stray capacitance introduced by the component leads and circuit board. Transformer T1 functions as an inverter, producing a

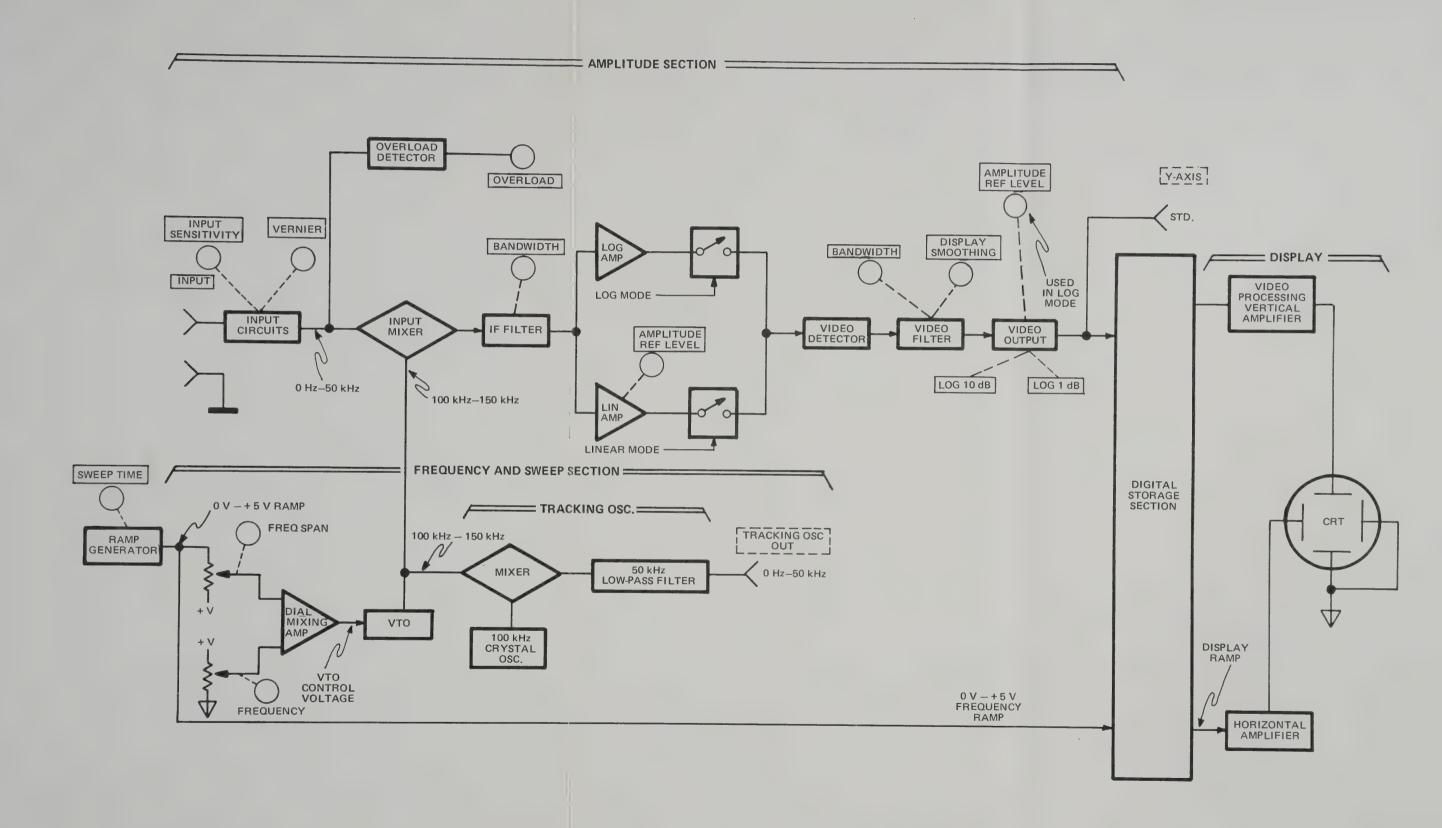


Figure 4-1. Simplified Block Diagram.

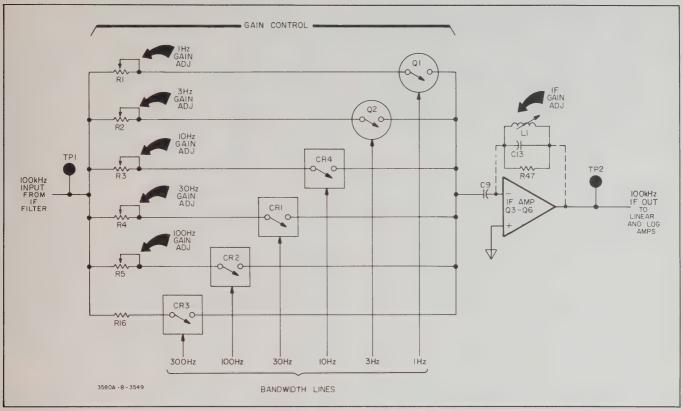


Figure 4-4. IF Amplifier.

**4-51. IF Amplifier.** The IF Amplifier section (Figure 4-4) consists of a Gain Control circuit and an LCR-tuned IF Amplifier.

4-52. Gain Control Circuit. The gain of the IF Amplifier is determined by the input resistance provided by the Gain Control circuit and by the impedance of the parallel LCR network in the feedback loop. The Gain Control circuit has six resistive input branches. The input branches are individually switched into the circuit by transistor and diode switches controlled by lines from the BANDWIDTH switch. With the exception of the 300 Hz branch, each section of the Gain Control circuit contains a variable resistor. This provides a separate gain adjustment for each BANDWIDTH setting. The separate gain adjustments compensate for gain variations that occur in the Input Mixer and IF Filter.

4-53. IF Amplifier. The IF Amplifier is a 3-stage amplifier circuit which is tuned to 100 kHz by the parallel resonant tank circuit in the feedback loop. The 3 dB bandwidth of the amplifier is approximately 1.2 kHz. The IF Amplifier has a low-impedance complementary-symmetry output stage which drives the following log and linear amplifier stages. The full-scale signal level at the output (TP2) of the IF Amplifier is approximately 2.8 V rms on all six BANDWIDTH settings.

4-54. Linear Amplifier. The Linear Amplifier (Figure 4-5) consists of an Input Attenuator, an Input Amplifier, an Output Attenuator and an Output Amplifier. The Input Attenuator is controlled by the front panel AMPLITUDE REF LEVEL switch and provides either - 40 dB or 0 dB of

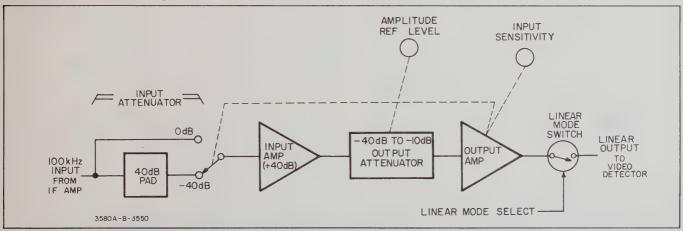


Figure 4-5. Linear Amplifier.

attenuation. The Input Amplifier provides a fixed gain of approximately 40 dB. The Output Attenuator, also controlled by the AMPLITUDE REF LEVEL switch provides -40 dB, -30 dB, -20 dB or -10 dB of signal attenuation. Table 4-1 lists the input attenuation, Input Amplifier gain, output attenuation and the resulting gain or attenuation for each AMPLITUDE REF LEVEL setting. Note that as the AMPLITUDE REF LEVEL switch is rotated from the X1 (NORMAL) position, the attenuation is decreased and the signal level is increased in steps of 10 dB.

4-55. The Output Amplifier stage provides the variable gain needed to maintain a 0 V rms to 1.2 V rms full-scale output on all input ranges and reference settings. The gain of the Output Amplifier is controlled by both the INPUT SENSI-TIVITY switch and the AMPLITUDE REF LEVEL switch. By observing these two front panel controls, it can be noted that the full-scale reference on the INPUT SENSITIVITY switch dial is indicated by a white window that is mechanically linked to the AMPLITUDE REF LEVEL switch. Changing the position of either switch changes the full-scale reference in a 20 V, 10 V, 2 V, 1 V sequence. This sequence differs from the 10 dB/step sequence provided by the A9 Input Circuit and the attenuators in the Linear Amplifier. For this reason, the gain of the Output Amplifier is changed on alternate ranges. With the full-scale reference set to 10 V, 1 V, 0.1 V, etc., the gain of the Output Amplifier is X56. With the reference set to 20 V, 2 V, 0.2 V, etc. the gain is increased to X88.

Table 4-1. Linear Amplifier Gain.

Ampl Ref Level	Input Atten.	Input Amp Gain	Output Atten.	Net Gain or Atten.
X1	- 40 dB	+ 40 dB	- 40 dB	- 40 dB
	- 40 dB	+ 40 dB	- 30 dB	- 30 dB
X0.1	- 40 dB	+ 40 dB	- 20 dB	- 20 dB
	- 40 dB	+ 40 dB	- 10 dB	- 10 dB
X0.01	0 dB	+ 40 dB	- 40 dB	0 dB
	0 dB	+ 40 dB	- 30 dB	+ 10 dB
X0,001	0 dB	+ 40 dB	- 20 dB	+ 20 dB
	0 dB	+ 40 dB	- 10 dB	+ 30 dB

4-56. Log Amplifier. The Log Amplifier (Figure 4-6) is a hybrid circuit consisting of a log amplifier package (U5) and four external control amplifiers (U1 - U4). The log amplifier package contains 12 differential amplifier stages. Each stage has a logarithmic output characteristic over a 10 dB range (Figure 4-7). Internal resistive dividers and the external control amplifiers bias each stage to respond to a different 10 dB portion of the input signal. The outputs of the 12 stages are summed in a common load (R<sub>L</sub>), forming the composite output characteristic shown in Figure 4-8.

4-57. From Figure 4-8, the following can be noted:

a. When the input signal is below the range of a given stage, that stage will make essentially no contribution to the output of the log amplifier.

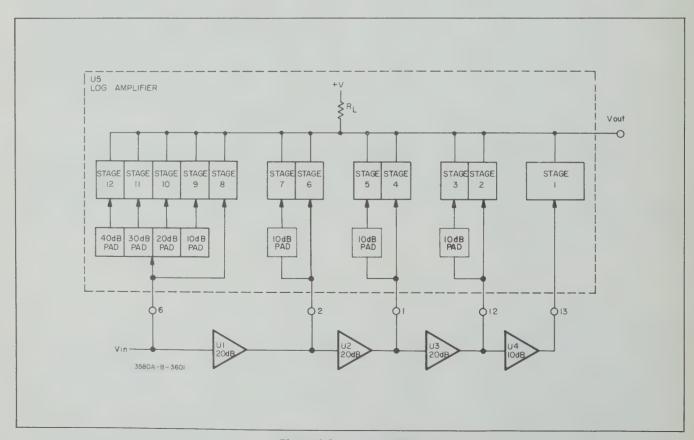


Figure 4-6. Log Amplifier.

Model 3580A Section IV

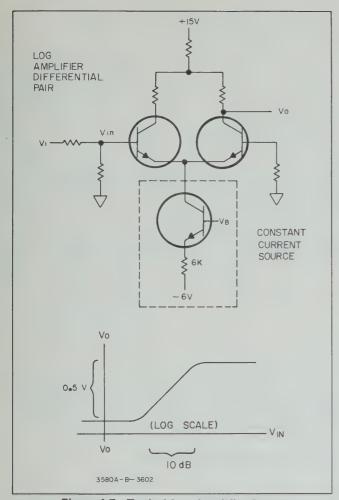


Figure 4-7. Typical Log Amplifier Stage.

- b. When the input signal is above the range of a given stage, that stage will make a constant contribution to the output of the log amplifier.
- c. When the input signal is within the range of a given stage, that stage provides the logarithmic output over a 10 dB range. The logarithmic output is added to the constant output of the more sensitive stages.
- 4-58. Since there are twelve 10 dB stages in the log amplifier package, it would appear that the overall dynamic range is 120 dB. In practice, however, the first and last stages do not produce usable outputs over their entire range. The dynamic range of the device is therefore limited to approximately 100 dB. The 3580A input levels are such that only 80 dB to 90 dB of the 100 dB range is used.
- **4-59.** Video Detector. The Video Detector is an average-responding, active, full-wave detector circuit which produces a dc voltage proportional to the amplitude of the log or linear IF signal. The output of the Video Detector, ranging from 0 V to +2.5 V dc full scale, is applied to the Video Filter.
- **4-60.** Video Filter. The purpose of the Video filter is to smooth out the ripple and random noise riding on the

detected video signal. The filter consists of a single-pole RC network followed by an output buffer. The response of the filter is varied by changing the values of the RC elements in the circuit. The amount of filtering is increased as the BANDWIDTH setting is narrowed or as the DISPLAY SMOOTHING control is varied from MIN to MAX.

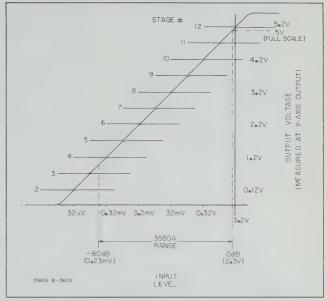


Figure 4-8. Log Amplifier Input And Output Levels.

- **4-61.** Video Output. The Video Output circuits (Figure 4-9) consist of a Reference Divider, a Summing Amplifier and an Output Amplifier.
- 4-62. The 0 V to +2.5 V dc video signal from the Video Detector is applied to the inverting (-) port of the Summing Amplifier where it is summed with a negative dc offset voltage from the Reference Divider. In the Log 10 dB and Log 1 dB amplitude modes, the dc offset voltage varies from -2.5 V dc to -0.75 V dc as the AMPLITUDE REF LEVEL control is rotated from the 0 dB (NORMAL) position to the -70 dB position. This offsets the display in steps of 10 dB. In the Linear mode, the offset voltage is fixed at -2.5 V dc and the CRT trace remains at the bottom of the screen.
- 4-63. In the Log 10 dB and Linear amplitude modes, the gain of the Summing Amplifier is X2 and an offset of -2.5 V dc produces an output of +5 V dc. This positions the CRT trace at the bottom of the screen. With a video response of +2.5 V dc, the offset voltage is cancelled and the output of the Summing Amplifier drops to 0 V dc for full-scale deflection. When the Log 1 dB mode is selected, the gain of the Summing Amplifier is increased to X20. This expands the CRT scale from 10 dB per division to 1 dB per division.
- 4-64. With the Summing Amplifier gain set to X20 and a video input of 0 V, the dc offset voltage from the Reference Divider drives the output of the Summing Amplifier positive. In this state, the Summing Amplifier

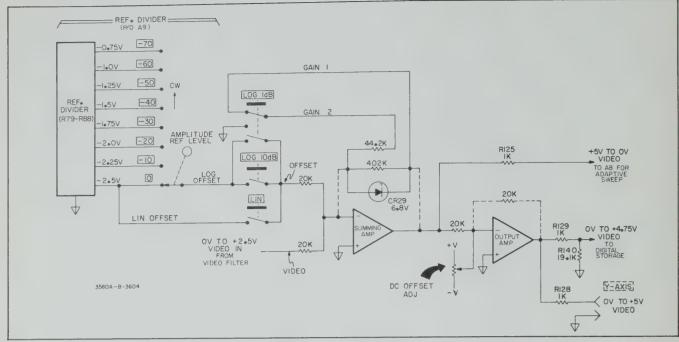


Figure 4-9. Video Output.

output is limited to +6.8 V dc by Zener diode CR29. An output between +5 V and +6.8 V positions the CRT trace at the bottom of the screen. If the positive video level equals the negative offset voltage, the output of the Summing Amplifier drops to 0 V for full-scale deflection. If the video level exceeds the offset voltage, the Summing Amplifier output is driven negative and is limited to -0.6 V by CR29. An output level between 0 V and -0.6 V peaks the display. Table 4-2 lists the offset voltage, displayed video levels, Summing Amplifier output levels and display range for each AMPLITUDE REF LEVEL setting.

4-65. The output of the Summing Amplifier is applied to the A8 Assembly through R125 and to the Output Amplifier. In the A8 Assembly, the video output from the Summing Amplifier is used to detect the presence of a video response for Adaptive Sweep purposes.

4-66. At the inverting port of the Output Amplifier, the +5 V dc to 0 V output from the Summing Amplifier is summed with a -5 V dc offset from the wiper of the DC Offset Adj. potentiometer, R11. The gain of the Output Amplifier is X1 and the resulting output ranges from 0 V dc

to +5 V dc, full scale. This output is attenuated to +4.75 V dc full scale by R129 and R140 and applied to the Digital Storage section. The 0 V to +5 V output is also applied to the rear panel Y-AXIS output connector.

### 4-67. Frequency And Sweep Section.

4-68. Figure 4-10 is a functional block diagram of the Frequency and Sweep Section. Elements shown on the diagram are described in the following paragraphs.

4-69. Linear Sweep Gerierator. Because of its Adaptive Sweep capability, the 3580A Linear Sweep generator is considerably more sophisticated than conventional sweep generators. The primary purpose of the Linear Sweep Generator is to produce a 0 V to +5 V linear ramp which simultaneously sweeps the VTO frequency and the refresh trace on the CRT. In the Adaptive Sweep process, however, it is required to perform a sequence of operations in response to video signals that rise above the baseline threshold set on the CRT display. This sequence or "algorithm" is illustrated and described in Figure 4-11.

Table 4-2. Video Output Circuits (Log 1 dB mode).

Reference Level	Offset Voltage	Displayed Video Level	Summing Amp Output	Display Range
0 dB	- 2.50 V	+ 2.25 V to + 2.50 V	+5 V to 0 V	- 10 dB to 0 dB
- 10 dB	- 2.25 V	+ 2.00 V to + 2.25 V	+ 5 V to 0 V	- 20 dB to - 10 dB
- 20 dB	- 2.00 V	+ 1.75 V to + 2.00 V	+ 5 V to 0 V	- 30 dB to - 20 dB
- 30 dB	- 1.75 V	+ 1.50 V to + 1.75 V	+5 V to 0 V	- 40 dB to - 30 dB
- 40 dB	- 1.50 V	+ 1.25 V to + 1.50 V	+5 V to 0 V	- 50 dB to - 40 dB
- 50 dB	- 1.25 V	+ 1.00 V to + 1.25 V	+5 V to 0 V	- 60 dB to - 50 dB
- 60 dB	- 1.00 V	+ 0.75 V to + 1.00 V	+ 5 V to 0 V	- 70 dB to - 60 dB
- 70 dB	- 0.75 V	+ 0.50 V to + 0.75 V	+5 V to 0 V	- 80 dB to - 70 dB

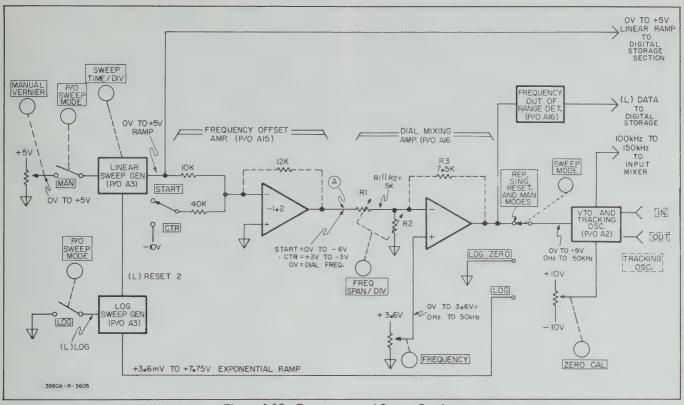
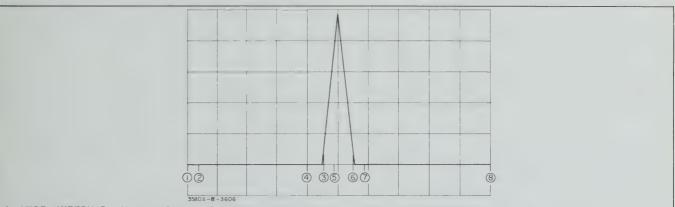


Figure 4-10. Frequency and Sweep Section.



- 1. STOP SWEEPING AND ALLOW TIME FOR THE IF FILTER TO SETTLE. SWEEP SLOW FORWARD TO POINT 2.
  - a. During slow sweeps the sweep time is as indicated by the front panel SWEEP TIME setting.
  - b. The instrument initially sweeps slowly to ensure that the zero response (if present) is properly detected.
  - c. The distance between Points 1 and 2 is equal to 0.75 times the Step Back distance (See Step 3).
- IF A RESPONSE IS NOT PRESENT, SWEEP FAST FOR-WARD.
  - The fast sweep time is 20 to 25 times faster than indicated by the SWEEP TIME setting.
- RESPONSE DETECTED, SWEEP FAST BACKWARD TO POINT 4.
  - The distance between Points 3 and 4 is called the "Step Back" distance and is determined by the FREQ SPAN and BANDWIDTH settings,

- 4. STOP SWEEPING AND ALLOW TIME FOR THE IF FILTER TO SETTLE. SWEEP SLOW FORWARD TO POINT 5.
  - a. The distance between Points 4 and 5 is equal to 1.75 times the Step Back distance.
  - b. This step ensures that the instrument sweeps slowly past the point it stepped back from initially.
- 5. CONTINUE TO SWEEP SLOWLY TO POINT 6 WHERE RESPONSE IS NO LONGER DETECTED.
- 6. SWEEP SLOW FORWARD TO POINT 7.
  - a. The distance between Points 6 and 7 is equal to 0.75 times the Step Back distance.
  - b. This step ensures that the response is completely passed before the sweep rate is increased.
- IF RESPONSE IS NOT PRESENT, SWEEP FAST FORWARD TO POINT 8.
- 8. END OF SWEEP, RESET TO POINT 1.

Figure 4-11. Adaptive Sweep Routine.

4-70. Figure 4-12 is a functional block diagram of the Linear Sweep Generator. The major circuit elements include a Digital Controller, a Programmable Ramp Generator, an End of Sweep Comparator, a Ramp Comparator and a Delay Circuit.

4-71. Digital Controller. The Digital Controller is a simple algorithmic state-machine (ASM) which provides sequential instructions that control the Adaptive Sweep process. The six input lines shown on the left-hand side of the controller block are qualifiers which determine the "next state" of the controller. The qualifier lines are listed and defined in Table 4-3. The outputs on the right-hand side of the controller block are instructions which are applied to the Ramp Generator and associated circuitry. The functions of the various instruction lines are described in the following paragraphs.

4-72. The Digital Controller is synchronized by a 55 kHz to 70 kHz pulse train applied to the Clock input. The clock signal is generated by an oscillator in the High Voltage Power Supply section. Even though the clock frequency is 55 kHz to 70 kHz, the Digital Controller does not cycle at a 55 kHz to 70 kHz rate. State times are determined strictly by the qualifier inputs and the clock only ensures that the counting elements within the controller are incremented simultaneously. In order for the digital controller to func-

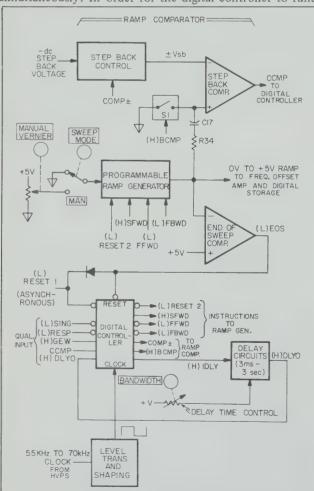


Figure 4-12. Linear Sweep Generator.

tion properly, the clock frequency must be between  $55\ kHz$  and  $70\ kHz$ .

4-73. Programmable Ramp Generator. The Programmable Ramp Generator produces a 0 V to +5 V linear ramp voltage in response to sequential instructions from the Digital Controller. The instructions applied to the Ramp Generator are listed and defined in Table 4-4.

4-74. End of Sweep Comparator. The EOS Comparator detects when the ramp voltage reaches + 5 V and, in turn, produces an End of Sweep (LEOS) command which asynchronously resets the Digital Controller to State  $\emptyset$ .

Table 4-3. Qualifier Inputs.

Table 40. Qualifier inputs.
DESCRIPTION
Line goes low when SWEEP MODE switch is set to RESET, MAN or LOG ZERO and when CLEAR WRITE button is pressed. When this line initially goes low, the Digital controller is asynchronously reset to State Ø. The controller then increments to State 1 and remains in that state until the (L)RESET 1 line goes high.
Line goes low when SWEEP MODE switch is set to SINGLE position.
Line goes low when a video response rises above the baseline threshold set on the CRT display.
Line from Digital Storage section goes high to indicate that the display sweep has reset.
Line from Delay Circuit goes high to indicate that the delay period is over.
NOTE
The "L" or "H" preceding each qualifier mneumonic indicates the "Low" or "High" assertion state (true or "1" state) of the qualifier line. In some cases, both states of a qualifier are used in the control sequence. For example, a qualifier might be "Response" (L)RESP or "No Response" (H)NRESP.

4-75. Ramp Comparator. In the Adaptive Sweep routine, the Ramp Comparator measures the forward and reverse excursions of the ramp voltage and informs the Digital Controller when the Ramp Generator has swept the required distance from a given point. The need for this is illustrated in Figure 4-11. At Point 3, for example, a response is initially detected and the Ramp Generator sweeps backward to Point 4. The controller must be informed when the sweep reaches Point 4 so that it can instruct the Ramp Generator to begin sweeping slow forward. Similarly, when a response is no longer detected at Point 6, the Ramp Generator continues to sweep slow forward to Point 7. The controller must be informed when the sweep reaches point 7 so that it can instruct the Ramp Generator to begin sweeping fast.

4-76. The Ramp Comparator consists of a Step Back Control circuit and a Step Back Comparator. Both of these elements operate in response to instructions from the Digital Controller.

Model 3580A Section IV

Table 4-4. Ramp Generator Instructions.

INSTR	DESCRIPTION
(L)RESET 2	In the Single and Repetitive sweep modes, the (L)RESET 2 instruction resets the Ramp Generator. When the Ramp Generator is reset, its output is 0 V. In the Manual sweep mode, the (L)RESET 2 instruction is given continuously. The Ramp Generator then functions as a X1 amplifier and receives its input from the MANUAL VERNIER potentiometer.
(H)SFWD	When the (H)SFWD (Slow Forward) instruction is given, the Ramp Generator sweeps in a positive direction from 0 V to +5 V. The sweep time is as indicated by the SWEEP TIME setting.
(L)FFWD	When the (L)FFWD (Fast Forward) instruction is given, the Ramp Generator sweeps in a positive direction at 20 to 25 times the panel-selected rate.
(H)FBWD	When the (H)FBWD (Fast Backward) instruction is given, the Ramp Generator sweeps in a negative direction (+5 V to 0 V) at 20 to 25 times the panel-selected rate.

4-77. The Step Back Control circuit is a "programmable inverter" which receives a negative dc input voltage and provides an inverted or non-inverted output, depending on the state of the COMP instruction line. The negative dc "step-back voltage" applied to the Step Back Control circuit is controlled by the FREQ SPAN and BANDWIDTH settings. The magnitude of this voltage determines the "step-back distance" described in Figure 4-11. As the frequency span is narrowed or bandwidth is widened, the magnitude of the step back voltage increases causing the step back distance to increase. When the COMP instruction line is high, the instruction is COMP (-). This means that the output of the Step Back Control circuit is a negative dc voltage that is equal in magnitude to the applied step-back voltage. When the COMP instruction line is low, the instruction is COMP (+). When the COMP (+) instruction is given, the output polarity is changed from negative to positive and the magnitude of the voltage is decreased to 0.75 times the applied step-back voltage. For example, if the applied step-back voltage is - 1 V dc and the instruction is COMP (-), the output of the Step Back Control circuit is - 1 V dc. If the instruction is changed to COMP (+), the output changes to + 0.75 V dc.

4-78. The Step Back Comparator is a high impedance differential amplifier circuit controlled by the (H)BCMP (Begin Comparison) instruction line from the Digital Controller. When the Begin Comparison instruction is not given (BCMP line low), switch S1 is closed and the non-inverting (+) port of the comparator is grounded. Capacitor C17 then charges to the ramp voltage through R34. When the Begin Comparison instruction is given, switch S1 opens and the instantaneous ramp voltage is retained by C17. With S1 open, the polarity of the charge on C17 is such that C17 serves as a bucking supply. Thus, as the Ramp Generator sweeps forward or backward from the point at which S1 opens, only the change in voltage is felt

at the non-inverting port of the comparator. If, for example, the BCMP instruction is given when the ramp voltage is +4 V and the ramp voltage then decreases to +3 V, the voltage at the non-inverting port is -1 V. When the voltage at the non-inverting port slightly exceeds the positive or negative step-back voltage at the inverting port, the output of the comparator changes states and the CCMP (Comparison Complete) qualifier is met. This indicates to the Digital Controller that the Ramp Generator has swept the required distance from the point at which the comparison began.

4-79. In the Adaptive Sweep routine, the COMP (-) and BCMP instructions are given when the Ramp Generator begins sweeping backward. At the time the BCMP instruction is given, the output of the Step Back Comparator is high. As the ramp voltage decreases, the voltage at the non-inverting input becomes increasingly negative until it slightly exceeds the negative step-back voltage at the inverting port. The output of the comparator then goes low and the CCMP qualifier is met. The COMP (+) and BCMP instructions are given when the Ramp Generator is sweeping forward. In this case, the output of the comparator is low when the BCMP instruction is given. As the ramp voltage increases, the voltage at the non-inverting port becomes increasingly positive until it slightly exceeds the positive step-back voltage at the inverting port. At that time, the output of the comparator goes high and the CCMP qualifier is met.

4-80. Delay Circuit. The Delay Circuit is a monostable multivibrator which provides a 3 ms to 3 sec. delay periodin response to the Initiate Delay (IDLY) instruction from the Digital Controller. At the end of the delay period, the Delay Circuit produces a "delay over" flag (DLYO) which serves as a qualifier input to the Digital Controller.

4-81. The purpose of the 3 ms to 3 sec. delay period is to allow time for the IF Filter to settle between fast and slow sweeps in the Adaptive Sweep routine. The delay period is determined by the BANDWIDTH setting. As the bandwidth is narrowed, the response time of the IF Filter increases and a longer delay period is required.

4-82. Control Sequence. Figure 4-13 is an ASM Chart showing the control sequence for the 8-state Adaptive Sweep routine. Each state of the Digital Controller is represented by a rectangular Instruction Block followed by one or two trapezoidal-shaped Qualifier Blocks. Items listed in the Instruction Block of a given state indicate the instruction(s) given by the controller in that state. Items in the Qualifier Blocks of a given state indicate the qualifiers that must be met before the controller can increment to the next state.

4-83. The routine begins with the Digital Controller asynchronously reset to State  $\emptyset$  by an End of Sweep (LEOS) command. The EOS command is momentary and does not prevent the controller from incrementing to the next state. State  $\emptyset$  is a "dummy" state where no instructions are given.

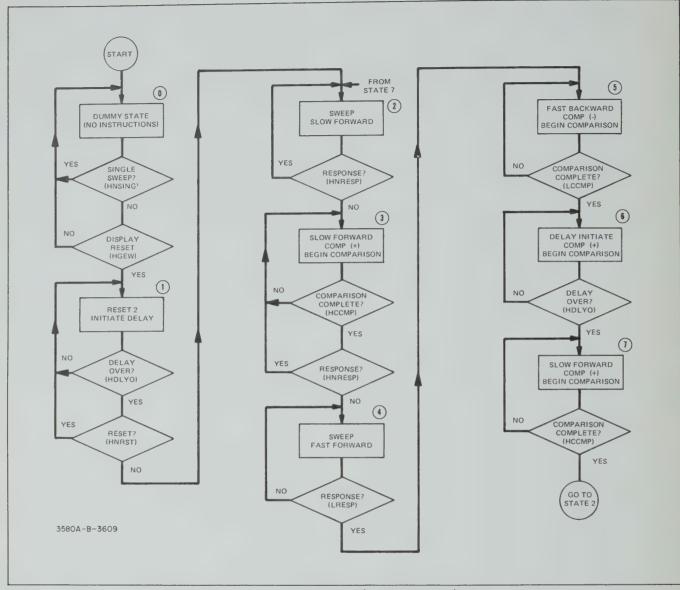


Figure 4-13. ASM Chart (Adaptive Sweep).

The two qualifiers in this state are HNSING (Not Single) and HGEW. The HNSING qualifier is met when the SWEEP MODE switch is *not* in the SING position. When the Single sweep mode is selected, the controller remains in State  $\emptyset$  following the End of Sweep command. The HGEW qualifier is met when the display sweep in the Digital Storage section resets.

4-84. In State 1, the (L) Reset 2 instruction is given to reset the Ramp Generator. At the same time, a delay is initiated to allow the IF Filter to settle. When the delay period is over (DLYO qualifier met) the controller increments to State 2. If the SWEEP MODE switch is in the RESET, MAN or LOG ZERO position, the (L) Reset 1 line is low causing the controller to remain in State 1.

4-85. In State 2, the Ramp Generator starts sweeping SLOW FORWARD. The sweep starts out slowly to ensure that any signals on or near the start frequency will be

properly detected. If a response is not present or when the initial response is no longer detected, The HNRESP qualifier is met and the controller increments to State 3.

4-86. In State 3, the Ramp Generator continues to sweep SLOW FORWARD. At this time, the COMP (+) and BCMP instructions are given and the Ramp Generator must sweep slow forward until the CCMP qualifier is met. If, for some reason, a response is detected in State 3, the controller will not increment to State 4 until the response is passed (HNRESP qualifier met).

4-87. In State 4, the Ramp Generator sweeps FAST FORWARD until a response is detected. When a response is detected (LRESP qualifier met), the controller increments to State 5.

4-88. In State 5, the COMP (-) and BCMP instructions are given and the Ramp generator sweeps FAST BACKWARD

Model 3580A Section IV

until the CCMP qualifier is met. The CCMP qualifier is met when the ramp voltage decreases by an amount equal to the step-back voltage.

4-89. In State 6, a delay is initiated and the Ramp Generator stops sweeping until the DLYO qualifier is met. The controller then increments to State 7.

4-90. In State 7, the Ramp Generator sweeps SLOW FORWARD until the CCMP qualifier is met. The controller then recycles to State 2. Note that the Begin Comparison instruction initiated in State 5 is sustained in States 6 and 7. This means that the ramp voltage stored in State 5 (response initially detected) is still the reference in State 7. Since, in State 7, the instruction applied to the Step Back Control circuit is COMP (+), the ramp generator must sweep slow forward past the point it stepped back from initially. (See Steps 3, 4 and 5 of Figure 4-11).

4-91. Non-Adaptive Sweep. When the ADAPTIVE SWEEP control is in the OFF position, the (L)RESP qualifier line is pulled low to simulate a response. As in the Adaptive Sweep routine, the Digital Controller is initially reset to State Ø and is incremented to States 1 and 2. In State 2, however, the (H)NRESP (No response) qualifier is never met and the controller is forced to remain in that state until it is again reset. When the controller is in State 2, the (H)SFWD (Slow Forward) instruction is given and the Ramp Generator sweeps at the rate indicated by the SWEEP TIME setting.

4-92. Manual Sweep. When the Manual sweep mode is selected, the (L)RESET 1 line is pulled low causing the Digital Controller to remain in State 1. The (L)RESET 2 instruction given in State 1 converts the Ramp Generator into a X1 amplifier. The 0 V to +5 V dc level from the wiper of the MANUAL VERNIER potentiometer is then present at the output of the Ramp Generator. This dc level determines the VTO frequency and the position of the refresh trace on the CRT.

4-93. Frequency Offset Amplifier. The 0 V to + 5 V ramp from the Linear Sweep Generator is applied to the inverting port of the Frequency Offset Amplifier. The gain of the amplifier is - 1.2 and, with the START/CTR switch in the START position, the ramp voltage at the output ranges from 0 V to - 6 V. With the START/CTR switch set to the CTR (Center) position, a negative dc offset is summed with the ramp voltage at the inverting port. The ramp voltage at the output then ranges from + 3 V to - 3 V.

**4-94.** Dial Mixing Amplifier. The output of the Frequency Offset Amplifier is applied to the inverting port of the Dial Mixing Amplifier through a resistive attenuator network (R1, R2) controlled by the FREQ SPAN switch. As the frequency span is narrowed, the attenuation increases and the effective gain of the amplifier (with respect to Point A) decreases. Table 4-5 lists the Dial Mixing Amplifier gain and resulting ramp output levels for each FREQ SPAN setting. Output levels listed in the table are measured with an input ramp of 0 V to - 6 V and with the non-inverting port of the amplifier at 0 V.

Table 4-5. Dial Mixing Amplifier Gain.

Freq Span/Div	Overall Span	Mixing Amp Gain (From Point A)	Output Ramp
5 kHz	50 kHz	- 1.5	0 V to +9 V
2 kHz	20 kHz	- 0.6	0 V to + 3.6 V
1 kHz	10 kHz	- 0.3	0 V to + 1.8 V
0.5 kHz	5 kHz	- 0.15	0 V to + 0.9 V
0.2 kHz	2 kHz	- 0.06	0 V to + 0.36 V
0.1 kHz	1 kHz	- 0,03	0 V to + 0.18 V
50 Hz	500 Hz	- 0.015	0 V to + 0.09 V
20 Hz	200 Hz	- 0.006	0 V to + 36 mV
10 Hz	100 Hz	- 0.003	0 V to + 18 mV

4-95. A 0 V to + 3.6 V dc control voltage from the front panel FREQUENCY potentiometer is applied to the non-inverting port of the Dial Mixing Amplifier. The gain at the non-inverting port is determined by the parallel resistance of R1 and R2 and by the feedback resistance, R3. The values of R1 and R2 are such that their parallel resistance is always 5 K. The fixed gain at the non-inverting port is therefore:

$$1 + \frac{7.5 \text{ K}}{5 \text{ K}} = +2.5$$

With the ramp input at 0 V the output of the Dial Mixing Amplifier varies from 0 V to +9 V as the FREQUENCY control is rotated from 0 Hz to 50 kHz. This tunes the analyzer over its entire frequency range. Any time the ramp at the inverting port is at 0 V, the analyzer frequency is as indicated on the FREQUENCY dial.

4-96. The following examples illustrate how the ramp and frequency-dial inputs are combined at the output of the Dial Mixing Amplifier to produce the required frequency sweep.

### Example 1:

FREQUENCY SPAN 5K/DIV
START/CENTERSTART
GAIN (Point A)
RAMP VOLTAGE (Point A) 0 V to - 6 V
RAMP CONTRIBUTION
TO OUTPUT
FREQUENCY DIAL 0 Hz
DIAL CONTRIBUTION TO OUTPUT0 V
<b>OUTPUT RAMP</b>
FREQUENCY SWEEP 0 Hz to 50 kHz

### Example 2:

START/CENTER CENTER GAIN (Point A) 1.5
RAMP VOLTAGE (Point A) $+ 3 \text{ V to } - 3 \text{ V}$
RAMP CONTRIBUTION
TO OUTPUT 4.5 V to + 4.5 V
FREQUENCY DIAL
DIAL CONTRIBUTION
TO OUTPUT+ 4.5 V
OUTPUT RAMP $0 \text{ V to} + 9 \text{ V}$
FREQUENCY SWEEP 0 Hz to 50 kHz

### Example 3:

<sup>\*</sup>Out of Range

4-97. Out of Range Detector. As illustrated in Example 3, certain combinations of FREQUENCY and FREQ SPAN settings cause the voltage at the output of the Dial Mixing Amplifier to go below the 0 V (0 Hz) lower limit or above the +9 V (50 kHz) upper limit. When either limit is exceeded, the VTO frequency is driven out of range. This could cause erroneous responses to appear on the display. The Frequency Out-of-Range Detector senses when the Dial Mixing Amplifier output is more negative than 0 V or more positive than +9 V and, in turn, generates an (L)Data flag which is applied to the Digital Storage section. In the Digital Storage section, the (L)Data flag clears the memory locations where the frequency is out of range. As a result, a clean baseline appears on the display (Figure 4-14).

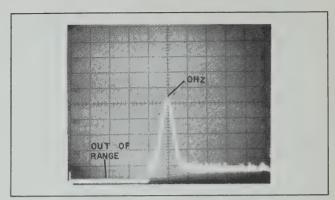


Figure 4-14. Frequency Out Of Range.

**4-98. VTO** and **Tracking Oscillator.** Refer to Figure 4-15 for the following discussion.

4-99. The 0 V to +9 V ramp from the Dial Mixing Amplifier is applied to the VTO and to the inverting port of the VTO Error Amplifier. At the inverting port of the Error Amplifier, the ramp voltage is summed with a dc voltage from the front panel ZERO CAL potentiometer. The sum of the two voltages serves as a reference for the frequency control loop.

4-100. The VTO. The VTO is a conventional oscillator circuit that is tuned by changing the dc bias on two varactor diodes which are the capacitive elements in the LC tank circuit. The 0 V to +9V ramp coarse tunes the VTO

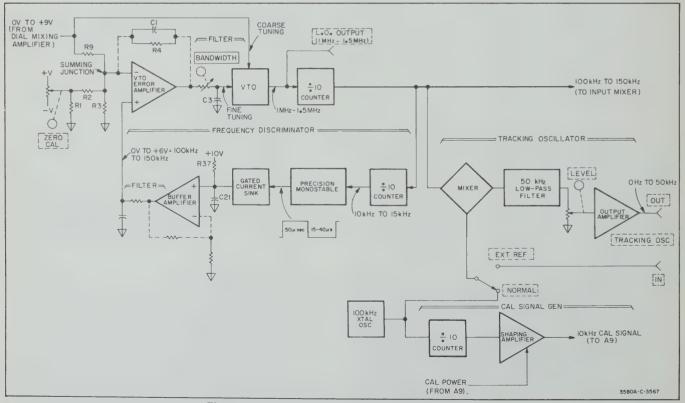


Figure 4-15. VTO And Tracking Oscillator (AS).

frequency from 1 MHz to 1.5 MHz. Fine tuning is provided by the error voltage from the VTO Error Amplifier. The output of the VTO is applied to a Divide-By-Ten Counter and to the rear panel L.O. OUTPUT connector. The output of the Divide-By-Ten Counter is a 100 kHz to 150 kHz square wave which is applied to the Input Mixer (A9) and to the Frequency Discriminator and Tracking Oscillator.

4-101. Frequency Discriminator. Due to the inherent non-linearity of the VTO, an external frequency control loop is required. The frequency control loop is comprised of a Frequency Discriminator and VTO Error Amplifier. The Frequency Discriminator produces a dc voltage that is linearily proportional to the VTO output frequency. This dc voltage is applied to the non-inverting port of the VTO Error Amplifier where it is compared to the reference voltage at the inverting port. Any difference between these two voltages causes the output of the Error Amplifier to increase or decrease to correct the VTO frequency.

4-102. The 100 kHz to 150 kHz VTO output signal is applied to a Divide-By-Ten Counter in the Frequency Discriminator. The output of the Divide-By-Ten Counter is a 10 kHz to 15 kHz square wave which positive-edge triggers the Precision Monostable Multivibrator. When triggered, the output of the Monostable Multivibrator goes high for exactly 50 usec. This gates off the Current Sink allowing C21 to charge toward + 10 V through R37. At the end of the 50 usec. charge period, the Current Sink is gated on causing C21 to discharge at a fixed rate. As the VTO frequency increases, the charge period of C21 remains at 50 µsec. but the discharge period becomes shorter. As a result, the average charge on C21 increases. The voltage across C21 is amplified, filtered and applied to the non-inverting port of the VTO Error Amplifier. This voltage varies from 0 V to + 6 V dc as the VTO frequency is tuned from 100 kHz to 150 kHz.

4-103. Precision Monostable Multivibrator. The magnitude of the dc voltage at the output of the Frequency Discriminator is determined by the duty cycle of the pulse generated by the Precision Monostable Multivibrator. In order for the output voltage to increase linearily with frequency, the width of the positive half cycle of the pulse must be constant regardless of frequency and the width of the negative half cycle must vary linearily with frequency. This requires precise timing and a high degree of stability not obtainable with conventional R/C-coupled "one-shot" multivibrators.

4-104. Figure 4-16 is a simplified block diagram of the Precision Monostable Multivibrator. In the reset state, the following conditions exist:

a. The "Q" output of the J-K Flip-Flop is low causing Q13 to cut off. Capacitor C27 then charges to +10 V through R54.

b. The "Q" output of the J-K Flip-Flop is high. This resets the 14-Pulse Counter to State  $\emptyset$ .

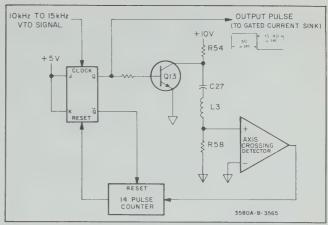


Figure 4-16. Precision Monostable.

4-105. The J-K Flip Flop is clocked by the zero crossing during a low-to-high transition of the VTO input signal. When the Flip-Flop is clocked, the "Q" output goes high, Q13 is gated on and the junction of C27 and R54 is grounded. A series-resonant tank circuit is then formed by C27, L3 and R58. As C27 discharges, the lightly damped tank circuit rings at its resonant frequency (approximately 230 kHz). The 230 kHz signal developed across R58 is squared-up by the Axis Crossing Detector and applied to the 14-Pulse Counter. The 14-Pulse Counter counts 14 pulses and then resets the J-K Flip-Flop to terminate the output pulse.

4-106. Tracking Oscillator. In the Tracking Oscillator section, the 100 kHz to 150 kHz output from the VTO is mixed with a 100 kHz signal from a Crystal Oscillator or with an external signal applied to the TRACKING OSC IN connector. The difference frequency at the output of the Mixer is fed through a 50 kHz Low-Pass Filter, amplified and applied to the rear panel TRACKING OSC OUT connector. With the rear panel switch in the NORMAL position, the signal at the Tracking Oscillator Output is a 0 Hz to 50 kHz sine wave which tracks the tuned frequency of the instrument. The amplitude of the signal can be varied from 0 V to 1 V rms by adjusting the rear panel LEVEL control.

4-107. Cal. Signal Generation. The 100 kHz output of the Crystal Oscillator is applied to a Divide-By-Ten Counter. The output of the counter is processed and applied to the A9 Input Circuits where it becomes the input signal with the INPUT SENSITIVITY switch set to the CAL position. The calibration signal is a 15/85 duty cycle pulse train which provides a 10 kHz fundamental-frequency component and odd and even harmonic components spaced at 10 kHz intervals. The amplitude of the fundamental-frequency component is such that it produces full-scale deflection when the instrument is properly calibrated. The amplitudes of the harmonic components are not meaningful.

**4-108.** Log Sweep Generator. In the Log sweep mode, the 0 V to +5 V linear ramp from the Linear Sweep Generator sweeps the display while a +3.6 mV to +7.75 V exponential ramp from the Log Sweep Generator sweeps the

VTO frequency. The frequency range of the log sweep is from 20 Hz to 43 kHz. During log sweeps, the SWEEP TIME control is disabled and the Linear Sweep Generator is automatically set for a 5 second sweep time. The Log Sweep Generator is synchronized by the (L)Reset 2 instruction from the Linear Sweep Generator.

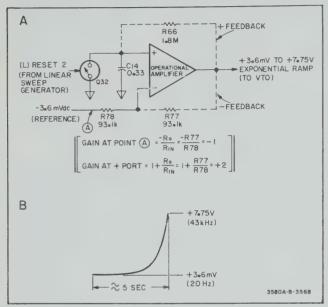


Figure 4-17. Basic Log Sweep Generator.

4-109. Figure 4-17A shows the basic circuit configuration for the Log Sweep Generator. The major circuit element is a high input-impedance operational amplifier. The gain of the amplifier with respect to Point A is - 1 and the gain at the non-inverting port is + 2. At the beginning of the log sweep the following conditions exist:

- a. The (L) Reset 2 line is low.
- b. FET switch Q32 is closed.
- c. The non-inverting port of the amplifier is grounded through Q32.
  - d. Capacitor C14 is fully discharged.
- e. The output voltage is +3.6 mV dc due to the -3.6 mV dc reference applied to Point A. This sets the analyzer frequency to 20 Hz which is the starting point for the log sweep.

When the (L)Reset 2 instruction is cleared, switch Q32 opens and C14 charges toward the output voltage through feedback resistor R66. As C14 charges, the output voltage becomes increasingly positive. Due to the bootstrapping effect of the positive feedback through R66, the charge rate of C14 increases exponentially. The exponential ramp at the output is as shown in Figure 4-17B.

4-110. Auto Zero Circuit. An Auto Zero Circuit is included in the Log Sweep Generator to null out any dc offset introduced by the operational amplifier. The overall circuit configuration is shown in Figure 4-18.

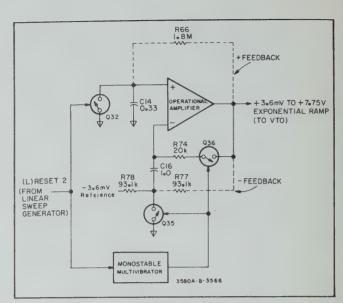


Figure 4-18. Overall Log Sweep Generator.

4-111. When the (L)Reset 2 instruction is initially given, the output of the Monostable Multivibrator goes high for approximately 0.4 seconds. This closes FET switches Q35 and Q36. With switch Q32 also closed, the offset voltage is present at the output of the amplifier and capacitor C16 charges to the offset voltage through R74. At the end of the auto zero period, Q35 and Q36 open and the charge on C16, in series with the input reference voltage, cancels the amplifier offset voltage.

### 4-112. Digital Storage And Display Sections.

4-113. Introduction To Digital Storage. Low frequency spectrum analyzers require narrow bandwidths and consequently, slow sweep rates. Because of these slow sweep rates, the video cannot be displayed directly on a standard CRT. If, for example, the X and Y axis outputs of the 3580A were applied to a standard CRT, the display would be merely a dot fluctuating up and down while moving slowly across the CRT face. Even with the SWEEP TIME control set to 0.01 SEC/DIV (fastest sweep time), a satisfactory display could not be obtained.

4-114. To retain the slowly scanned video information of the 3580A, some form of display storage is required. As indicated in the Simplified Block Diagram Description (Paragraph 4-22), a storage CRT having long persistance could be used. Recent advances in large-scale integrated circuits and the innovative design efforts of -hp- engineers, however, have made it possible to use a digital storage technique in the 3580A. The major advantages of digital storage are:

- a. Digital storage permits the use of a standard oscilloscope CRT. Standard CRT's are rugged (a must for portable operation) and relatively inexpensive to replace.
- b. A digitally stored trace can be retained indefinitely . . . as long as the instrument is turned on. If a single

sweep is made, the trace that is generated will continue to be displayed until it is cleared or updated by a new sweep.

- c. If a trace is needed for future reference, it can be permanently stored in memory by pressing the STORE button. The permanently stored trace and a current or "refresh" trace can then be displayed simultaneously.
- d. Display adjustments are not required when the sweep parameters are changed. The digitally stored trace is automatically cleared and updated at the correct rate. The INTENSITY and FOCUS controls have the same effect as those of a regular oscilloscope. Once they are set, they do not need to be readjusted.
- e. Digital storage provides a bright, crisp flicker-free presentation. There is no blooming or display ambiguity.

### **4-115.** How A Trace Is Stored. Refer to Figure 4-19 for the following discussion.

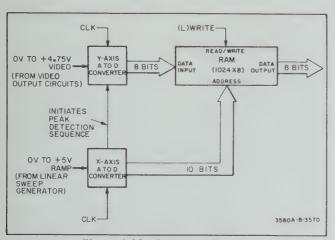


Figure 4-19. Storing A Trace.

4-116. The Digital Memory. The heart of the Digital Storage Section is a Random Access Memory (RAM) comprised of eight 1024 X 1-bit static, MOS memory elements. The RAM has 1024 storage locations or "addresses" (Ø thru 1023). The addresses are selected by a 10-bit binary code applied to the Address lines. Each address is capable of storing an 8-bit binary word applied to the Data Input lines. The input/output function of the RAM is determined by the state of the Read/Write control line. When the Read/Write line is low, the 8-bit word present on the Data Input lines is stored or "written" in the memory location selected by the Address lines. When the Read/Write line is high, the 8-bit word stored in the selected address is present on the Data Output lines. In this state, data is non-destructively "read" out of memory.

4-117. X And Y Inputs. The two major inputs to the Digital Storage section are the 0 V to +5 V frequency ramp from the Linear Sweep Generator and the 0 V to +4.75 V video signal from the Video Output Circuits. The magnitude of the ramp voltage at any given time represents a specific frequency and the magnitude of the video signal represents the signal amplitude at that frequency. To store

a trace in the Digital Memory, it is first necessary to convert these analog inputs to their corresponding binary codes. This is accomplished by the X-Axis and Y-Axis A to D (Analog to Digital) Converters.

4-119. Y-Axis A To D Converter. The 0 V to +4.75 V video input is converted to an 8-bit binary code by the Y-Axis A to D Converter. During each X-axis segment, this 8-bit word is written into the memory location addressed by the X-Axis A to D Converter. As a result, the entire memory is filled and its contents are updated by each frequency sweep. Since each address represents a specific frequency and the 8-bit word stored in a given address represents the video amplitude at that frequency, the memory, in effect, contains a point-by-point plot of the amplitude vs. frequency display.

4-120. With 1024 X-axis segments, the duration of each segment varies from approximately 100 µsec. to 1.9 seconds, depending on the SWEEP TIME setting. Since the frequency is continually changing as the ramp voltage increases, the amplitude of the video signal can vary greatly during a given segment. The amount of variation depends on the magnitude of the random noise riding on the video signal and on the slope of the response being traced. Since only one value can be used to represent the video amplitude during each segment, the peak value, being the most important parameter, is the value that is used. The Y-Axis A to D Converter is designed so that it detects and retains the peak value of the video signal during each X-axis segment. The peak detection sequence is initiated by a signal from the X-Axis A to D Converter.

**4-121.** Displaying A Stored Trace. Refer to Figure 4-20 for the following discussion.

4-122. To obtain a flicker-free stored presentation on the CRT, the memory must be read and the display must be swept at a much faster rate than that of the frequency ramp used for storing data. This rapid scan rate is provided by the Address Counter and Display Ramp Generator.

4-123. Address Counter. During the "read" phase, the X-Axis A to D Converter is disconnected and the Address lines of the RAM are switched to the Address Counter. (The switching operation is performed by a 10-bit multiplexer described in following paragraphs.) The Address Counter is a 10-bit binary counter that is incremented at

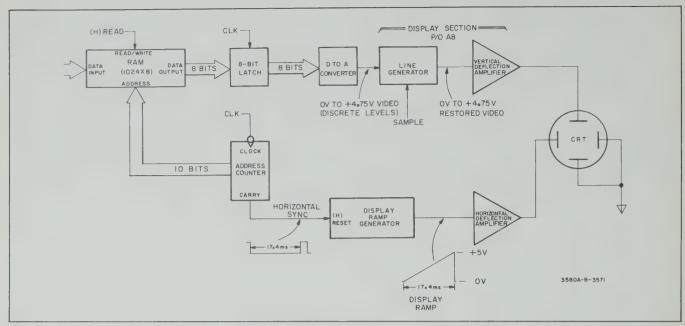


Figure 4-20. Displaying a Stored Trace.

approximately 17  $\mu$ sec. intervals. The counter continually cycles from state  $\emptyset$  (0000000000) to state 1023 (1111111111) and then resets to state  $\emptyset$ . As a result, the entire memory is read in periods of approximately 17.4 msec. When the Counter reaches state 1022, its "Carry" output goes high to reset the Display Ramp Generator.

4-124. Display Ramp Generator. The Display Ramp Generator, synchronized by the Address Counter, generates a 0 V to +5 V linear ramp which provides the horizontal sweep for the CRT display, the duration of each sweep is approximately 17.4 msec., corresponding to 1022 increments of the Address Counter. The display sweep is initiated when the Address Counter resets to state  $\emptyset$  and is terminated when the counter reaches state 1022. Addresses 1022 and 1023, are therefore, not displayed.

4-125. 8-Bit Latch. During each increment of the Address Counter, the 8-bit word present on the RAM Data Output lines is strobed into the 8-bit latch. The 8-bit word is retained by the Latch until the Address Counter is again incremented.

4-126. D To A Converter. The D to A (Digital to Analog) Converter contains a buffered resistive ladder network which converts the 8-bit word at the output of the Latch to its corresponding dc level. The output of the D to A Converter, ranging from 0 V to +4.75 V full scale, is applied to the vertical deflection plates of the CRT through the Line Generator and Vertical Deflection Amplifier.

4-127. Line Generator. The output of the D to A Converter is a series of discreet levels which, if applied to the CRT, would produce a display of dots. The Line Generator produces a variable slope ramp which draws lines between the dots to provide a fully reconstructed display.

4-128. The Overall System. During each frequency sweep, the memory contents must be updated by the frequency ramp while the trace currently in memory is being displayed. Since the read and write operations cannot be performed simultaneously, the Address lines of the RAM are rapidly switched between the X-Axis A to D Converter and the Address Counter. Figure 4-21 is a block diagram showing the overall system. Two elements not previously described are the Clock Generator and the Address Multiplexer.

4-129. Clock Generator. The Clock Generator, driven by a signal from the High Voltage Power Supply, produces ten clock outputs which synchronize the various operations of the system. The frequency of the signal applied to the Clock Generator varies from instrument to instrument and can be anywhere within the range of 55 kHz to 65 kHz. In the following discussion, the input frequency is considered to be 60 kHz which provides a base time period of about  $17~\mu \rm sec.$ 

4-130. There are four clocks that are of particular significance in the following discussion. These are: C1, C9, C10 and C10. The relationship between these clocks is shown on the block diagram. Clock C11 which synchronizes the X-Axis A to D Converter is also shown.

4-131. Address Multiplexer. The 10-bit Address Multiplexer switches the RAM Address lines between the X-Axis A to D Converter and the Address Counter. The switching input to the Multiplexer is Clock C1 which is a 60 kHz square wave. The positive half cycle of C1 is the "write" phase and the negative half cycle is the "read" phase. During the write phase of C1, the RAM is addressed by the X-Axis A to D Converter. During the read phase of C1, the RAM is addressed by the Address Counter.

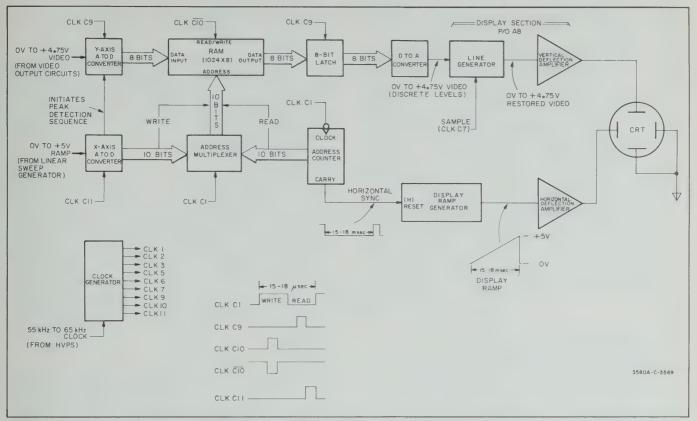


Figure 4-21. Digital Storage And Display Sections.

- **4-132.** Timing Functions. Before proceeding with the operational sequence, note the following timing functions:
- a. The Address Counter is incremented by the high to low transition that occurs when Clock C1 changes from the write phase to the read phase.
- b. Clock C9 goes high during the read phase of C1. When C9 goes high, the 8-bit word present on the RAM Data Output lines is strobed into the Latch.
- c. The Read/Write line of the RAM is controlled by Clock C10 which goes low during the write phase of C1. When C10 goes low, the 8-bit word from the Y-Axis A to D Converter is written into the RAM address selected by the X-Axis A to D Converter.
- d. The A to D Converters are clocked by C9 and C11 during the read phase of C1. This means that their outputs can change *only* during the read phase.
- **4-133. Operational Sequence.** For the operational description, the following initial conditions exist:
- a. The SWEEP MODE switch is set to SING to provide a single frequency sweep.
- b. The SWEEP TIME setting is 0.01 SEC/DIV (fastest sweep time).

- c. The ADAPTIVE SWEEP control is set to the OFF position.
- d. The CLEAR WRITE button has been pressed to clear the display and initiate a new sweep.
  - e. Clock C1 is high (write phase).
- f. The Address Counter is in state 1023 and will reset to  $\emptyset$  when C1 goes low.
- 4-134. At the beginning of the frequency sweep, the ramp input to the X-Axis A to D Converter is 0 V and the binary code at its output is Ø (000000000). During the write phase of C1, the RAM Address lines are switched to the X-Axis A to D Converter and address Ø is selected. When Clock C10 goes low, the 8-bit word from the Y-Axis A to D Converter is written into address Ø. This 8-bit word represents the video amplitude at the start frequency of the sweep. When Clock C1 goes low, the RAM Address lines are switched to the Address Counter and the Address Counter resets to  $\emptyset$ . At this point, the Display Ramp Generator is reset, the CRT sweep is at the left-hand side of the screen and RAM address Ø is selected by the Address Counter. When Clock C9 goes high, the 8-bit word stored in Address Ø is strobed into the Latch, converted to dc by the D to A Converter and applied to the vertical deflection plates of the CRT. When Clock C1 goes high, the RAM Address Lines are again switched to the X-Axis A to D Converter. With the SWEEP TIME control set to 0.01 SEC/DIV, it

takes approximately 100 usec. for the frequency ramp to increase enough to increment the X-Axis A to D Converter to state 1. In this case, only 17 µsec. have elapsed since the beginning of the sweep so the output of the X-Axis A to D Converter is still Ø. When C10 goes low, the 8-bit word from the Y-Axis A to D Converter is again written into address Ø. This 8-bit word may be the same or may differ from the one previously written into address Ø. Since the Y-Axis A to D Converter detects and retains the peak value of the video signal during each X-axis segment, the final word written into address Ø will represent the peak amplitude during the first segment. When C1 again goes low, the Address Counter is incremented to state 1 (0000000001) and RAM address 1 is selected. When C9 goes high, the contents of address 1 are strobed into the 8-bit Latch. Since the RAM was cleared at the beginning of the sweep and the X-Axis A to D Converter has not yet incremented to state 1, addresses 1 through 1023 contain all zeros. The output of the D to A Converter is, therefore, 0 V and the CRT trace at this point is at the bottom of the screen.

4-135. As the sequence continues, the Address Counter is incremented at 17  $\mu$ sec. intervals by Clock C1. During each read phase of C1, a new RAM address is selected and a new 8-bit word is strobed into the Latch, converted to dc and applied to the vertical deflection plates of the CRT. As a result, all 1022 addresses are read and the display is swept in approximately 17.4 msec.

4-136. At the end of the first display sweep, the frequency ramp will be about +0.81 V and only the first 174 RAM addresses will be filled. Thus, almost six display sweeps will have been made by the time the RAM is completely filled.

4-137. At the end of the 0.1 second single sweep, the entire memory will be filled and the frequency ramp at the input of the X-Axis A to D Converter will remain at + 5 V. At that time, the output of the X-Axis A to D Converter will be 111111111111, corresponding to RAM address 1023. During each write phase of C1, an 8-bit word will be written into address 1023. This is of no consequence because the Address Counter resets the Display Ramp Generator in state 1022 and addresses 1022 and 1023 are not displayed. The Address Counter will continue to cycle, the memory will be read and the display will be swept at a 17.4 msec. rate. The trace stored in memory will, therefore, continue to be displayed until it is cleared or updated by a new frequency sweep.

- **4-138.** Clearing A Trace. When the CLEAR/WRITE button is pressed, the following things take place:
- a. The Y-Axis A to D Converter is held in the reset state and its output is 00000000.
- b. The RAM Address lines are switched to the Address Counter during both the read and write phases of C1.

c. As the Address Counter scans the memory, all zeros are written in each address and the entire memory is cleared in 17.4 msec.

4-139. Store Function. A major feature of the Digital Storage Section is the "store function" which allows a trace to be permanently stored in memory for future reference. The permanently stored trace can be blanked from the display and then recalled at any time for comparison with the current or "refresh" trace.

4-140. To permanently store a trace, the operator presses the front panel STORE button. This initiates a sequence of operations in which the trace currently in memory is processed and reloaded into 512 of the 1024 memory locations. The remaining half of the memory is used for the refresh trace. To display both traces, the display sweep rate is doubled to provide two 8.7 msec. sweeps. During the first display sweep, the Address Counter scans the memory locations containing the refresh trace. It then recycles and scans the memory locations containing the permanently stored trace. As a result, the two traces are displayed alternately in a 17.4 msec. period.

4-141. Figure 4-22 is an expanded block diagram showing the additional circuitry needed to implement the store function. A 4-state digital controller called the "Store Function Controller" is used to direct the store operation. The ASM chart for the Store Function Controller is shown in Figure 4-23. Other elements used only for the store function are the Store Multiplexer, the 8-Bit Adder and the Write Control circuit.

4-142. Store Multiplexer. The Store Multiplexer switches the RAM Data Input lines between the Y-Axis A to D Converter and the "Q" outputs of the 8-Bit Latch. The switching inputs to the Store Multiplexer are the SFL and TRA instructions from the Store Function Controller. During normal operation, the SFL and TRA instructions are not given and the RAM Data Input lines are always connected to the Y-Axis A to D Converter. When the SFL or TRA instruction is given during the store sequence, the RAM Data Input lines are switched to the "Q" outputs of the 8-Bit Latch and the Y-Axis A to D Converter is disconnected.

4-143. 8-Bit Adder. In State 1 of the store sequence, the Adder is used to compare the 8-bit word on the RAM Data Output lines to the 8-bit word at the output of the Latch. The comparison is made using one's compliment addition i.e., the "Q" outputs of the Latch are the compliments of the "Q" outputs. If the numerical value of the word at the output of the RAM is greater than that of the word at the output of the Latch, the "Carry" output of the Adder goes high, supplying a "Write Enable" command to the Write Control circuit.

4-144. Write Control Circuit. During normal operation, the "Set" input to the write control flip-flop is high, forcing the "Q" output to be high. Clock C10 is then present at the output of the NAND gate and data is written into memory during each write phase of Clock C1. When

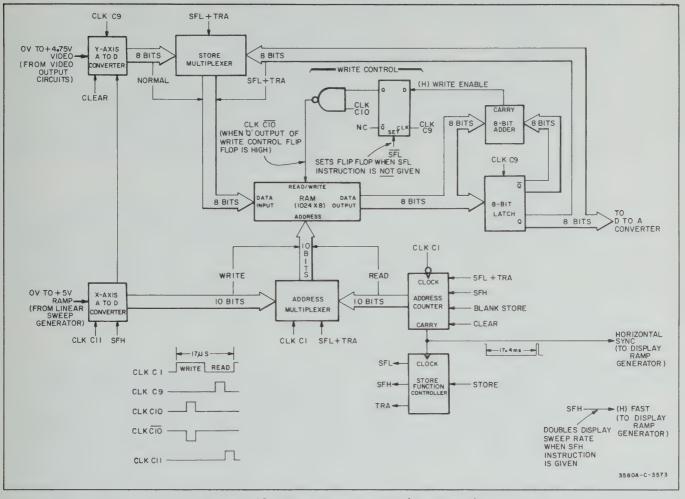


Figure 4-22. Digital Storage Section (Store Mode).

the SFL instruction is given during State 1 of the store sequence, the "Set" input of the flip-flop is low and the "Q" output goes high only if the Write Enable line from the 8-Bit Adder is high when the flip-flop is clocked by the positive going edge of C9. If a Write Enable command is given, the "Q" output will be high and data will be written into memory by  $\overline{C10}$  during the next write phase. If a Write Enable command is not given, the "Q" output of the flip-flop will be low and Clock  $\overline{C10}$  will be inhibited during the next write phase.

4-145. Store Sequence (State 1). Refer to Figure 4-23. When the STORE button is initially pressed, the Store Function Controller is in State  $\emptyset$  where no instructions are given. It remains in State  $\emptyset$  until the Address Counter completes its current cycle and resets to  $\emptyset$ . The Controller then increments to State 1.

4-146. The purpose of State 1 is to condense the trace currently in memory and store it in the 512 memory locations where the Least Significant Bit (LSB) of the address is a logical "0" (addresses  $\emptyset$ , 2, 4, 6, etc.). To accomplish this, the Address Counter is incremented from state  $\emptyset$  to state 1023. At each increment, the contents of the present address and the preceding address are compared

and the larger value is stored in the appropriate memory location. Storing only the larger of the two values ensures that the peak value of each response will be retained in the permanently stored trace.

4-147. In State 1, the Store Function Controller gives the SFL (Sweep Flag) instruction which performs the following functions:

a. Overrides the Clock C1 input to the Address Multiplexer, causing the Multiplexer to remain switched to the Address Counter. The Address lines of the RAM are, therefore, controlled by the Address Counter during both the read and write phases of Clock C1.

b. Forces the LSB of the Address Counter to a logical "0" during the write phase of Clock C1. This means that information can only be written into the memory locations where the LSB of the address is a logical "0." The contents of addresses where the LSB is a logical "1" are left unaltered.

c. Switches the Store Multiplexer so that the Data Input lines of the RAM are connected to the "Q" outputs of the 8-Bit Latch. In this state, the Y-Axis A to D Converter is disconnected.

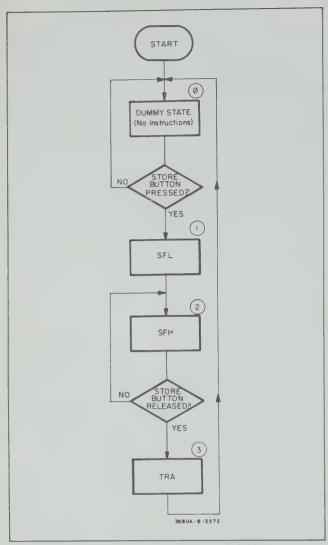


Figure 4-23. Store Function ASM Chart.

- d. Enables the Write Control circuit. During normal operation, the Write Control circuit is disabled and Clock  $\overline{C10}$  is present at the output of the NAND gate. With the Write Control circuit enabled, Clock  $\overline{C10}$  is inhibited unless a Write Enable command has been generated by the 8-Bit Adder.
- 4-148. Figure 4-24 shows the equivalent circuit during State 1. Elements not shown can be disregarded.
- 4-149. The State 1 sequence begins with the following conditions:
- a. Clock C1 has just completed the transition from the write phase to the read phase.
  - b. The Address Counter is in state 1 (000000001).
- c. The 8-bit word from the preceding address  $(\emptyset)$  is at the output of the Latch.
- d. The 8-bit word stored in the present address (1) is on the Data Output lines of the RAM.

- 4-150. Just before Clock C9 goes high, the 8-bit word on the RAM Data Output lines (present address) is compared to the 8-bit word at the output of the Latch (preceding address) by the 8-Bit Adder. If the numerical value of the 8-bit word in the present address is greater than that of the preceding address, the Adder generates a Write Enable command which is applied to the Write Control circuit. If a Write Enable command is generated, data will be written into memory during the next write phase. When Clock C9 goes high, the 8-bit word on the RAM Data Output lines (address 1) is strobed into the Latch.
- 4-151. When Clock C1 goes into the write phase, the Address Counter remains in state 1 (0000000001) but because its LSB is forced to a logical "0," RAM address  $\emptyset$  (0000000000) is selected. If a Write Enable command was generated during the read phase, the 8-bit word from address 1 (now at the output of the Latch) is written into address  $\emptyset$ . If a Write Enable command was not generated, Clock  $\overline{C10}$  is inhibited and the contents of address  $\emptyset$  are left unchanged.
- 4-152. When Clock C1 again goes into the read phase, the Address Counter is incremented to state 2 (000000010). At this time, the 8-bit word from address 1 is still at the output of the Latch and the 8-bit word stored in address 2 is on the RAM Data Output lines. If the 8-bit word in address 2 is greater than that of address 1, a Write Enable command will be generated and, during the next write phase of C1, the contents of address 2 will be written back into address 2, leaving address 2 unchanged. Moreover, if the 8-bit word in address 2 is less than that of address 1, a Write Enable command will not be generated and the contents of address 2 will still be left unchanged. This is an important point. Even though the 8-bit word in each address is compared to that of the address that is one count higher, only alternate comparisons have any effect. For example, addresses Ø and 1 are compared and the largest value is written into address Ø addresses 1 and 2 are compared and address 2 is left unchanged, addresses 2 and 3 are compared and the largest value is written into address 2, etc.
- 4-153. The comparison sequence continues until the Address Counter reaches state 1023 and resets. At that time, the Store Function Controller increments to State 2 where it remains until the STORE button is released.
- 4-154. State 2. In State 2, the SFH (Sweep Flag Hold) instruction is given and the system returns to its normal mode of operation with the following exceptions:
- a. The LSB of the X-Axis A to D Converter is forced to a logical "1." Since the X-Axis A to D Converter addresses the RAM during the write phase of C1, new information is written only in addresses where the LSB is a logical "1" (addresses 1, 3, 5, 7, etc.). Addresses containing the permanently stored trace are, therefore, left undistrubed.

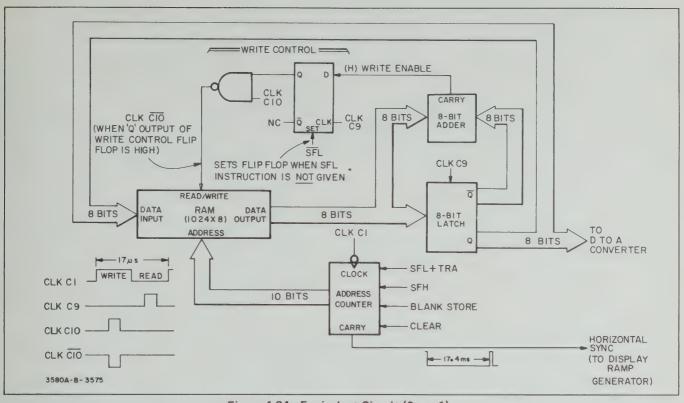


Figure 4-24. Equivalent Circuit (State 1).

- b. The Address Counter is switched so that it first scans the addresses where the LSB is a "1" (refresh trace). It then recycles and scans the addresses where the LSB is a "0" (permanently stored trace).
- c. The SFH instruction (labled (H) FAST) is applied to the Display Ramp Generator. This doubles the display sweep rate, providing one 8.7 msec. sweep for each set of addresses. As a result, both the refresh trace and the permanently stored trace appear on the CRT.
- **4-155.** Clear/Write Button. When the CLEAR/WRITE button is pressed during State 2, the following things take place:
- a. The Y-Axis A to D Converter is held in the reset state and its output is 00000000.
- b. The RAM address lines are switched to the Address Counter during the 8.7 msec. periods when it is scanning the addresses containing the refresh trace.
- c. As the Address Counter scans the addresses containing the refresh trace, all zeros are written into memory during the write phases of C1. As a result, the refresh trace is cleared from memory and the permanently stored trace is not disturbed.
- 4-156. Blank Store Button. When the BLANK STORE button is pressed, the LSB of the Address Counter is forced to a logical "1." The Address Counter, therefore, continually scans the addresses containing the refresh trace and the

permanently stored trace is not displayed. When the BLANK STORE button is released, the permanently stored trace returns to the display.

- **4-157.** State 3. When the STORE button is released and the Address Counter resets to  $\emptyset$ , the Store Function Controller is incremented to State 3. The purpose of State 3 is to clear the permanently stored trace by filling the memory with the refresh trace. This is accomplished by loading the contents of addresses where the LSB is 1 into addresses where the LSB is  $\emptyset$ .
- 4-158. In State 3, the Store Function Controller gives the TRA (Transfer) instruction which performs the following functions:
- a. Overrides the Clock C1 input to the Address Multiplexer causing the RAM Address lines to remain switched to the Address Counter.
- b. Forces the LSB of the Address Counter to a logical "0" during the write phase of Clock C1. This means that information can only be written in addresses where the LSB is a logical "0."
- c. Switches the Store Multiplexer so that the Data Input lines of the RAM are connected to the "Q" outputs of the 8-Bit Latch.

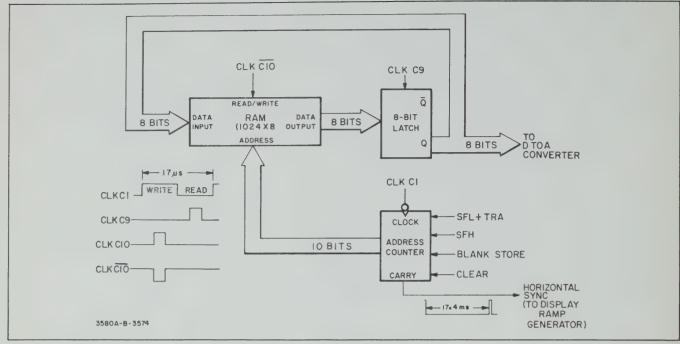


Figure 4-25. Equivalent Circuit (State 3).

4-159. Figure 4-25 shows the equivalent circuit during State 3. Note that the 8-Bit Adder and the Write Control circuit are not used.

4-160. The State 3 sequence begins with the following conditions:

a. Clock C1 has just made the transition from the write phase to the read phase.

b. The Address Counter is in state  $\emptyset$  and RAM address  $\emptyset$  is selected.

4-161. When Clock C9 goes high during the read phase, the 8-bit word from address Ø is strobed into the Latch. When Clock C1 goes into the write phase, the Address Counter remains in state Ø and, when C10 goes low, the 8-bit word from address Ø is written back into address Ø, leaving address Ø unchanged. When Clock C1 again goes into the read phase, the Address Counter is incremented to State 1 and, when C9 goes high, the 8-bit word from address 1 is strobed into the Latch. During the next write phase of C1. the Address Counter is still in state 1 (0000000001) but because its LSB is forced to a logical "0," RAM address Ø (000000000) is selected and the 8-bit word from address 1 (now at the output of the Latch) is written into address 0. As the sequence continues, the contents of address 3 are written into address 2, the contents of address 5 are written into address 4, etc. When the Address Counter reaches state

1023 and resets, the Store Function Controller resets to State  $\emptyset$  and the system returns to its normal mode of operation. At this point, each pair of addresses ( $\emptyset$  and 1, 2 and 3, 4 and 5, etc.) contains the same information. Since the addresses are now read sequentially as the Address Counter increments from state  $\emptyset$  to state 1023, the video amplitude on the display is the same for each pair of X-axis segments. Because of this redundancy, a series of dots appears on the display (Figure 4-26). The dots are automatically cleared when the memory contents are updated by a new frequency sweep.

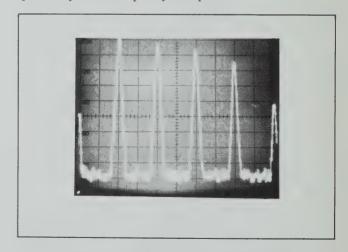


Figure 4-26. Store Button Released.

# WARNING

These servicing instructions are for use by trained service personnel only. To avoid electrical shock, do not perform any servicing other than that contained in the operating instructions unless you are qualified to do so.



# SECTION V MAINTENANCE

### 5-1. INTRODUCTION.

5-2. This section contains Performance Tests (Paragraph 5-5) and Adjustment Procedures (Paragraph 5-48) for the Model 3580A Spectrum Analyzer. Troubleshooting information is presented in Section VII, along with the Schematic Diagrams.

### 5-3. RECOMMENDED TEST EQUIPMENT.

5-4. The test equipment that is recommended for maintaining the Model 3580A is listed in Table 5-1. The equipment is designated as to its use for Performance Tests, Adjustments or Troubleshooting.

#### 5-5. PERFORMANCE TESTS.

- 5-6. The following Performance Tests are procedures that can be used to verify that the 3580A is operating properly and meets the specifications listed in Table 1-1. These procedures can be used for incoming quality control inspection, to check specifications after a repair or for routine maintenance. Where possible, the Performance Tests call out the proper adjustment in the Adjustment Procedures. Since adjustments interact, it is important to follow the procedures carefully.
  - a. FREQUENCY TESTS (Paragraph 5-9).
  - b. SWEEP TESTS (PARAGRAPH 5-13).
  - c. AMPLITUDE TESTS (Paragraph 5-18).
  - d. BANDWIDTH TESTS (Paragraph 5-28).
- e. DYNAMIC RANGE TESTS (NOISE TESTS) (Paragraph 5-30).
- f. IF FEEDTHRU and ZERO BEAT RESPONSE TESTS (Paragraph 5-36).
  - g. INPUT IMPEDANCE TESTS (Paragraph 5-38).
  - h. OUTPUT TESTS (Paragraph 5-40).
- i. BALANCED INPUT TESTS (Option 002 only) (Paragraph 5-44).

#### 5-7. Test Card.

5-8. A Performance Test Card is provided at the end of this section for your convenience in recording the performance of the Model 3580A during Performance Tests. This card

can be removed from the manual and used as a permanent record of the incoming inspection or of a routine performance test. The Performance Test Card may be reproduced without written permission from Hewlett-Packard.

#### NOTE

Always allow one hour continuous warm-up before attempting any tests.

### 5-9. Frequency Tests.

5-10. These tests verify part of the Frequency Characteristic Specifications listed in Table 1-1. If, for any reason, the instrument will not pass these tests, perform the Sweep Alignment and Dial Calibration (Paragraph 5-63) of the Adjustment Procedures.

### 5-11. Range and Frequency Dial Accuracy Test.

a. Position the following front panel controls:

ADAPTIVE SWEEP	OFF
DISPLAY All pushbuttons rel	eased
AMPLITUDE MODE LOG 10 dBv	/DIV
AMPLITUDE REF LEVELNOR	MAL
$dBv/LIN - dBm 600 \Omega \dots dBv$	/LIN
INPUT SENSITIVITY	CAL
VERNIER (Amplitude)	CAL
(Fully	CW)
FREQUENCY	) kHz
START - CTRST	ART
RESOLUTION BANDWIDTH 3	30 Hz
DISPLAY SMOOTHING	MIN
FREQ. SPAN/DIV	0 Hz
SWEEP TIME/DIV 0.2	SEC
SWEEP MODE	

Option 002: Set dBm 900  $\Omega/\text{LIN}$  - dBm 600  $\Omega$  switch to dBm 900  $\Omega$ ; set INPUT MODE switch to UNBAL.

b. Adjust the front panel ZERO CAL control for a maximum display indication. Readjust the FREQUENCY control for 10, 20, 30, 40 and 50 kHz. A peak response should occur for each of these settings (± .1 kHz) to verify the Range Specifications and Frequency Dial Accuracy Specifications (20°C to 30°C) given in Table 1-1.

### **NOTE**

As the frequency of the peak response is approached, pull out the knob for easier tuning.

Table 5-1. Recommended Test Equipment.

			USAGE		
INSTRUMENT	REQUIRED CHARACTERISTICS	Performance Checks	Adjustments	Troubleshooting	RECOMMENDED MODEL
Digital Multimeter	DC Function: Full scale ranges: 1 V, 10 V, 100 V Resolution: 4 digits Input Impedance: 10—12 MΩ Accuracy: ± .1% of reading AC Function: Response: average Frequency Range: 45 Hz−100 kHz Full Scale Range: 1 V, 10 V, 100 V Resolution: 4 digits Input Impedance: ≥ 10 MΩ, ≤ 100 p Accuracy: ± 1% of reading	X	x	×	-hp- 34740/34702
Oscilloscope	Sensitivity: .005 V/DIV Sweep: .005 μsec/DIV to .1 sec/div Frequency: 0 to 10 MHz Input Impedance: 1 MΩ, 25 pF Dual Trace (troubleshooting only)		×	×	-hp- 180C/D with -hp- 1801A Veritcal Amplifier and 1820C Time Base
Voltage Dividers for Oscilloscope (2)	Division Ratio 10:1 Impedance: 10 MΩ, 10 pF		×	x	-hp- 10004B
Electronic Counter	Function: Frequency and Time Interval Frequency Range: 10 Hz to 10 MHz Resolution: 6 digits Sensitivity: 0.1 V rms	c	x	X	-hp- 5326A
Frequency Synthesizer (50 ohms)	Frequency Range: 10 Hz to 1.5 MHz Amplitude Range: (-67.99 dBm 50 $\Omega$ to +26.99 dBm 50 $\Omega$ )  Amplitude Accuracy: $\pm$ .1 dB Amplitude Resolution: .01 dB Frequency Resolution: .1 Hz	×	×	x	-hp- 3320B
50 Ohm Termination for Synthesizer	1 watt 50 ohms ± .1 Ω	×	×	×	-hp- 11048C
Distortion Analyzer	Fundamental Frequency Range: 10 Hz to 100 kHz Distortion Measurement Accuracy: ± 10% for greater than .3% distortion	x			-hp- 333A
Bandpass Filter	Center of Bandpass at 5 kHz Output Distortion: (with Frequency Synthesizer): > 90 dB dov	vn X			White Model 2640
DC High Voltage Probe Calibrated to 1000 V dc	Range: 5 kV DC Standard Accuracy: ± 1%				-hp- 11045A and -hp- 740B
standard OR Precision High Voltage Probe	OR Accuracy: ± ,1% Range: 5 kV		X	×	OR -hp- 3440A-K05 Probe and -hp- 3440A DVM
1 kΩ Resistor	1% film resistor	×			-hp- 0757-0280
(2) 453 Ω resistors	1% film resistor	×			-hp- 0698-3510
10 kΩ Resistor	10% carbon or film resistor			×	-hp- 0757-0442
550 Ω Resistor	10% carbon or film resistor	×			-hp- 0698-4456
1 MΩ Resistor	1% film resistor	x			-hp- 0757-0344
Logic Clip	Able to detect TTL HIGH and LOW levels for DUAL IN-LINE configuration, 16 pins			×	-hp- 10528A

### 5-12. Display Accuracy Tests.

a. Reposition the following front panel controls.

FREQUENCY	 	00.0 kHz
RESOLUTION BANDWIDTH	 	. 300 Hz
FREQ. SPAN/DIV	 	5 KHz

b. The 10 kHz CAL signal and its harmonics should be repetitively swept and appear on the display as shown by Figure 5-1. The separation between the Zero Response and 50 kHz harmonic should be 10 major divisions  $\pm$  1 minor division. The separation between any two adjacent responses should be 2 major divisions  $\pm$  .2 minor divisions. Momentarily push and release DISPLAY - STORE, watching the display to verify that the STORE and NON-STORE traces appear in the same position.

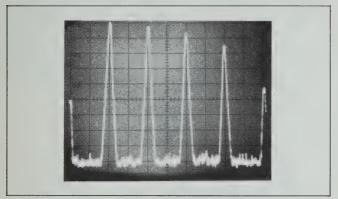


Figure 5-1. 10 kHz CAL Signal.

### 5-13. Sweep Tests.

5-14. These tests verify the Sweep Characteristics Specifications given in Table 1-1. If the instrument fails the Frequency Span Tests (Paragraph 5-15), perform the Sweep Alignment and Dial Calibration (Paragraph 5-63) of the Adjustment Procedures. If it fails only the Log Sweep Test (Paragraph 5-16), perform only the Log Sweep Adjustments (Paragraph 5-67) of the Sweep Alignment and Dial Calibration. All sweep time calibration is done with a factory selected resistor. If the instrument will not pass the Sweep Time Tests (Paragraph 5-17), refer to Section VII for additional information.

### Equipment Required:

Electronic Counter (-hp- Model 5326A)

### 5-15. Frequency Span Test.

a. Position the following front panel controls: (Only those controls printed in BOLD require a change from the previous test.)

ADAPTIVE SWEEP OF	F
DISPLAY All pushbuttons release	ed
AMPLITUDE MODE LOG 10 dBv/DI	V
AMPLITUDE REF LEVELNORMA	ιL

$dBv/LIN - dBm 600 \Omega$
VERNIER (Amplitude)CAL
(Fully CW)
FREQUENCY 00.0 kHz
START - CTRSTART
RESOLUTION BANDWIDTH 300 Hz
DISPLAY SMOOTHING MIN
FREQ. SPAN/DIV 5 Hz
SWEEP TIME/DIV 0.2 SEC
SWEEP MODE

Option 002: Set dBm 900  $\Omega/LIN$  - dBm 600  $\Omega$  switch to dBm 900  $\Omega$ ; set INPUT MODE switch to UNBAL.

### b. Adjust MANUAL VERNIER full CCW.

c. Set the electronic counter to the frequency mode and adjust the time base/multiplier for a measurement of 1 MHz with 6 digits of resolution (1000.00 kHz). Adjust for maximum input sensitivity and either a zero trigger level or Preset. For the -hp- 5326A Counter, the controls should be set to:

Sample Rate: Fast
Function: Freq. A
Multiplier: 10<sup>6</sup>
Channel A Slope +
AC

Atten: X1

Level: Preset

BNC Input: Sep.

- d. Connect the counter Channel A input to the L.O. OUTPUT terminal on the back panel of the 3580A.
- e. Adjust the ZERO CAL for approximately a 1000.00 kHz reading on the counter. Adjust the FRE-QUENCY dial (pulled out for fine tuning) for a 1000.00 kHz indication on the counter.
- f. Adjust MANUAL VERNIER full CW. The counter indication should be 1000.50 kHz ± .01 kHz.
- g. Readjust MANUAL VERNIER full CCW. Reposition FREQ. SPAN/DIV 10 Hz.
- h. Readjust the FREQUENCY dial (pulled out for fine tuning) for a 1000.00 kHz indication on the counter.
- i. Adjust MANUAL VERNIER full CW. The counter indication should be  $1001.00 \text{ kHz} \pm .02 \text{ kHz}$ .

j. Continue this procedure for the remaining FREQ. SPAN/DIV settings. Refer to Table 5-2 for the proper tolerances.

Table 5-2. Frequency Span Test.

	COUNTE	R READING
FREQ. SPAN/DIV	MANUAL VERNIER FULL CCW	MANUAL VERNIER FULL CW
5 Hz	1000.00 kHz	1000.50 kHz ± .01 kHz
10 Hz	1000.00 kHz	1001.00 kHz ± .02 kHz
20 Hz	1000.00 kHz	1002.00 kHz ± .04 kHz
50 Hz	1000.00 kHz	1005.00 kHz ± .10 kHz
.1 kHz	1000.00 kHz	1010.00 kHz ± .20 kHz
.2 kHz	1000.00 kHz	1020.00 kHz ± .40 kHz
.5 kHz	1000.00 kHz	1050.00 kHz ± 1.00 kHz
1 kHz	1000.00 kHz	1100.00 kHz ± 2.00 kHz
2 kHz	1000.00 kHz	1200.00 kHz ± 4.00 kHz
5 kHz		
(checked in		
Para 5-16)		

### 5-16. Log Sweep Test.

a. Reposition the controls as follows:

INPUT SENSITIVITY					CAL
RESOLUTION BANDWIDTH		٠			30 Hz
SWEEP MODE		Ī	.0	G	ZERO

- b. Momentarily press DISPLAY CLEAR WRITE.
- c. Adjust the ZERO CAL control for a maximum indication on the leftmost display graticule.
  - d. Reposition the controls as follows:

RESOLUTION	BANDWIDTH	 300 Hz
SWEEP MODE		LOG

Allow time for three complete sweeps.

e. Verify that the 20 kHz harmonic of the internal CAL signal falls on the proper graticule (± 1 minor division). If the instrument will not pass this test, but passes all previous tests, perform only the Log Sweep Adjustments (Paragraph 5-67) of the Adjustment Procedures.

### 5-17. Sweep Time Tests.

a. Reposition the controls as follows:

SWEEP TIME/DIV					٠		0.01 SEC
SWEEP MODE							SING

Momentarily press:

b. The display should be erased, and then swept once. Remembering the sweep time, reposition the controls as follows:

SWEEP TIME/DIV .......... 0.02 SEC

c. Again, press:

The sweep time should appear slower.

d. Repeat this procedure for all sweep times, always looking for progressively slower sweep rates. On the slowest sweep rates, it will not be necessary to complete a full sweep before switching to the next SWEEP TIME/DIV. Let the instrument sweep only as long as is necessary to monitor the rate. A more accurate method for measuring sweep time is given in Paragraph 7-39.

### 5-18. Amplitude Tests.

5-19. These tests verify the Amplitude Specifications given in Table 1-1. Amplitude accuracy must be determined before the Bandwidth Specifications can be tested. Since the IF Filter Alignment (Paragraph 5-70) interacts with the Amplitude Accuracy, it is important that the IF Filter Alignment be performed first if the instrument will not pass any of the Amplitude Accuracy Tests. The Amplitude Tests should then be repeated, and if the instrument still fails these tests, then perform the Amplitude Adjustments (Paragraph 5-74) of the Adjustment Procedures.

### NOTE

There are no adjustments for Amplitude Reference Level Tests (Linear and Log Mode). If the instrument passes all Amplitude Tests except one or both of these, refer to Section VII for troubleshooting information.

### Equipment Required:

Frequency Synthesizer (-hp-Model 3320B, 50 ohms) 50 Ohm Termination (-hp-11048C) Digital Multimeter (-hp-Model 34740/34702)

### 5-20. Bandwidth Switching Accuracy Tests.

a. Position the following front panel controls: (Only those controls printed in BOLD require a change from the previous tests.)

ADAPTIVE SWEEPOFF
DISPLAY All pushbuttons released
AMPLITUDE MODE LOG 1 dBv/DIV
AMPLITUDE REF LEVELNORMAL
$dBv/LIN - dBm 600 \Omega \dots dBv/LIN$
INPUT SENSITIVITY 0 dBV
VERNIER (Amplitude)CAL
(Fully CW)
FREQUENCY 10.0 kHz
START - CTRCTR
RESOLUTION BANDWIDTH 300 Hz
DISPLAY SMOOTHINGMIN
FREQUENCY SPAN/DIV 5 Hz
SWEEP TIME/DIV 0.1 SEC
SWEEP MODE

Option 002: Set dBm 900  $\Omega/LIN$  - dBm 600  $\Omega$  switch to dBm 900  $\Omega$ ; set INPUT MODE switch to UNBAL.

b. Connect a properly terminated frequency synthesizer to the 3580A INPUT and adjust the source for a 10 kHz, 0 dBV output level (-20 dBm 900  $\Omega$  for instruments with Option 002).

#### NOTE

See Table 5-3 for the proper level to use with your source. See Figure 5-2 for the proper hookup with an -hp- 3320B Frequency Synthesizer.

Table 5-3. Conversion Table.

3580A INPUT SIGNAL LEVEL	3320B or OTHER 50 OHM SOURCE	ABSOLUTE VOLTAGE
+ 10 dBv	+ 23.01 dBm	3.162 volts
+ 10 dBm 900 Ω	+ 22.55 dBm	3 volts
0 dBv	+ 13.01 dBm	1 volts
0 dBm 900 Ω	+ 12.55 dBm	.949 volts
- 10 dBv	+ 3.01 dBm	.3162 volts
- 10 dBm 900 Ω	+ 2.55 dBm	.3000 volts
- 20 dBv	- 6.99 dBm	.1 volts
- 20 dBm 900 Ω	- 7.45 dBm	.0949 volts
- 30 dBv	- 16.99 dBm	.03162 volts
- 30 dBm 900 Ω	- 17.45 dBm	.03 volts
- 40 dBv	- 26.99 dBm	.01 volts
- 40 dBm 900 Ω	- 27.45 dBm	.095 volts
- 50 dBv	- 36.99 dBm	3162 mV
- 50 dBm 900 Ω	- 37.45 dBm	3 mV
- 60 dBv	- 46.99 dBm	1 mV
- 60 dBm 900 Ω	- 47.45 dBm	.95 mV
- 70 dBv	- 56.99 dBm	.3162 mV
- 70 dBm 900 Ω	- 57.45 dBm	.3 mV
- 80 dBv	- 66.99 dBm	.1 mV
- 80 dBm 900 Ω	- 67.99 dBm	.095 mV

- c. By alternately pressing and releasing DISPLAY-CLEAR WRITE while adjusting MANUAL VERNIER, center the display indication (a narrow spike). Adjust ZERO CAL for a peak display of this spike.
- d. Adjust the front panel ZERO CAL for a peak indication.

- e. Adjust the front panel VERNIER (Amplitude) for a -1 dB display indication. Note: The display indication is calibrated 1.0 dB per major division.
  - f. Reposition the following front panel control:

RESOLUTION BANDWIDTH . . . . . 100 Hz

- g. Adjust the front panel ZERO CAL for a peak display indication. The display indication should be-1 dB ± .5 dB.
  - h. Reposition the following front panel control:

RESOLUTION BANDWIDTH ..... 30 Hz

- i. Slowly adjust MANUAL VERNIER for a peak display indication. The peak indication should be 1 dB ± .5 dB. Momentarily press DISPLAY CLEAR WRITE.
  - j. Reposition the following front panel control:

RESOLUTION BANDWIDTH . . . . . . 10 Hz

- k. Readjust MANUAL VERNIER for a peak display indication. The peak indication should be 1 dB ± .5 dB. Momentarily press DISPLAY CLEAR WRITE.
  - 1. Reposition the following front panel control:

RESOLUTION BANDWIDTH . . . . . . 3 Hz

- m. Slowly readjust MANUAL VERNIER for a peak display indication. The peak indication should be -1 dB ± .5 dB. Momentarily press DISPLAY CLEAR WRITE.
  - n. Reposition the following front panel control:

RESOLUTION BANDWIDTH . . . . . 1 Hz

o. Very slowly readjust MANUAL VERNIER for a peak display indication. The peak indication should be -1 dB  $\pm$  1 dB.

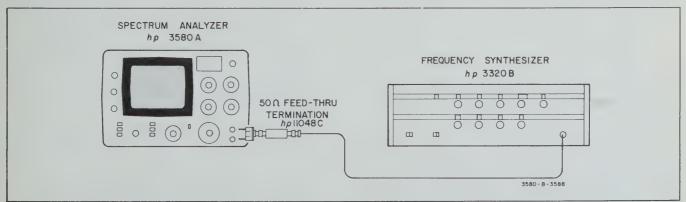


Figure 5-2. Proper Hookup.

### 5-21. Log Amplitude Display Accuracy Tests.

a. Reposition the following front panel controls:

VERNIER (Amplitude) . . . . . . . . . CAL AMPLITUDE MODE . . . . LOG 10 dB/DIV RESOLUTION BANDWIDTH . . . . . 10 Hz

- b. By alternately pressing and releasing DIS-PLAY-CLEAR WRITE while adjusting MANUAL VERN-IER, center the display indication (a narrow spike).
- c. Adjust the CAL 10 KHz for a full scale 0 dB display indication of the spike.
- d. Adjust the signal source to the levels indicated by Table 5-4. Check the display for proper level. (See Table 5-3 for the proper Input Level setting to use on your signal source.)

#### NOTE

For the - 60 dB, - 70 dB and - 80 dB readings, readjust MANUAL VERNIER for a peak display indication.

Table 5-4. Log Amplitude Tests.

INPUT LEVI								
STANDARD INSTRUMENT	OPTION 002 900 Ω INSTRUMENT	DISPLAY INDICATION (0 dB = full scale)						
- 10 dBv - 20 dBv - 30 dBv - 40 dBv - 50 dBv - 60 dBv - 70 dBv - 80 dBv	- 10 dBm - 20 dBm - 30 dBm - 40 dBm - 50 dBm - 60 dBm - 70 dBm - 80 dBm	- 10 dB ± 2 dB - 20 dB ± 2 dB - 30 dB ± 2 dB - 40 dB ± 2 dB - 50 dB ± 2 dB - 60 dB ± 2 dB - 70 dB ± 2 dB - 80 dB ± 2 dB						

### 5-22. Linear Amplitude Display Accuracy Tests.

a. Reposition the following front panel controls:

AMPLITUDE MODE .....LINEAR

- b. Adjust the signal source for a 1 volt (0 dBv) output (See Table 5-3 for the proper setting to use on your source). Adjust MANUAL VERNIER for a peak display indication.
- c. Adjust the CAL 10 KHz for a full scale 1 volt display indication. Momentarily press DISPLAY CLEAR WRITE.
- d. Adjust the signal source to the levels indicated by Table 5-5. Check that the display is accurate for each setting.

Table 5-5. Linear Amplitude Tests.

INPUT LEVEL (10 KHz)	3320B or OTHER 50 OHM SOURCE	DISPLAY INDICATION (1 volt = full scale)
.9 V	12.10 dBm	.9 V ± .02 V
.8 V	11.07 dBm	.8 V ± .02 V
.7 V	9.91 dBm	.7 V ± .02 V
.6 V	8.51 dBm	.6 V ± .02 V
.5 V	6.99 dBm	.5 V ± .02 V
.4 V	5.05 dBm	.4 V ± .02 V
.3 V	2.55 dBm	.3 V ± .02 V
.2 V	97 dBm	.2 V ± .02 V
.1 V	- 6.99 dBm	.1 V ± .02 V

### 5-23. Amplitude Reference Level Tests (Linear Mode).

a. Reposition the following front panel control:

- b. Adjust the synthesizer for a 10 kHz, 1 volt output (+13.01 dBm, 50 ohm).
- c. Readjust MANUAL VERNIER for a peak display indication. Adjust VERNIER (Amplitude) for a display indication at 90% of full scale.
- d. Adjust the frequency synthesizer and AMPLITUDE REF LEVEL to the values given in Table 5-6. Check for proper display level.

### NOTE

If the instrument fails this test, see Section VII for troubleshooting information. There are no adjustments for this specification.

Table 5-6. Amplitude Ref Level Tests (Linear Mode).

INPUT LEVEL (10 KHz)	3320B or OTHER 50 OHM SOURCE	AMP REF LEVEL	DISPLAY INDICATION (% of Full Scale)
1 V	+,13.01 dBm	Normal	90% (CAL)
200 mV 100 mV 20 mV 10 mV 2 mV 1 mV .2 mV	99 dBm - 6.99 dBm - 20.99 dBm - 26.99 dBm - 40.99 dBm - 46.99 dBm - 60.99 dBm	- 10 - 20 - 30 - 40 - 50 - 60 - 70	90% ± 1.5 minor div. 90% ± 1 major div.

### 5-24. Amplitude Reference Level Tests (Log Mode).

### NOTE

If the instrument fails this test, see Section VII for troubleshooting information. There are no adjustments for this specification.

a. Reposition the following front panel controls:

AMPLITUDE MODE . . . . . . LOG 10 dB/DIV AMPLITUDE REF LEVEL . . . . . NORMAL

- b. Connect the digital multimeter (DC mode, 100 volt range) to the Y AXIS output of the 3580A.
- c. Adjust the signal source for a -70 dB V output (-70 dBm 900  $\Omega$  for Option 002). (See Table 5-3 for proper levels.) Adjust the MANUAL VERNIER and ZERO CAL for a peak display. Adjust VERNIER (Amplitude) for a 1.50 volt  $\pm$  .01 volt reading on the multimeter.
- d. Adjust the AMPLITUDE REF LEVEL switch to the settings given in Table 5-7. Check for the proper multimeter reading.

### **NOTE**

MANUAL VERNIER may have to be readjusted to insure a peak display indication.

Table 5-7. Amplitude Ref. Level Tests (Log Mode).

INPUT LEV	EL (10 KHz)		
STANDARD OPTION 002 INSTRUMENT 900 Ω		AMPLITUDE REF. LEVEL	MULTIMETER READING
- 70 dBv - 70 dBv - 70 dBv - 70 dBv - 70 dBv - 70 dBv - 70 dBv	- 70 dBm - 70 dBm - 70 dBm - 70 dBm - 70 dBm - 70 dBm - 70 dBm	- 10 dB - 20 dB - 30 dB - 40 dB - 50 dB - 60 dB - 70 dB	2.00 V ± .02 V 2.50 V ± .02 V 3.00 V ± .03 V 3.50 V ± .03 V 4.00 V ± .04 V 4.50 V ± .04 V 5.00 V ± .05 V

e. Disconnect the multimeter from the 3580A.

### 5-25. Input Attenuator Tests.

a. Reposition the following front panel controls:

VERNIER (Amplitude)	\L
AMPLITUDE MODE LINE	R
AMPLITUDE REF LEVEL 30 c	lB
INPUT SENSITIVITY	
(according to white marker	V
DISPLAY SMOOTHING M	

- b. Adjust the signal source for a 1 volt 10 kHz output (See Table 5-8). Adjust MANUAL VERNIER for a peak display indication. Adjust CAL 10 KHz for a full scale display. Momentarily press DISPLAY CLEAR WRITE.
- c. Adjust the signal source and INPUT SENSITIVITY switch to the levels given in Table 5-8. Check for the proper display indication.
  - d. Reposition the following front panel control:

AMPLITUDE REF LEVEL ......NORMAL

Table 5-8. First Input Attenuator Test.

	INPUT LEVEL (10 KHz)	VEL 50 OHM SOURCE SENSITIVITY		DISPLAY
ĺ	1 V	+ 13.01 dBm	1 V	Full Scale (CAL)
	.2 V .1 V 20 mV	99 dBm - 6.99 dBm - 20.99 dBm	.2 V .1 V 20 mV	Full Scale (± 3%) Full Scale (± 3%) Full Scale (± 3%)

- e. Adjust the signal source for a 1 volt 10 kHz output (See Table 5-9). Adjust the CAL 10 KHz for a full scale display.
- f. Adjust the signal source and INPUT SENSITIVITY switch to the values given in Table 5-9. Check for the proper display indication.

Table 5-9. Second Input Attenuator Test.

INPUT LEVEL (10 KHz)	3320B or OTHER 50 OHM SOURCE	INPUT SENSITIVITY	DISPLAY INDICATION
1 V	+ 13.01 dBm	1 V	Full Scale (CAL)
.2 V .1 V 20 mV 10 mV 2 mV 1 mV .2 mV	99 dBm - 6.99 dBm - 20.99 dBm - 26.99 dBm - 40.99 dBm - 46.99 dBm - 60.99 dBm	.2 V .1 V 20 mV 10 mV 2 mV 1 mV .2 mV	Full Scale (± 3%)

### 5-26. Frequency Response Tests.

a. Reposition the following front panel controls:

AMPLITUDE MODE LOG 10 dB/DIV
AMPLITUDE REF LEVEL 30 dB
INPUT SENSITIVITY
(according to white marker) 0 dB
RESOLUTION BANDWIDTH 3 Hz

- b. Adjust the signal source for a 10 kHz 0 dBv output (0 dBm 900  $\Omega$  for Option 002). (See Table 5-3).
- c. By alternately pressing and releasing DISPLAY-CLEAR WRITE while adjusting MANUAL VERNIER, center the display indication (a narrow spike). Adjust ZERO CAL for a peak display of this spike.
  - d. Reposition the following front panel control:

AMPLITUDE MODE . . . . . . . LOG 1 dB/DIV

- e. Readjust MANUAL VERNIER for a peak display.
- f. Adjust VERNIER (Amplitude) for a -1 dB display (1 dB/div).
- g. Adjust the signal source to the frequencies given in Table 5-10 for an INPUT SENSITIVITY of 0 dB. At each frequency, adjust the FREQUENCY dial to that of the source. Then, slowly adjust the ZERO CAL for a peak

display indication. Momentarily press DISPLAY - CLEAR WRITE. Check for proper level as given in Table 5-10. Note: The display is calibrated 1 dB per major division.

h. Repeat Steps e through g for an INPUT SENSITI-VITY and source levels of -10 dB, -20 dB, -30 dB and -40 dB (according to white marker and with a -30 dB AMPLITUDE REF LEVEL). Consult Table 5-3 and Table 5-10 for the proper input level and frequencies to use. At the start of each new INPUT SENSITIVITY, always recalibrate the instrument at 10 KHz with CAL 10 KHz.

#### 5-27. Internal Calibrator Test.

a. Reposition the following front panel controls:

VERNIER (Amplitude) CAL
AMPLITUDE REF LEVEL NORMAL
INPUT SENSITIVITY 20 dB
FREQUENCY 00.0 kHz
START - CTR START
RESOLUTION BANDWIDTH 300 Hz
FREQ. SPAN/DIV 5 kHz
SWEEP TIME/DIV 0.2 SEC
SWEEP MODE

- b. Adjust the signal source for a 10 KHz 20 dBv (- 20 dBm 900  $\Omega$  if Option 002) output. (See Table 5-3 for proper level.)
- c. Adjust the ZERO CAL for a display response on the 10 KHz graticule (2 major divisions from left graticule). (After each trial adjustment, allow 2 seconds for the next sweep before verifying the accuracy of the adjustment.)
- d. Adjust the CAL 10 KHz for a full scale 0 dB display. (After each trial adjustment, allow 2 seconds for the next sweep before verifying the accuracy of the adjustment.)
  - e. Reposition the following front panel control:

INPUT SENSITIVITY ......CAL

f. Verify that the 10 KHz harmonic of the CAL signal appears 2 major divisions from left graticule with a full scale 0 dB level (± .15 dB). (1 dB = 1 major division.)

### 5-28. Bandwidth Tests.

5-29. This test verifies the bandwidth specifications of Table 1-1. If the instrument will not pass this test, perform the IF Filter Alignment (Paragraph 5-70) of the Adjustment Procedures.

### Equipment Required:

Frequency Synthesizer (-hp- Model 3320B, 50 ohms) 50 Ohm Termination (-hp- 11048C)

a. Position the following front panel controls. (Only those controls printed in BOLD require a change from the previous test.)

ADAPTIVE SWEEPOFF
DISPLAY All pushbuttons released
AMPLITUDE MODE LOG 10 dBv/DIV
AMPLITUDE REF LEVELNORMAL
$dBv/LIN - dBm 600 \Omega \dots dBv/LIN$
INPUT SENSITIVITY 20 dB
VERNIER (Amplitude) CAL
(Fully CW)
FREQUENCY
START - CTRCTR
RESOLUTION BANDWIDTH 300 Hz
DISPLAY SMOOTHING MIN
FREQUENCY SPAN/DIV 50 Hz
SWEEP TIME/DIV 0.2 SEC
SWEEP MODE

Option 002: Set dBm 900  $\Omega/\text{LIN}$  - dBm 600  $\Omega$  switch to dBm 900  $\Omega$ ; set INPUT MODE switch to UNBAL.

Table 5-10. Frequency Response Tests.

INPUT	INPUT LEVEL			DISPLA	Y INDICATI	ON (0 dB = 1	full scale; 1 di	B/DIV)
SENSITIVITY (according to white marker)	STD.	OPT. 002 900 Ω	10 kHz	10 Hz	20 Hz	1 kHz	20 kHz	50 kHz
0 dB	0 dBv	0 dBm	CAL	- 1 dB ± .5 dB	- 1 dB ± .3 dB	- 1 dB ± .3 dB	- 1 dB ± .3 dB	- 1 dB ± .5 dB
- 10 dB	- 10 dBv	- 10 dBm	CAL	- 1 dB ± .5 dB	- 1 dB ± .3 dB	- 1 dB ± .3 dB	- 1 dB ± .3 dB	- 1 dB ± .5 dB
- 20 dB	- 20 dBv	- 20 dBm	CAL	- 1 dB ± .5 dB	- 1 dB ± .3 dB	- 1 dB ± .3 dB	- 1 dB ± .3 dB	- 1 dB ± .5 dB
- 30 dB	- 30 dBv	- 30 dBm	CAL	- 1 dB ± .5 dB	- 1 dB ± .3 dB	- 1 dB ± .3 dB	- 1 dB ± .3 dB	- 1 dB ± .5 dB
- 40 dB	- 40 dBv	- 40 dBm	CAL	- 1 dB ± .5 dB	- 1 dB ± .3 dB	- 1 dB ± .3 dB	- 1 dB ± .3 dB	- 1 dB ± .5 dB

- b. By alternately pressing and releasing DISPLAY-CLEAR WRITE while adjusting MANUAL VERNIER, center the display indication (a narrow spike).
- c. Connect a properly terminated frequency synthesizer to the input of the 3580A. Adjust the synthesizer for a 10 kHz 20 dBv signal (- 20 dBm 900  $\Omega$  for Option 002) output. (See Table 5-3.) Momentarily press DISPLAY CLEAR WRITE.
- d. Adjust the FREQUENCY dial (pulled out for fine tuning) for a peak display indication.
  - e. Reposition the following front panel controls:

### AMPLITUDE MODE ......LOG 1 dB/DIV

- f. Readjust the FREQUENCY dial (fine tune position) for a peak display indication of the 10 KHz input. Adjust CAL 10 KHz for a full scale 0 dB display, if not already so adjusted.
- g. Slowly rotate MANUAL VERNIER CW until the display dot has dropped 3 dB in amplitude. (Remember, the display is calibrated 1 dB/DIV). This is the upper 3 dB point of the filter.
- h. Momentarily press DISPLAY-CLEAR WRITE. Slowly increase the frequency of the source. The dot will move to a full scale display and then down to the lower 3 dB point of the filter.
- i. Note the frequency of the source at this lower 3 dB point \_\_\_ Hz. This frequency, less the original 10 KHz start frequency, is the 3 dB bandwidth of the 300 Hz filter. It should be 300 Hz ± 45 Hz.
- j. Repeat Steps f through i for the 100 Hz, 30 Hz and 10 Hz filters. See Table 5-11 for the start frequency of the source, FREQUENCY dial setting, RESOLUTION BAND-WIDTH, FREQ. SPAN/DIV, and the test limits. At the start of each new bandwidth setting, always center the display with MANUAL VERNIER, and adjust the FREQUENCY dial, and CAL 10 KHz for a full scale, peak display at the appropriate start frequency. Then make the appropriate adjustments for the upper and lower 3 dB points.

Table 5-11. 300 Hz thru 10 Hz Bandwidth Tests.

SOURCE START FREQ. and 3580A FREQUENCY	and 3580A BANDWIDTH		3 dB BANDPASS TEST LIMITS	
10 kHz	300 Hz	50 Hz	300 Hz ± 45 Hz	
1 kHz	100 Hz	50 Hz	100 Hz ± 15 Hz	
1 kHz	30 Hz	10 Hz	30 Hz ± 4.5 Hz	
1 kHz	10 Hz	5 Hz	10 Hz ± 1.5 Hz	

k. Using Table 5-12 and the same technique used for the 300 Hz, 100 Hz, 30 Hz, and 10 Hz Bandwidths, test the 60 dB Bandpass of the 3 Hz and 1 Hz filters. However, use

### AMPLITUDE MODE . . . . . LOG 10 dB/DIV

and measure the frequency difference between the 60 dB points. As before, always adjust the FREQUENCY dial and CAL 10 KHz for a peaked full scale display before attempting to measure the 60 dB bandwidths. Note: The display is now calibrated 10 dB/DIV.

Table 5-12. 3 Hz and 1 Hz Bandwidth Tests.

SOURCE START FREQ, and 3580A FREQUENCY	RESOLUTION BANDWIDTH	FREQ. SPAN/DIV	60 dB BANDPASS TEST LIMITS	
1 kHz	3 Hz	5 Hz	30 Hz ± 4.5 Hz	
1 kHz	1 Hz	5 Hz	10 Hz ± 1.5 Hz	

### 5-30. Dynamic Range Tests (Noise Tests).

5-31. Dynamic range is the ability of the instrument to detect large and small signals and display them simultaneously. The range and accuracy of the amplifiers is a determining factor. This specification was tested in the Amplitude Tests (Paragraph 5-18). The instrument noise and spurious responses are the other determining factors of dynamic range. These tests verify these parameters. If the instrument will not pass any of these tests, see Section VII for troubleshooting information. There are no adjustments for these specifications.

#### Equipment Required:

Frequency Synthesizer (-hp- Model 3320B, 50 ohms) 50 Ohm Termination (-hp- 11048C) Bandpass Filter (White Model 2640) Proper input resistor for filter (550  $\Omega$  ± 10%, Part No. 0698-4456) 1% 1 k $\Omega$  film resistor (-hp- Part No. 0757-0280)

### 5-32. Noise Level Tests.

- a. Connect the 1  $k\Omega$  resistor across the INPUT terminals of the 3580A. Disconnect all signal sources.
- b. Position the following front panel controls: (Only those controls printed in BOLD require a change from the previous tests).

ADAPTIVE SWEEP OFF
DISPLAY All pushbuttons released
AMPLITUDE MODE LOG 10 dBv/DIV
AMPLITUDE REF LEVELNORMAL
$dBv/LIN - dBm 600 \Omega \dots dBv/LIN$
INPUT SENSITIVITY 70 dB
VERNIER (Amplitude) CAL
(Fully CW)
FREQUENCY 00.0 kHz
START - CTRSTART
RESOLUTION BANDWIDTH 300 Hz

DISPLAY SMOOTHING MAX	Momentarily press the following control:
FREQ. SPAN/DIV 5 KHz SWEEP TIME/DIV 5 SEC	DISPLAYCLEAR WRITE
SWEEP MODE	o. The display indication should be less than - 150 dB
Option 002: Set dBm 900 $\Omega$ /	(8 major divisions down from top graticule).
LIN - dBm 600 $\Omega$ switch to dBm 900 $\Omega$ ; set INPUT MODE switch to UNBAL.	p. Readjust MANUAL VERNIER for a display indication at 100 Hz (1 major division from leftmost graticule).
c. Adjust the MANUAL VERNIER full CCW.	Momentarily press the following control:
Adjust ZERO CAL for a peak display indication.	DISPLAYCLEAR WRITE
d. Adjust the MANUAL VERNIER for a display indica-	q. The indication should be less than - 143 dB (7.3
tion at 10 KHz (2 major divisions from left graticule).  Momentarily press the following control:	major divisions down from top graticule).
DISPLAYCLEAR WRITE	r. Reposition the following controls:
e. The display indication should always be less than	DISPLAY SMOOTHING MIN
- 130 dB (6 major divisions down from top graticule, since	FREQ. SPAN/DIV 5 Hz
Full Scale = -70 dB).	s. Adjust MANUAL VERNIER full CCW. Momentarily
f. Reposition the following front panel control:	press the following front panel control:
RESOLUTION BANDWIDTH 30Hz	DISPLAY CLEAR WRITE
g. Momentarily press the following control:	t. Adjust ZERO CAL for a peak response at the leftmost graticule. Reposition the following front panel control:
DISPLAY	DISPLAY SMOOTHING
The display indication should be less than -140 dB (7 major divisions down from top graticule).	
	u. Adjust the MANUAL VERNIER for a display indication at 10 Hz (2 major divisions from leftmost graticule).
h. Reposition the following control:	Momentarily press the following control:
FREQ. SPAN/DIV 0.1 KHz	DISPLAYCLEAR WRITE
i. Adjust MANUAL VERNIER full CCW. Adjust ZERO	v. The display indication should be less than - 135 dB
CAL for a peak display indication.	(6.5 major divisions down from top graticule). Remove the
j. Adjust MANUAL VERNIER for a display indication at 100 Hz. (1 major division from leftmost graticule).	1 k $\Omega$ resistor from the input terminals.
Momentarily press the following control:	5-33. Noise Sideband Test.
DISPLAYCLEAR WRITE	a. Reposition the following controls:
k. The display indication should be less than - 132 dB	INPUT SENSITIVITY
(6.2 major divisions down from top graticule).	FREQUENCY 10.0 kHz START - CTR CTR
1. Adjust MANUAL VERNIER for a display indication of 1 KHz (far right graticule). Momentarily press the	DISPLAY SMOOTHING MIN FREQ. SPAN/DIV 5 Hz
following control:	SWEEP TIME/DIV 10 SEC
DISPLAYCLEAR WRITE	
m. The display indication should be less than - 140 dB	b. By alternately pressing and releasing DISPLAY -
(7 major divisions down from top graticule).	CLEAR WRITE while adjusting MANUAL VERNIER, center the display indication (a narrow spike).
n. Reposition the following control:	c. Adjust the FREQUENCY dial (pulled out for fine
RESOLUTION BANDWIDTH 1 Hz	tuning) for a neak display of this spike

tuning) for a peak display of this spike.

RESOLUTION BANDWIDTH . . . . . 1 Hz

d. Reposition the following controls:

SWEEP MODE ..... SING

e. After waiting for the sweep to be completed (100 sec.), verify that the noise on the display  $\pm$  10 Hz ( $\pm$  2 major divisions) away from the 10 KHz CAL signal (in center of display) is at least 70 dB below the CAL signal.

### 5-34. Spurious Response Test.

a. Reposition the following controls:

INPUT SENSITIVITY 20 dB
FREQUENCY 00.0 Hz
START - CTRSTART
RESOLUTION BANDWIDTH30 Hz
FREQ. SPAN/DIV 2 KHz
SWEEP TIME/DIV 5 SEC
SWEEP MODE

b. Momentarily press:

- c. Adjust ZERO CAL for a peak display on the leftmost display graticule.
  - d. Reposition the following controls:

and momentarily press:

e. Connect the frequency synthesizer (use proper output impedance needed for the bandpass filter) to the input of the bandpass filter. Adjust the filter for a 5 kHz center frequency and adjust the synthesizer for a 5 kHz output. (For a 50 ohm source and the White 2640 filter, connect a 550  $\Omega$  resistor (± 10%) in series between the filter and synthesizer. This gives the 600  $\Omega$  source impedance required by the White filter (See Figure 5-3).

- f. Connect the output of the filter to the input of the 3580A. Always terminate properly if required. (The White Model 2640 filter requires no output termination. See Figure 5-3).
- g. Adjust MANUAL VERNIER for a display indication at 5 kHz (2 1/2 major divisions from left graticule). Adjust the source level for a -20 dBv (full scale) input to the 3580A (For the White 2640 filter and a 50  $\Omega$  source, this corresponds to -16.99 dBm 50  $\Omega$  level on the source). Readjust MANUAL VERNIER for a peak display. Adjust CAL 10 KHz for a full scale display.

h. Reposition the following controls:

SWEEP MODE . . . . . . . . . . . SING

i. After waiting for one complete sweep (50 sec.) verify that all responses other than the zero response are at least 80 dB below the 5 kHz response.

### 5-35. Line Related Spurious Test.

Specification: > 80 dB below input reference level or - 140 dBV (0.1  $\mu$ V).

- a. Disconnect the Synthesizer and Bandpass Filter from the 3580A Input. Turn off all unnecessary equipment located near the 3580A. This especially includes large current users such as soldering irons, blowers, moters, etc.
- b. Using a short piece of wire, connect a short across the 3580A INPUT terminals.
- d. Connect the LO OUTPUT (rear panel) to the input of an Electronic Counter (-hp- Model 5326A).

### **NOTE**

If the power-line frequency is 50 Hz, substitute the following Counter readings for Steps e and f

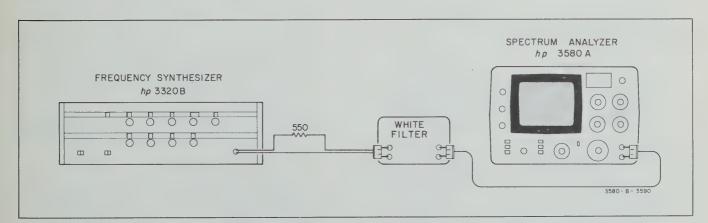


Figure 5-3. Spurious Response Test.

Step e and f: 1000.48 kHz to 1000.52 kHz Step e and f: 1000.98 kHz to 1001.02 kHz Step e and f: 1001.48 kHz to 1001.52 kHz

- e. With the FREQUENCY control pulled out for fine tuning, tune the 3580A frequency for a Counter reading between 1000.58 kHz and 1000.62 kHz.
- f. Press CLEAR WRITE, then slowly turn the MANUAL VERNIER to obtain a peak reading. The peak should be more than 70 dB below full scale (-140 dBV).
- g. Repeat Steps e and f substituting 1001.18 kHz to 1001.22 kHz, and 1001.78 kHz to 1001.82 kHz for the Counter readings.

DISPLAY SMOOTHING					٠		MIN
FREQ.SPAN/DIV	٠					٠	.20 Hz

#### NOTE

If the instrument fails this test double check that the input short is as small as possible; that all power line current is kept at a minimum, and that all covers are tightly secured on the 3580A.

### 5-36. IF Feedthru and Zero Beat Response Tests.

5-37. These tests verify the ability of the instrument to reject a 100 kHz signal at the input and also how well the Zero Beat Response is suppressed. Proceed to the Mixer Balance Adjustments (Paragraph 5-81) of the Adjustment Procedures if the Zero Beat Response is too large. Proceed to Section VII for troubleshooting information if there is too much IF Feedthru.

### Equipment Required:

Frequency Synthesizer (-hp-Model 3320B, 50 ohm)

- a. Reconnect the synthesizer to the 3580A. Do not terminate. Adjust the source for a 10 volt 100 kHz output ( $\pm$  26.99 dBm 50 ohms setting on 3320B and unterminated).
- b. Position the following front panel controls: (Only those controls printed in BOLD require a change from the previous test).

ADAPTIVE SWEEPOFF
DISPLAY All pushbuttons released
AMPLITUDE MODE LOG 10 dBv/DIV
AMPLITUDE REF LEVELNORMAL
$dBv/LIN - dBm 600 \Omega \dots dBv/LIN$
INPUT SENSITIVITY+ 20 dB
VERNIER (Amplitude) CAL
(Fully CW)
FREQUENCY 00.0 kHz
START-CTRSTART
RESOLUTION BANDWIDTH 3 Hz
DISPLAY SMOOTHINGMIN

FREQ. SPAN/DIV											2	0 Hz
SWEEP TIME/DIV						۰	۰				5	SEC
SWEEP MODE					۰			.]	M	A	N	UAL

Option 002: Set dBm 900  $\Omega/\text{LIN}$  - dBm 600  $\Omega$  switch to dBm 900  $\Omega$ ; set INPUT MODE switch to UNBAL.

- c. Adjust MANUAL VERNIER for a response in the center of the screen. The display indication should be at least 70 dB below full scale to verify the IF Feedthru specification of Table 1-1. If the instrument fails this test, see Section VII for troubleshooting information.
- d. Disconnect the synthesizer. Reposition the following front panel controls:

RESOLUTION BANDWIDTH	300 Hz
FREQ. SPAN/DIV	5 KHz
SWEEP MODE	RESET

e. Momentarily press the following front panel control:

DISPLAY ...... CLEAR WRITE

f. Adjust ZERO CAL for a maximum display indication on the left graticule. This display should be at least 30 dB (3 major divisions) below full scale to verify the Zero Beat Response specification of Table 1-1. If the instrument fails this test, go to the Mixer Balance Adjustments (Paragraph 5-81) of the Adjustment Procedures.

### 5-38. Input Impedance Tests.

5-39. These tests verify the Input Impedance characteristics of Table 1-2. Since there is no adjustment for this parameter, see Section VII for troubleshooting information if the instrument fails this test.

### Equipment required:

 $1 \text{ M}\Omega \pm 1\%$  film resistor (-hp- Part No. 0757-0344)

a. Position the following front panel controls. (Only those controls printed in BOLD require a change from the previous tests.)

ADAPTIVE SWEEP OFF
DISPLAY All pushbuttons released
AMPLITUDE MODELOG 10 dBv/DIV
AMPLITUDE REF LEVELNORMAL
$dBv/LIN - dBm 600 \Omega \dots dBv/LIN$
INPUT SENSITIVITY 0 dB
VERNIER (Amplitude CAL
(Fully CW)
FREQUENCY 00.0 kHz
START - CTRSTART
DISPLAY SMOOTHING MIN
RESOLUTION BANDWIDTH 10 Hz
FREQ. SPAN/DIV 1 KHz
SWEEP TIME/DIV 5 SEC
SWEEP MODE

Option 002: Set dBm 900  $\Omega/\text{LIN}$  - dBm 600  $\Omega$  switch to dBm 900  $\Omega$ ; set INPUT MODE switch to UNBAL.

- b. Adjust ZERO CAL for a peak display on the left graticule.
  - c. Reposition the following front panel controls:

AMPLITUDE MODE				.LOG 1 dB/DIV
SWEEP MODE				MANUAL

d. Connect the rear panel TRACKING OSC OUT to the front INPUT terminals of the 3580A. Adjust the rear panel TRACKING OSC LEVEL control fully CW. Adjust MANUAL VERNIER for a 1 kHz display indication (1 major division from left graticule). Readjust the TRACKING OSC LEVEL control for a full scale 0 dB display. Momentarily press the following control:

### DISPLAY ...... CLEAR WRITE

- e. Connect the 1 M $\Omega$  resistor in series between the TRACKING OSC OUT and front panel INPUT terminals. The display indication should drop 6 dB  $\pm$  .3 dB (6 major divisions  $\pm$  .3 major divisions) to verify an input impedance of 1 M $\Omega$ .
  - f. Reposition the following front panel control:

INPUT SENSITIVITY ..... - 10 dB

g. Readjust the rear panel TRACKING OSC LEVEL control for a full scale display. Adjust MANUAL VERNIER for a display indication at 10 kHz (far right display graticule). DO NOT REMOVE 1 M $\Omega$  RESISTOR. Momentarily press the following front panel control:

### 

- h. 1) Std. 3580A: The amplitude should drop 3 dB ± 1 dB, verifying that the input shunt capacitance is 30 pF, nominal.
  - Option 002: The amplitude should drop 4 dB ± 1 dB, verifying that the input shunt capacitance is 40 pF, nominal.
- i. Disconnect the cable connected between the TRACK-ING OSC OUT and the front panel INPUT terminals.

### 5-40. Output Tests.

5-41. These tests verify the Output specifications of the 3580A listed in Table 1-1.

### Equipment Required:

Electronic Counter (-hp- Model 5326A) Digital Multimeter (-hp- Model 34740/34702) Distortion Analyzer (-hp- Model 333A)

#### 5-42. TRACKING OSC OUTPUT Tests.

a. Position the following front panel controls. (Only those controls printed in BOLD require a change from the previous tests).

ADAPTIVE SWEEP OFF DISPLAY All pushbuttons released
AMPLITUDE MODE LOG 10 dBv/DIV
AMPLITUDE REF LEVELNORMAL
$dBv/LIN - dBm 600 \Omega \dots dBv/LIN$
INPUT SENSITIVITY + 20 dB
VERNIER (Amplitude) CAL
(Fully CW)
FREQUENCY 00.0 kHz
START - CTRSTART
RESOLUTION BANDWIDTH 10 Hz
DISPLAY SMOOTHING MIN
FREQ. SPAN/DIV 5 KHz
SWEEP TIME/DIV 5 SEC
SWEEP MODE
Option 002: Set dBm 900 Ω/LIN - dBm
$600 \Omega$ switch to dBm $900 \Omega$ ; set INPUT
MODE switch to UNBAL.

- b. Momentarily press DISPLAY CLEAR WRITE. Adjust the ZERO CAL for a peak display (on leftmost display graticule).
- c. Connect the multimeter (AC mode 100 volt range) to the rear panel TRACKING OSC OUT. Adjust the FRE-QUENCY dial for 50 Hz (300 Hz for Option 002). Adjust the rear panel TRACKING OSC LEVEL control for a 2.00 volt reading on the multimeter.\*
- d. Adjust the FREQUENCY control to  $50.0\,\text{kHz}$  (20.0 kHz for Option 002 instruments). Verify that the multimeter reads 2.00 volts  $\pm$  .06 volts ( $\pm$  .1 volts for Option 002 instruments).
  - e. Reposition the following front panel controls:

AMPLITUDE MODE	LIN
INPUT SENSITIVITY	2 V
FREQUENCY 00.0	0 Hz
RESOLUTION BANDWIDTH 30	0 Hz
SWEEP MODE	JAL

f. Connect the rear panel TRACKING OSC OUT to the front panel INPUT terminals. Momentarily press the following control:

<sup>\*</sup>For measurements below 50 Hz, use a low frequency Digital Voltmeter such as the -hp- Model 3480/3484 with true rms.

- g. By alternately pressing and releasing DISPLAY-CLEAR WRITE while adjusting MANUAL VERNIER, center the display indication (a narrow spike).
- h. Adjust the rear panel TRACKING OSC LEVEL control for a full scale 2 V display. Reposition the following front panel control:

### RESOLUTION BANDWIDTH ...... 3 Hz

- i. The display indication should drop no lower than  $1\,V$  (5 major divisions) to verify the frequency accuracy of the tracking oscillator. If the tracking oscillator frequency is out of tolerance, remove the top cover and adjust A2C4 for a peak display indication.
  - j. Reposition the following front panel control:

FREQ. SPAN/DIV . . . . . . . . . 0.1 KHz

k. Adjust MANUAL VERNIER for a 1 KHz display indication (indication on far right display graticule). Momentarily press the following front panel control:

### 

- 1. Connect the TRACKING OSC OUT to the INPUT of the distortion analyzer. Adjust the TRACKING OSC LEVEL control fully CW.
- m. Reference the TRACKING OSC OUT to 0 dB on the distortion analyzer. (For the -hp- 333A Distortion Analyzer, position the following controls:

FUNCTION	ET LEVEL
METER RANGE	0 dB
FREQUENCY RANGE	X100
FREQUENCY	10 (1 kHz)
HIGH PASS FILTER	OUT

Adjust the SENSITIVITY and VERNIER controls of the distortion analyzer for a 0 dB meter indication. Set the distortion analyzer FUNCTION switch to DISTORTION.)

- n. Measure the distortion in dB by nulling the distortion analyzer.
- o. Adjust the FREQUENCY and BALANCE controls for a meter null. Use automatic nulling if available.
- p. The total distortion indication should be at least 40 dB below the reference level. If it is not, perform the Mixer Balance Adjustments (Paragraph 5-81). Disconnect the distortion analyzer from the 3580A.

### 5-43. RECORDER Output Tests.

a. Connect the multimeter (DC mode, 100 volt range) to the rear panel X-AXIS RECORDER output. Adjust MANUAL VERNIER fully CCW.

- b. The multimeter should read 0 V dc ± .15 V.
- c. Adjust the MANUAL VERNIER fully CW. The multimeter reading should be 5 V dc  $\pm .15 \text{ V}$ .
  - d. Reposition the following front panel control:

### RESOLUTION BANDWIDTH ......30 Hz

- e. Reconnect the TRACKING OSC OUTPUT to the INPUT terminals of the 3580A and readjust the rear panel LEVEL control for a full scale display (on the far right graticule). Use DISPLAY CLEAR WRITE, if necessary, to clear all unwanted data from the display.
- f. Connect the multimeter (DC mode, 100 volt range) to the rear panel Y-AXIS RECORDER output. The multimeter reading should be  $5.00~V~dc~\pm .15~V.*$
- g. Disconnect the TRACKING OSC OUT from the INPUT terminals. The voltmeter should now read 0 volts dc ± .15 V. Disconnect the multimeter from the 3580A.\*

### 5-44. Balanced Input Tests (Option 002 only).

5-45. These tests verify the Balanced Input specifications for the Option 002 instrument. If the instrument fails these tests, see Section VII for troubleshooting information since there are no adjustments for the parameters tested.

Equipment Required:

Frequency Synthesizer (-hp- Model 3320B, 50 ohm) 50 Ohm Termination (-hp- 11048C) Two 453 ohm 1% resistors (-hp- Part No. 0698-3510)

### 5-46. Common Mode Rejection Test.

a. Position the following front panel controls:

ADAPTIVE SWEEPOFF
DISPLAY All pushbuttons released
AMPLITUDE MODELOG 10 dBv/DIV
AMPLITUDE REF LEVELNORMAL
dBm 900 Ω/LIN - $dBm$ 600 Ω
- dBm 900 Ω/LIN
INPUT SENSITIVITY 0 dB
VERNIER (Amplitude)
(Fully CW)
INPUT MODE BRDG
FREQUENCY 00.0 kHz
START - CTRSTART
RESOLUTION BANDWIDTH 3 Hz
DISPLAY SMOOTHINGMIN
FREQ. SPAN/DIV 10 Hz
SWEEP TIME/DIV 5 SEC
SWEEP MODE

b. Disconnect all inputs to the 3580A. Momentarily press the following front panel control:

### 

c. Adjust the ZERO CAL control for a peak display at the leftmost graticule of the CRT. Reposition the following front panel control:

- d. Adjust the frequency synthesizer for a 60 Hz, +5 dBm 900  $\Omega$  output (+ 17.55 dBm/50 ohms). Connect the synthesizer (properly terminated) to the INPUT of the 3580A.
- e. Slowly adjust MANUAL VERNIER to the 60 Hz signal which will appear as a peak on the sixth major division from the left. Momentarily press the following front panel control:

- f. Adjust the VERNIER (Amplitude) for a full scale 0 dB display.
- g. Disconnect the synthesizer from the 3580A and connect two 453 ohm resistors in series between the INPUT terminals. (See Figure 5-4)
- h. Connect the synthesizer to the junction of the two resistors and to the chassis on the rear panel as shown in Figure 5-4. (Do not change the synthesizer amplitude setting.)
- i. The display indication on the 3580A should be at least 70 dB below full scale (10 dB/DIV).

### 5-47. Frequency Response Test.

a. Disconnect the resistors from the 3580A INPUT terminals and reconnect the synthesizer (properly terminated in 50 ohms). Adjust the source for a 0 dBm 900  $\Omega$  (+ 12.55 dBm 50  $\Omega$ ) 10 kHz signal.

b. Reposition the following front panel controls:

FREQUENCY				۰	٠	٠						٠				10.0 kHz
START - CTR		۰	۰				۰	۰	٠		۰			۰		CTR
VERNIER (An	n	r	ol	it	เน	ιd	le	)	٠	۰			۰	۰	٠	Fully CW

- c. By alternately pressing and releasing DISPLAY-CLEAR WRITE while adjusting MANUAL VERNIER, center the display indication (a narrow spike). Adjust the FREQUENCY dial (pulled out for fine tuning) for a peak display of the 10 kHz input signal.
  - d. Reposition the following front panel control:

AMPLITUDE MODE . . . . . . LOG 1 dB/DIV

- e. Readjust the FREQUENCY dial for a peak display indication. Adjust VERNIER (Amplitude) for a full scale 1 dB display indication (1 major division down from full scale).
- f. Adjust the frequency synthesizer and 3580A FRE-QUENCY dial to the frequencies given by Table 5-13. Always peak the display indication with the FREQUENCY dial and check for proper amplitude accuracy.

Table 5-13. Balanced Input Frequency Response Tests.

FREQUENCY	INPUT 900 Ω	DISPLAY ACCURACY
10 kHz	0 dBm	CAL to - 1 dB
40 Hz $\Delta_1$	0 dBm	- 1 dB ± .5 dB (± .5 major divisions)
300 Hz	0 dBm	- 1 dB ± .5 dB
1 kHz	0 dBm	- 1 dB ± .5 dB
20 kHz	0 dBm	- 1 dB ± .5 dB

 $\Delta_1$  See Backdating.

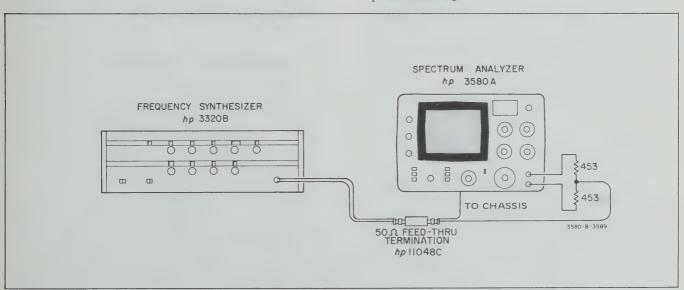


Figure 5-4. Common Mode Rejection Test.

### 5-48. ADJUSTMENT PROCEDURE.

5-49. This portion of Section V contains complete Adjustment Procedures for the Model 3580A Spectrum Analyzer:

POWER SUPPLY CHECKS AND ADJUST-MENTS (Paragraph 5-53).

DISPLAY ADJUSTMENTS (Paragraph 5-68).

SWEEP ALIGNMENT AND DIAL CALIBRATION (Paragraph 5-63).

LINE GENERATOR ADJUSTMENTS (Paragraph 5-68).

I.F. FILTER ALIGNMENT (Paragraph 5-70).

AMPLITUDE CALIBRATION (Paragraph 5-74).

MIXER BALANCE ADJUSTMENTS (Paragraph 5-81).

ADAPTIVE SWEEP MARKER ADJUSTMENT (Paragraph 5-84).

### 5-50. TEST POINT AND ADJUSTMENT LOCATIONS.

5-51. Test point and adjustment locations are shown in Figure 5-9 at the end of Section V. Most of the test points and adjustments are easily accessible with the outer covers removed. In some cases it will be necessary to remove the inner cover and place the appropriate pc boards on extenders. Set the 3580A POWER switch to OFF when removing or replacing a pc assembly.

5-52. The Adjustment Procedure is written in a logical sequence. If the instrument is known to be completely out of calibration, the sequence should be strictly followed. Many times, however, only certain adjustments need to be made. The Performance Tests have been written in such a manner that they will lead you to the proper adjustment. In addition, a brief description of each adjustment is given. Read through the procedures carefully, doing only those that are necessary. Take careful note of any previous adjustments which may affect a future adjustment.

### **NOTE**

Always test the low voltage power supply before performing any calibration. All test measurements should be made with respect to circuit ground, which is available at any point on the instrument chassis. Adjustments should not be made until the instrument has had one hour of continuous warm-up.

### 5-53. POWER SUPPLY TESTS AND ADJUSTMENTS.

5-54. These tests and adjustments check the operation of the low voltage + 10 V dc and - 10 V dc regulated power

supplies and set the level of the high voltage - 2915 V dc regulated power supply. The low voltage power supply tests should be performed prior to all other adjustments. In addition, the High Voltage - 2915 V dc power supply voltage should be tested if any of its components were changed or if the instrument will not pass the Frequency Tests (Paragraph 5-9) or Amplitude Tests (Paragraph 5-18) of the Performance Tests.

### 5-55. Recommended Test Equipment:

AC/DC Digital Multimeter (-hp- Model 34740A and 34702A plug-on)

High Voltage DC Probe for above multimeter, calibrated to 1000 V DC Standard (-hp- Model 11045 A Probe and -hp- Model 740B DC Standard)

or

Precision .1% High Voltage Probe and appropriate DVM (-hp- 3440A-K05 High Voltage Probe and -hp- Model 3440A DVM)

### 5-56. ± 10 Volt Power Supply Tests.

- a. Connect the digital multimeter (DC mode 10 volt range) to the red lead (pin 12) at the A13 board connector. The multimeter reading should be  $\pm$  10.000 V  $\pm$  .050 V. If it is not, refer to the Factory-Selected Components information in Section VII.
- b. Connect the digital multimeter (DC mode 10 volt range) to the violet lead (pin 10) at the A13 board connector. The dc voltage present should be -10.000 V  $\pm .050 \text{ V}$ .
- c. Test the ac ripple voltage present on the above two leads with the digital multimeter. There should be less than .1 mV ac difference between the reading obtained on each lead and that obtained with a short circuit to the multimeter.

### 5-57. High Voltage Power Supply Tests.

# WARNING

The voltages involved in the following measurements may cause serious injury or even death. USE EXTREME CAUTION.

a. If a precision .1% high voltage dc probe is available, omit this step and proceed to Step b. Otherwise, calibrate your high voltage probe using a 1000 volt DC Standard. Connect the probe to the digital multimeter (DC mode) and note the reading obtained when measuring 1000 volts on the DC Standard. Multiply this reading by 2.915 to obtain the proper reading for 2915 volts. Record your calculation.

#### NOTE

Always select a range on the multimeter so that a range change is not necessary for reading 2915 volts. For instance, when using the -hp-34740A/34702A Multimeter and 11045A probe, put the 34702A on the 10 volt range, not the 1 volt range.

- b. Turn the 3580A POWER switch to OFF and remove the gray metal shield on the rear panel of the 3580A. Remove the plastic shield on the CRT connector to expose the wiring terminals. Turn the 3580A POWER switch to ON (AC). HIGH VOLTAGE LEADS ARE NOW EXPOSED.
- c. With the high voltage probe and digital multimeter, measure the dc voltage present at pin 2 (yellow lead) of the CRT connector. Adjust A8R1 (HV ADJ.) for a voltage reading of -2915 volts  $\pm$  3 volts. If you calibrated your own probe as described in Step a, adjust the voltage to the appropriate value,  $\pm$  1%.

### NOTE

This adjustment affects the Sweep Alignment and Dial Calibration (Paragraph 5-63), as well as the Amplitude Calibration (Paragraph 5-74). Repeat the Frequency Tests (Paragraph 5-9) and Amplitude Tests (Paragraph 5-18) of the Performance Tests to determine if these additional adjustments need to be made.

d. Turn the 3580A POWER switch to OFF and replace the two CRT shields removed in Step b). Turn the 3580A POWER switch back to ON (AC).

### 5-58. Display Adjustments.

5-59. These adjustments set the proper intensity limits, astigmatism, and trace alignment on the CRT. In many cases, these display parameters will require no adjustments.

### 5-60. Intensity Limit Adjustment.

a. Turn the 3580A power switch to OFF. Unplug the A13J3 connector. Remove the nylon access screw from the top of the high voltage power supply box. Turn the front panel INTENSITY control to the "9 o'clock" position. Turn the 3580A POWER switch back to ON (ac).

### WARNING

The voltages present inside the high voltage power supply box can cause serious injury. Never place an uninsulated conductive tool or object inside this box while the instrument is turned on.

- b. Using an insulated non-metalic tuning wand, such as -hp- Part No. 8710-0033, adjust A11R1 (INTENSITY LIMIT, inside high voltage power supply box) so that the dot on the CRT just disappears.
- c. Replace the nylon screw in the high voltage power supply box.

### 5-61. Astigmatism Adjustment.

- a. Adjust the front panel focus fully CCW. Turn the front panel INTENSITY adjust to about 10 or 11 o'clock so that the dot on the CRT is bright enough to see, but does not form a "halo".
- b. Adjust A8R2 (ASTIG. ADJ.) for the largest circular dot.
- c. Turn the 3580A POWER switch to OFF. Reconnect the connector to A13J3. Turn the 3580A POWER switch back to ON (ac).

### 5-62. Trace Alignment Adjustment.

a. Position the 3580A front panel controls as follows:

ADAPTIVE SWEEP	Centered
DISPLAY All pushbu	ttons released
AMPLITUDE MODE LO	G 10 dB/DIV
AMPLITUDE REF LEVEL	NORMAL
$dBv/LIN - dBm 600 \Omega \dots$	dBv/LIN
INPUT SENSITIVITY	30 dB
VERNIER (Amplitude)	CAL
	(Fully CW)
FREQUENCY	00.0 Hz
START - CTR	
RESOLUTION BANDWIDTH	300 Hz
DISPLAY SMOOTHING	MIN
FREQ. SPAN/DIV	0.2 KHz
SWEEP TIME/DIV	
SWEEP MODE	REP

Option 002: Set dBm 900  $\Omega/LIN$  - dBm 600  $\Omega$  switch to dBm 900  $\Omega$ ; set INPUT MODE switch to UNBAL.

- b. Adjust the front panel ADAPTIVE SWEEP for a line in the middle of the display. Adjust the front panel FOCUS control for the narrowest and sharpest line.
- c. Adjust A13R5 (TRACE ALIGN) for a level trace. If unable to achieve this, switch A13S1 and readjust A13R5.

### 5-63. Sweep Alignment and Dial Calibration.

5-64. These adjustments calibrate the front panel FRE-QUENCY dial plus align the frequency sweep limits. They should be done if the Frequency Tests (Paragraph 5-9) or Sweep Tests (Paragraph 5-13) of the Performance Tests cannot be passed by the instrument. In addition, the adjustment should be made if the high voltage supply was previously adjusted.

### 5-65. Recommended Test Equipment.

Digital Multimeter (-hp- Model 34740A and 34702A plug-on)

Electronic Counter (-hp- Model 5326A)

Oscilloscope (-hp- Model 180A with 1801A and 1820A plug-ins)

### 5-66. Linear Sweep Adjustments.

a. Position the following front panel controls: (Only those controls printed in BOLD require a change from the previous adjustments).

ADAPTIVE SWEEP OFF
DISPLAY All pushbuttons released
AMPLITUDE MODE LOG 10 dB/DIV
dBv/LIN - dBm 600 ΩdBv/LIN
INPUT SENSITIVITYCAL
VERNIER (Amplitude)CAL
(Fully CW)
FREQUENCY 00.0 kHz
START-CTRSTART
RESOLUTION BANDWIDTH 300 Hz
DISPLAY SMOOTHING MIN
FREQ. SPAN/DIV 0 kHz
SWEEP TIME/DIV 0.1 SEC
SWEEP MODE LOG ZERO

Option 002: Set dBm 900  $\Omega/\text{LIN}$  - dBm 600  $\Omega$  switch to dBm 900  $\Omega$ ; set INPUT MODE switch to UNBAL.

- b. Connect the multimeter (DC mode, 1 volt range) to the wiper of the front panel ZERO CAL pot (center terminal of pot). Adjust the front panel ZERO CAL pot for a dc reading on the multimeter of  $0 \pm 100$  mV.
- c. Set the counter to the Frequency Mode and adjust the time base/multiplier for a measurement of 1 MHz with 6 digits of resolution (1000.00 kHz). Adjust for maximum input sensitivity and either a zero trigger Level or Preset. For the -hp- Model 5326A Counter, the controls should be set to:

Sample Rate:

Fast

Function:

Freq.

Multiplier

 $10^{6}$ 

Channel A:

Slope +

mannoi A.

DC

DC

Atten: X1

BNC Input:

Sep.

Connect the counter Channel A Input to the LO Output terminal on the back of the 3580A, and adjust the Level control until the LO frequency is displayed.

d. Remove the inner circuit board shield (covering A2-A5). Connect the multimeter (DC mode 100 volt range) to A2TP4.

- e. Adjust A2L3 (100 kHz FREQ. ADJ.) for a reading of 1000.00 kHz ± .1 kHz on the counter. (100 kHz FREQ. ADJ. can be reached through side of circuit board card nest.)
- f. Adjust A2L1 (100 kHz VCO ADJ.) for a voltage reading on the multimeter between -1.5 V and -1.7 V. Record the reading.
- g. Repeat Steps e and f as necessary to meet the frequency and voltage specifications.
- h. Set the SWEEP MODE control to MANUAL and turn the MANUAL VERNIER control fully counterclockwise (CCW).
- i. Adjust A14R27 (DIAL LOW END ADJ.) for a display of 1000.00 kHz ± .01 kHz on the counter.
  - j. Reposition the following front panel control:

FREO. SPAN/DIV . . . . . . . . . 5 kHz

- k. Adjust A3R54 (INTEGRATOR BALANCE) for a display of  $1000.00 \text{ kHz} \pm .01 \text{ kHz}$  on the counter.
- 1. Position the front panel MANUAL VERNIER control fully clockwise (CW).
- m. Adjust A2R75 (BUFFER AMP GAIN ADJ.) for a display of  $1500.00 \text{ kHz} \pm .01 \text{ kHz}$  on the counter.
- n. Adjust A2R100 (VCO RANGE SET) for a reading on the multimeter equal to that obtained in Step  $f (\pm 10 \text{ mV})$ .
- o. Repeat Steps m and n as necessary to meet the frequency and voltage specifications.
- p. Position the front panel MANUAL VERNIER fully CCW.
  - q. Reposition the following front panel control:

FREQUENCY ..... 50.0 kHz

- r. Adjust A14R25 (DIAL HIGH END SET) for a display of 1500.00 kHz ± .01 kHz on the counter.
  - s. Reposition the following front panel control:

FREQUENCY ..... 00.0 kHz

- t. Readjust A14R27 (DIAL LOW END ADJ.) for a display of 1000.00 kHz ± .01 kHz on the counter.
- u. Repeat Steps q through t as necessary to meet the frequency specifications.
- v. Adjust the front panel FREQUENCY dial for 0, 10, 20, 30, 40 and 50 kHz. The corresponding frequency counter reading should be 1 MHz, 1.1 MHz, 1.2 MHz etc. with a tolerance of  $\pm$  1 kHz.

w. Reposition the following front panel controls::

FREQUENCY 00.0 kH	Iz
RESOLUTION BANDWIDTH 300 H	12
SWEEP TIME/DIV 2 SE	3C
SWEEP MODE	ΞP

- x. Adjust A13R1 (HORIZONTAL GAIN ADJ.) and A13R2 (HORIZONTAL POSITION ADJ.) for a full 10 cm display. The 10 kHz signal and its harmonics should fall on the proper graticule marking  $\pm$  1/2 minor divisions (2nd, 4th, 6th, 8th and 10th graticule from the left).
- y. Connect the input of the oscilloscope to A3TP11. Set the oscilloscope input to dc coupling. Connect a jumper between A3TP3 and A3TP4.
- z. Adjust the A3R14 (RAMP COMPARATOR BALANCE) so that the output of the ramp comparator (on scope) just changes states.
  - aa. Remove the jumpers from the A3 board.
  - ab. Reposition the following front panel control:

SWEEP TIME/DIV ..... 0.1 sec

ac. Alternately press and release the STORE pushbutton, adjusting A8R4 (RAMP SIZE ADJ.) so that the 40 kHz harmonic of the CAL signal falls on the same point for both the STORE and non-STORE display modes.

### 5-67. Log Sweep Adjustments.

a. Position the following front panel controls: (Only those controls printed in BOLD require a change from the previous adjustments).

ADAPTIVE SWEEP OFF
<b>DISPLAY</b> All pushbuttons released
AMPLITUDE MODE LOG 10 dB/DIV
AMPLITUDE REF LEVELNORMAL
$dBv/LIN - dBm 600 \Omega \dots dBv/LIN$
INPUT SENSITIVITY CAL
VERNIER (Amplitude) CAL
(Fully CW)
FREQUENCY 00.0 kHz
START - CTR START
Diritti Citti I I I I I I I I I I I I I I I I I I
RESOLUTION BANDWIDTH
RESOLUTION BANDWIDTH 300 Hz
RESOLUTION BANDWIDTH
RESOLUTION BANDWIDTH 300 Hz DISPLAY SMOOTHING

Option 002: Set dBm 900  $\Omega/\text{LIN}$  - dBm 600  $\Omega$  switch to dBm 900  $\Omega$ ; set INPUT MODE switch to UNBAL.

b. Momentarily push:

Adjust the front panel ZERO CAL pot for a peak at the left graticule. If the peak is off the screen, adjust A13R3 for an on screen indication.

c. Reposition the following front panel control:

SWEEP MODE . . . . . . LOG

d. Allow the 3580A to make three complete sweeps. Then adjust A3R76 (20 kHz LOG SWEEP ADJ.) so that the 20 kHz harmonic of the CAL signal falls on the 20 kHz LOG SWEEP graticule.

#### NOTE

After each adjustment of A3R76, wait for the 3580A to sweep through 20 kHz before attempting to readjust the setting.

### 5-68. Line Generator Adjustments.

- 5-69. This adjustment properly aligns the line generator circuitry. The adjustment is usually not necessary, but should be done if components in the high voltage power supply are changed, or if the display exhibits overshoot to abrupt level changes.
- a. Position the following front panel controls: (Only those controls printed in BOLD require a change from the previous adjustments).

A D A DTIVE CWEED	210
ADAPTIVE SWEEP OF	
DISPLAY All pushbuttons release	ed
AMPLITUDE MODELOG 10 dB/DI	
AMPLITUDE REF LEVEL NORMA	L
$dBv/LIN - dBm 600 \Omega \dots dBv/LI$	IN
INPUT SENSITIVITYCA	L
VERNIER (Amplitude)CA	
(Fully CV	
FREQUENCY 10.0 kI	
START - CTRCT	R
RESOLUTION BANDWIDTH 300 I	Hz
DISPLAY SMOOTHING	[N
FREQ. SPAN/DIV 0.2 KI	Ηz
SWEEP TIME/DIV 0.1 SH	EC
SWEEP MODE	L

Option 002: Set dBm 900  $\Omega/\text{LIN}$  - dBm 600  $\Omega$  switch to dBm 900  $\Omega$ ; set INPUT MODE switch to UNBAL.

- b. Adjust MANUAL VERNIER for a peak display signal. Note: The Amplitude VERNIER may have to be adjusted to keep the signal within the display limits.
  - c. Momentarily press:

DISPLAY ......CLEAR WRITE

d. Adjust A8C1 (LINE GENERATOR ADJ.) for a single round dot in the top center of the screen.

### 5-70. I.F. Filter Alignment.

5-71. This adjustment aligns the I.F. crystal filters for proper center frequency and symmetry. The TRACKING OSC is also precisely adjusted to 100 kHz. This adjustment should be done if the Bandwidth Tests (Paragraph 5-28) of the Performance Tests cannot be passed by the instrument. This adjustment will interact with the Amplitude Calibration (Paragraph 5-74). If it is performed, the Amplitude Tests (Paragraph 5-18) of the Performance Tests should be redone to verify whether any amplitude calibration is necessary.

Recommended Test Equipment: Timer/Counter (-hp- Model 5326A)

# 5-72. Tracking Oscillator and Center Frequency Adjustments.

a. Position the following front panel controls: (Only those controls printed in BOLD require a change from the previous adjustments).

ADAPTIVE SWEEPOFF
DISPLAY All pushbuttons released
AMPLITUDE MODELINEAR
AMPLITUDE REF LEVELNORMAL
$dBv/LIN - dBm 600 \Omega \dots dBv/LIN$
INPUT SENSITIVITY + 20 dB
VERNIER (Amplitude)CAL
(Fully CW)
FREQUENCY 10.0 kHz
START - CTRSTART
DISPLAY SMOOTHINGMIN
RESOLUTION BANDWIDTH 1 Hz
FREQ. SPAN/DIV 0.5 KHz
SWEEP TIME/DIV0.5 SEC.
SWEEP MODEMANUAL
Option 002: Set dBm 900 $\Omega/\text{LIN}$ - dBm 600 $\Omega$ switch to dBm 900 $\Omega$ ; set INPUT MODE switch to UNBAL.

b. Set the counter to the Frequency Mode and adjust the time base/multiplier for a measurement of 100 kHz with six digits of resolution (100.000 kHz). Adjust for maximum input sensitivity and either a zero trigger level or Preset. Select ac coupling on input. If using the -hp-Model 5326A Counter, the controls should be set to:

Sample Rate:

Fast

Function:

Frequency

Multiplier:

10<sup>7</sup>

Channel A:

Slope +

AC

Atten: X1

Level: Preset

BNC Input: Se

Sep.

- c. Connect the counter Channel A input to A2TP3. Adjust the 3580A rear panel TRACKING OSC LEVEL fully CW. Connect the rear panel TRACKING OSC OUT to the front panel INPUT.
- d. Adjust A2C4 (TRACKING OSCILLATOR 100 kHz FREQUENCY ADJ.) for a counter reading of 99.999 kHz to 100.001 kHz.
  - e. Center the CRT Trace with MANUAL VERNIER.
- f. Remove the blue lead between A5TP1 and A5TP2 and connect a clip lead between A5TP1 and A5TP6. Momentarily press:

### 

- g. Adjust A5C13 (STAGE 5 100 kHz ADJ.) for a maximum display indication. Remove the clip lead on A5TP6 and connect to A5TP5. Adjust A5C10 (STAGE 4 100 kHz ADJ.) for a maximum display indication. Repeat this procedure for A5TP4 (adjust A5C7), A5TP3 (adjust A5C4), and A5TP2 (adjust A5C1).
- h. Remove the cable between the TRACKING OSC OUT and the 3580A INPUT.

### 5-73. Symmetry Adjustments.

a. Position the following front panel controls: (Only those controls printed in BOLD require a change from the previous adjustments).

ADAPTIVE SWEEP	
DISPLAY All pushbutt	
AMPLITUDE MODE LOG	6 10 dB/DIV
AMPLITUDE REF LEVEL	NORMAL
$dBv/LIN - dBm 600 \Omega \dots$	dBv/LIN
INPUT SENSITIVITY	
VERNIER (Amplitude)	
	(Fully CW)
FREQUENCY	
START - CTR	
RESOLUTION BANDWIDTH	
DISPLAY SMOOTHING	
FREQ. SPAN/DIV	
SWEEP TIME/DIV	
SWEEP MODE	

OPTION 002: Set dBm 900  $\Omega/\text{LIN}$  - dBm 600  $\Omega$  switch to dBm 900  $\Omega$ ; set INPUT MODE switch to UNBAL.

b. Reposition the internal circuit board shield over circuit boards A2-A4 (leave A5 partially uncovered). Reconnect A5TP1 to A5TP6 and adjust MANUAL VERNIER while pressing and releasing DISPLAY - CLEAR WRITE to obtain a spike display indication in the center of the CRT screen.

- c. Fine tune the FREQUENCY dial for a maximum display indication.
  - d. Reposition the following front panel controls:

RESOLUTION	BANDWIDTH	300 Hz
SWEEP MODE		REP

- e. Adjust A5C14 (STAGE 5 CRYSTAL BALANCE ADJ.) for equal and symmetrical skirts on the right and left halves of the CRT display.
- f. Adjust A5C15 (STAGE 5 PEAK RESPONSE ADJ.) to move the peak to the center of the CRT screen. Recheck Step 3 and adjust A5C14 and A5C15 if necessary. See Figure 5-5 for a properly adjusted display.

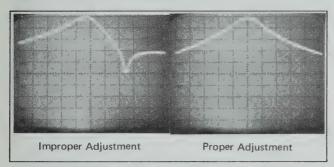


Figure 5-5. Symmetry Adjustment

g. Repeat Steps e and f for stages 4, 3, 2 and 1. Connect the appropriate test points and adjust the appropriate capacitors:

Stage	Test Point Connection	Balance Cap	Peak Cap
Stage 4 Stage 3 Stage 2 Stage 1	Connect A5TP1 to A5TP5 Connect A5TP1 to A5TP4 Connect A5TP1 to A5TP3 Connect A5TP1 to A5TP	C8	C12 C9 C6 C3

#### NOTE

For the narrower bandwidth displays, positioning the internal circuit board shield completely over A2-A5 eliminates noise and improves symmetry.

h. Reposition the following front panel controls:

AMPLITUDE MODE	LINEAR
RESOLUTION BANDWIDTH	30 Hz
FREQ. SPAN/DIV	50 Hz
SWEEP MODE	.MANUAL

i. Adjust MANUAL VERNIER while pressing and releasing DISPLAY - CLEAR WRITE for a spike display indication in the center of the screen. Adjust the front panel FREQUENCY dial for a maximum display indication. j. Reposition the following front panel controls:

RESOLUTION	<b>BANDWIDTH</b>	۰	 	 300 Hz
<b>SWEEP MODE</b>				 REP

- k. Disconnect the clip lead between A5TP1 and A5TP2 and reconnect it between A5TP1 and A5TP6.
- 1. Readjust A5C15 for a peak at the center of the display.
- m. Repeat Steps k and 1, adjusting A5C12, A5C9, A5C6, and A5C3 with the clip lead connected to the same test points used in Step g for these same capacitors.
- n. Remove the clip lead and reconnect the standard blue lead between A5TP1 and A5TP2.

### 5-74. Amplitude Calibration.

5-75. These adjustments properly calibrate the amplitude section of the 3580A. These adjustments should be made if the instrument fails the Amplitude Tests (Paragraph 5-18) of the Performance Tests. In addition, if the I.F. Filter Alignment (Paragraph 5-70), or the High Voltage Power Supply Adjustments (Paragraph 5-57) have been made, the Amplitude Tests should be performed again to determine if any amplitude calibration is necessary.

### 5-76. Recommended Test Equipment.

Frequency Synthesizer (-hp-Model 3320B, 50 ohms) Digital Multimeter (-hp- 34740/34702) 50 Ohm Termination (-hp- 11048C)

### 5-77. Linear and Log Gain Adjustments.

a. Position the following front panel controls: (Only those controls printed in BOLD require a change from the previous adjustments).

ADAPTIVE SWEEP	OFF
DISPLAY All pus	hbuttons released
AMPLITUDE MODE	
AMPLITUDE REF LEVEL	NORMAL
$dBv/LIN - dBm 600 \Omega \dots$	dBv/LIN
INPUT SENSITIVITY	20 dB
VERNIER (Amplitude)	CAL
	(Fully CW)
FREQUENCY	10.0 kHz
START - CTR	CTR
RESOLUTION BANDWIDTH	300 Hz
DISPLAY SMOOTHING	
FREQ. SPAN/DIV	0.5 kHz
SWEEP TIME/DIV	0.1 SEC
SWEEP MODE	MANUAL

Option 002: Set dBm 900  $\Omega/\text{LIN}$  - dBm 600  $\Omega$  switch to dBm 900  $\Omega$ ; set INPUT MODE switch to UNBAL.

- b. Adjust the front panel CAL 10 KHz pot fully CCW.
- c. Adjust the frequency synthesizer to -20 dBV 10.0 kHz output (Option 002, adjust to -20 dBm 900  $\Omega$ ) and connect the synthesizer to the INPUT terminals of the 3580A.

#### NOTE

Always terminate your source properly and consult Table 5-14 for the setting needed for a signal source calibrated in dBm 50  $\Omega$ . Figure 5-6 shows the proper hookup for use with a 50 ohm frequency synthesizer such as the 3320B.

- d. Adjust MANUAL VERNIER for a maximum display indication.
- e. Connect the multimeter (AC mode, 10 volt range) to A4TP1. Note the reading with the front panel 10 KHz CAL pot fully CCW.
- f. Adjust front panel CAL 10 KHz pot for 1.26 times the reading obtained in Step e.

### Examples:

Examples.	
100 mV x 1.26 = 126 mV	117 mV x 1.26 = 147 mV
101 mV x 1.26 = 127 mV	118 mV x 1.26 = 149 mV
102 mV x 1.26 = 129 mV	119 mV x 1.26 = 150 mV
103 mV x 1.26 = 130 mV	120 mV x 1.26 = 151 mV
104 mV x 1.26 = 131 mV	121 mV x 1.26 = 152 mV
105 mV x 1.26 = 132 mV	122 mV x 1.26 = 154 mV
106 mV x 1.26 = 134 mV	123 mV x 1.26 = 155 mV
107 mV x 1.26 = 135 mV	124 mV x 1.26 = 156 mV
108 mV x 1.26 = 136 mV	125 mV x 1.26 = 158 mV
109 mV x 1.26 = 137 mV	126 mV x 1.26 = 159 mV
110 mV x 1.26 = 139 mV	$127 \text{ mV} \times 1.26 = 160 \text{ mV}$
111 mV x 1.26 = 140 mV	128 mV x 1.26 = 161 mV
112 mV x 1.26 = 141 mV	129 mV x 1.26 = 163 mV
113 mV x 1.26 = 142 mV	130 mV x 1.26 = 164 mV
114 mV x 1.26 = 144 mV	131 mV x 1.26 = 165 mV
115 mV x 1.26 = 145 mV	132 mV x 1.26 = 166 mV
116 mV x 1.26 = 146 mV	133 mV x 1.26 = 168 mV

g. Turn 3580A POWER SWITCH to OFF. Place A4 on extender boards. Turn the power switch back to ON and reposition the following front panel control:

### AMPLITUDE MODE . . . . . LINEAR

- h. Push DISPLAY CLEAR WRITE momentarily. Adjust A4L1 (I.F. AMP GAIN ADJ.) for a maximum screen display. Remove the source from the 3580A INPUT.
- i. Set the controls of the multimeter for DC mode, 1 volt range. Connect the multimeter to the rear panel Y-AXIS output and adjust A4R11 (DC OFFSET ADJ.) for 0 volt Y-AXIS output level (± 10 mV).
- j. Turn POWER switch to OFF, replace A4 into card nest of 3580A, and turn power switch back to ON (AC). Reconnect the frequency synthesizer (with proper termination) to the 3580A INPUT. Push CLEAR WRITE momentarily. Adjust the source to the same level as in Step c (-20 dBV for standard instrument or -20 dBm 900  $\Omega$  if Option 002).

Table 5-14. Conversion Table.

	5-14. Conversion i	doro.
3580A INPUT	3320B or OTHER	ABSOLUTE
SIGNAL LEVEL	50 OHM SOURCE	VOLTAGE
+ 10 dBv	+ 23.01 dBm	3.162 volts
+ 10 dBm 900 Ω	+ 22.55 dBm	3 volts
0 dBv	+ 13.01 dBm	1 volts
0 dBm 900 Ω	+ 12.55 dBm	.949 volts
- 10 dBv	+ 3,01 dBm	.3162 volts
- 10 dBm 900 Ω	+ 2.55 dBm	.3000 volts
- 20 dBv	- 6.99 dBm	.1 volts
- 20 dBm 900 Ω	- 7.45 dBm	.0949 volts
- 30 dBv	- 16.9 dBm	.03162 volts
- 30 dBm 900 Ω	- 17.45 dBm	.03 volts
- 40 dBv	- 26.99 dBm	.01 volts
- 40 dBm 900 Ω	- 27.45 dBm	.095 volts
- 50 dBv	- 36.99 dBm	3162 mV
- 50 dBm 900 Ω	- 37.45 dBm	3 mV
- 60 dBv	- 46.99 dBm	1 mV
- 60 dBm 900 Ω	- 47.45 dBm	.95 mV
- 70 dBv	- 56.99 dBm	.3162 mV
- 70 dBm 900 Ω	- 57.45 dBm	.3 mV
- 80 dBv	- 66.99 dBm	.1 mV
- 80 dBm 900 Ω	- 67.45 dBm	.095 mV

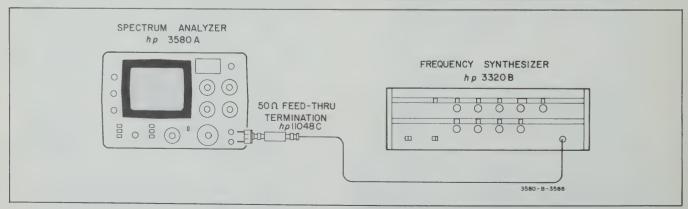


Figure 5-6. Proper Hookup.

k. Center the following pots:

A4R7, A4R8, A4R9, A4R10, A13R3 and A13R4.

1. Reposition the following front panel controls:

INPUT SENSITIVITY . . . . CAL SWEEP MODE . . . . . REP

### **NOTE**

If the peak of the waveform is beyond display limits, slightly readjust A13R3 or A13R4 to bring it into view.

- m. Alternately switch between the LOG 1 dB/DIV and LOG 10 dB/DIV AMPLITUDE MODEs and adjust A4R8 (DETECTOR GAIN ADJ.) until the peak amplitude of both waveforms is equal.
- n. Alternately switch between the LOG 10 dB/DIV and LINEAR AMPLITUDE MODEs and adjust A4R6 (LINEAR GAIN ADJ.) until the peak amplitude of both waveforms is equal.
- o. Repeat Steps m and n until the peak amplitude of all three waveforms is equal.
  - p. Reposition the following front panel control:

AMPLITUDE MODE . . . . . . LOG 10 dB/DIV

Press and hold the DISPLAY-CLEAR WRITE button to obtain a base line trace. Press the DISPLAY-STORE button to store the base line trace. Release the CLEAR WRITE button.

q. Adjust A13R3 (VERTICAL GAIN ADJ.) and A13R4 (VERTICAL ZERO ADJ.) for a full scale and base line screen display (waveform peak at 0 dB and base line at -100 dB). Press and release the DISPLAY-STORE button.

### **NOTE**

There may be some non-symmetry in the bottom corners of the CRT display. Use the center portion of the base line trace for the above calibration.

r. Reposition the following front panel controls:

INPUT SENSITIVITY .... - 20 dB

- s. Adjust the frequency synthesizer output level to -80 dBV for standard instruments or -80 dBm 900  $\Omega$  for instruments equipped with Option 002. See Table 5-14 for proper level setting of source.
- t. Adjust A4R7 (LOG GAIN ADJ.) so display peak is at the proper level. (-60 dB graticule ± 1 dB on CRT display, since full scale equals 20 dB).

### NOTE

This is a very low level signal. Always slide the cover shield over the A5 assembly after making an adjustment; then verify the results.

- u. Increase the signal level back to full scale (- 20 dBV for standard instruments or 20 dBm 900  $\Omega$  for Option 002) and adjust A4R8 (DETECTOR GAIN ADJ.) for a full scale (0 dB) indication on the display.
- v. Repeat Steps r, s, t, and u until the 0 dB and -60 dB points on the display are calibrated properly.
- w. Alternate the input signal level between 80 dBV and 60 dBV (- 80 dBm to 60 dBm 900  $\Omega$  for Option 002). See Table 5-14 for proper level. The indication should fall on the 60 dB and 40 dB ( $\pm$  1 dB) graticule lines of the display. If not, adjust A4R10 (BOTTOM END LINE-ARITY ADJ.) to bring these two points as close into tolerance as possilbe.
- x. Alternate the input signal level between 20 dBV and 40 dBV (- 20 dBm to 40 dBm,  $900 \Omega$  for Option 002). See Table 5-14 for proper level setting. These levels should give 0 dB and 20 dB (± 1 dB) indications on the display. If not, adjust A4R9 (TOP END LINEARITY ADJ.) to bring these two points as close into calibration as possible.
- y. Adjust the input signal level to 20 dBV (- 20 dBm, 900  $\Omega$  for Option 002). Switch the AMPLITUDE MODE pushbuttons between LOG 10 dB/DIV and LOG 1 dB/DIV. Adjust A4R8 (DETECTOR GAIN ADJ.) or A4R7 (LOG GAIN ADJ.) to make the levels for the two AMPLITUDE MODE settings equal.
  - z. Reposition the following front panel controls:

AMPLITUDE MODE .....LOG 10 dB/DIV

aa. Step the input signal level in 10 dB steps from a full scale 0 dB indication (- 20 dBV or - 20 dBm 900  $\Omega$  input signal) to a - 60 dB indication (- 80 dBV or - 80 dBm 900  $\Omega$  input signal). The display should fall within  $\pm$  2 dB of the proper graticule marking to meet specifications.

### NOTE

Remember to position the inner circuit board shield over A2-A5 when making low level measurements.

- ab. Repeat Steps r thru aa to bring the log amplifier into the desired test limits.
- ac. Adjust the input signal level to 20 dBV (Instruments with Option 002 should also have this same input level.)

ad. Reposition the following front panel control:

AMPLITUDE MODE .....LINEAR

ae. Adjust A4R6 (LINEAR GAIN ADJ.) for a full scale screen display.

### 5-78. Bandwidth Gain Switching Adjustments.

a. Position the following front panel controls: (Only those controls printed in BOLD require a change from the previous adjustments).

ADAPTIVE SWEEP OFF
DISPLAY All pushbuttons released
AMPLITUDE MODE LINEAR
AMPLITUDE REF LEVEL NORMAL
$dBv/LIN - dBm 600 \Omega \dots dBv/LIN$
INPUT SENSITIVITY20 dB
VERNIER (Amplitude) CAL
(Fully CW)
FREQUENCY 10.0 kHz
START - CTRCTR
DISPLAY SMOOTHING
RESOLUTION BANDWIDTH 300 Hz
FREQ. SPAN/DIV50 Hz
SWEEP TIME/DIV
SWEEP MODE MANUAL

Option 002: Set dBm 900  $\Omega/LIN$  - dBm 600  $\Omega$  switch to dBm 900  $\Omega$ ; set INPUT MODE switch to UNBAL.

- b. Replace the inner cover shield over A2-A5 and screw down tightly.
- c. Adjust the frequency synthesizer to a 10 kHz 20 dBV output (same level for Option 002). See Table 5-14 for proper level setting on the frequency synthesizer.
- d. Adjust MANUAL VERNIER and the front panel ZERO CAL pot for a peak reading in the center of the display. Make the following full scale adjustments on the appropriate bandwidth setting.

#### NOTE

The ZERO CAL pot may have to be readjusted after each Bandwidth/Freq. Span setting for a peak reading in the center of the screen.

RESOLUTION	FREQ.	GAIN	SETTING
BANDWIDTH	SPAN/DIV	POT ADJ.	
100 Hz 30 Hz 10 Hz 3 Hz 1 Hz	50 Hz 10 Hz 5 Hz 5 Hz 5 Hz	A4R5 A4R4 A4R3 A4R2 A4R1	Full scale 0 dB display indi- cation.

### 5-79. Frequency Response Adjustments.

a. Position the following front panel controls: (Only those controls printed in BOLD require a change from the previous adjustments.

ADAPTIVE SWEEP OFF
DISPLAY All pushbuttons released
AMPLITUDE MODE 1 dB/DIV
AMPLITUDE REF LEVEL NORMAL
$dBv/LIN - dBm^{2}600 \Omega \dots dBv/LIN$
INPUT SENSITIVITY20 dB
VERNIER (Amplitude) CAL
(Fully CW)
FREQUENCY 01.0 kHz
START - CTR CTR
RESOLUTION BANDWIDTH 300 Hz
DISPLAY SMOOTHINGMIN
FREQ. SPAN/DIV 0.2 kHz
SWEEP TIME/DIV 0.1 SEC
SWEEP MODE
Option 002: Set dBm 900 $\Omega/\text{LIN}$ - dBm 600 $\Omega$
switch to dBm $900 \Omega$ ; set INPUT MODE
switch to UNBAL.

- b. Adjust the frequency synthesizer for 1 kHz output at 20 dBV (- 20 dBm 900  $\Omega$  for Option 002), and connect it to the 3580A INPUT (properly terminated). Adjust the front panel CAL 10 KHz for a full scale (0 dB) display.
  - c. Reposition the following front panel controls:

FREQUENCY ...... 40.0 kHz INPUT SENSITIVITY ..... 10 dB

- d. Adjust the signal source for a 40 kHz 10 dBV signal (-10 dBm 900  $\Omega$  for Option 002).
- e. Adjust A9C2 (40 kHz 10 dB AMP ADJ.) for a full scale (0 dB) display (± 1 minor division). In a similar manner, perform the following adjustments:

SIGNAL SOURCE OUTPUT LEVEL	INPUT SENSITIVITY	ADJUST	DISPLAY READING
0 dBV (or - 0 dBm 900 Ω)	0	A9C3	0 dB ± .2 dB
+ 10 dBV (or 0 dBm 900 Ω)	+ 10	A9C4	0 dB ± .2 dB

f. Adjust the ac signal source to 1 kHz at + 10 dBV (+ 10 dBm 900  $\Omega$  for Option 002). Reposition the following front panel controls:

AMPLITUDE REF LEVEL - 10 dB
INPUT SENSITIVITY + 20 dB (According to MAX INPUT indicator, not white underlay on INPUT SENSITIVITY dial).
FREQUENCY 01.0 kHz

g. Store the screen display level by pushing:

DISPLAY .....STORE

h. Adjust the signal source for a 40 kHz output (same level as in Step f). Reposition the following front panel controls:

FREQUENCY ...... 40.0 kHz

- i. Adjust A9C5 (40 kHz + 20 dB AMP ADJ.) for the same level stored in Step g.
  - j. Reposition the following front panel controls:

AMPLITUDE REF LEVEL 20 dB
INPUT SENSITIVITY + 30 dB
(According to MAX
INPUT indicator)
FREQUENCY 01.0 kHz

k. Adjust the signal source to a 1 kHz + 10 dBV (+ 10 dBm for Option 002) output. (Note the screen display level by releasing and then depressing:

DISPLAY .....STORE).

1. Adjust the signal source for a 40 kHz output (same level as in Step k). Reposition the following front panel control:

FREQUENCY ..... 40.0 kHz

m. Adjust A9C6 (40 kHz + 30 dB AMP ADJ.) for the same level stored in Step k).

### 5-80. Internal Calibrator Adjustment.

a. Position the following front panel controls: (Only those controls printed in BOLD require a change from the previous adjustments).

DISPLAY All pushbuttons released AMPLITUDE MODE
AMPLITUDE REF LEVEL NORMAL
This bir obb itel be vee
$dBv/LIN - dBm 600 \Omega \dots dBv/LIN$
INPUT SENSITIVITY 20 dB
VERNIER (Amplitude)CAL
(Fully CW)
FREQUENCY 10.0 kHz
START-CTRCTR
RESOLUTION BANDWIDTH 300 Hz
DISPLAY SMOOTHINGMIN
FREQ. SPAN/DIV 0.2 KHz
SWEEP TIME/DIV 0.1 SEC
SWEEP MODE REP

Option 002: Set dBm 900  $\Omega/\text{LIN}$  - dBm 600  $\Omega$  switch to dBm 900  $\Omega$ ; set INPUT MODE switch to UNBAL.

- b. Connect a properly terminated frequency synthesizer to the 3580A INPUT and adjust the synthesizer for a 10 kHz 20 dBV output level (- 20 dBm 900  $\Omega$  for instruments with Option 002). See Table 5-14 for proper settings.
- c. Adjust front panel CAL 10 KHz for a full scale (0 dB) peak on the display.
  - d. Reposition the following front panel control:

e. Remove the input signal source. Adjust A2R5 (CAL LEVEL ADJ.) for a full scale (0 dB) screen display.

### 5-81. Mixer Balance Adjustments.

5-82. These adjustments balance the input mixer and tracking oscillator mixer. These adjustments should be done if the zero beat response of the instrument under calibration is too large (> - 30 dB) or if the TRACKING OSC OUTput is distorted (> - 40 dB distortion).

### 5-83. Recommended Test Equipment:

Oscilloscope (-hp- Model 180A with 1801A and 1820A plug-ins)

- a. Disconnect all signal sources from the 3580A.
- b. Position the following front panel controls: (Only those controls printed in BOLD require a change from the previous adjustments).

ADAPTIVE SWEEP OFF
DISPLAY All pushbuttons released
AMPLITUDE MODE LOG 10 dB/DIV
AMPLITUDE REF LEVELNORMAL
$dBv/LIN - dBm 600 \Omega \dots dBv/LIN$
INPUT SENSITIVITY 20 dB
VERNIER (Amplitude)CAL
(Fully CW)
FREQUENCY 00.0 kHz
START - CTRSTART
RESOLUTION BANDWIDTH 300 Hz
DISPLAY SMOOTHING MIN
FREQ. SPAN/DIV 0.2 KHz
SWEEP TIME/DIV 0.1 SEC
SWEEP MODE REP

Option 002: Set dBm 900  $\Omega/\text{LIN}$  - dBm 600  $\Omega$  switch to dBm 900  $\Omega$ ; set INPUT MODE switch to UNBAL.

c. Adjust A9R1 (MIXER BALANCE) for a minimum screen display (less than - 30 dB to meet specifications).

d. Reposition the following front panel controls:

FREQUENCY	,						a		. 05.0 kHz
<b>SWEEP MODE</b>									

- e. Adjust the rear panel TRACKING OSC LEVEL fully CW, and set the EXT REF NORMAL switch to NORMAL.
- f. Connect the oscilloscope to the rear panel TRACK-ING OSC OUT connector and monitor the output.
- g. Adjust A2R113 (T.O. MIXER BALANCE) for the cleanest signal. See Figure 5-7.

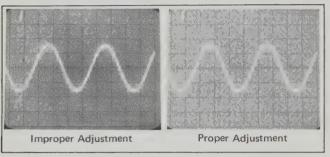


Figure 5-7. Tracking Oscillator Output Adjustment.

### 5-84. Adaptive Sweep Marker Adjustment.

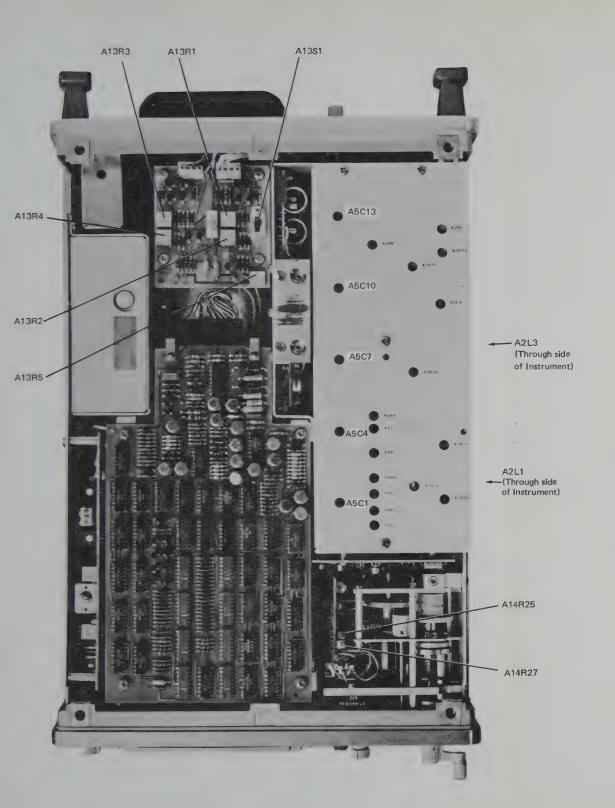
5-85. This adjustment properly positions the ADAPTIVE SWEEP marker. If the marker (blank spot on screen) does not appear at the same point on the display as new information being written onto the display, do this adjustment:

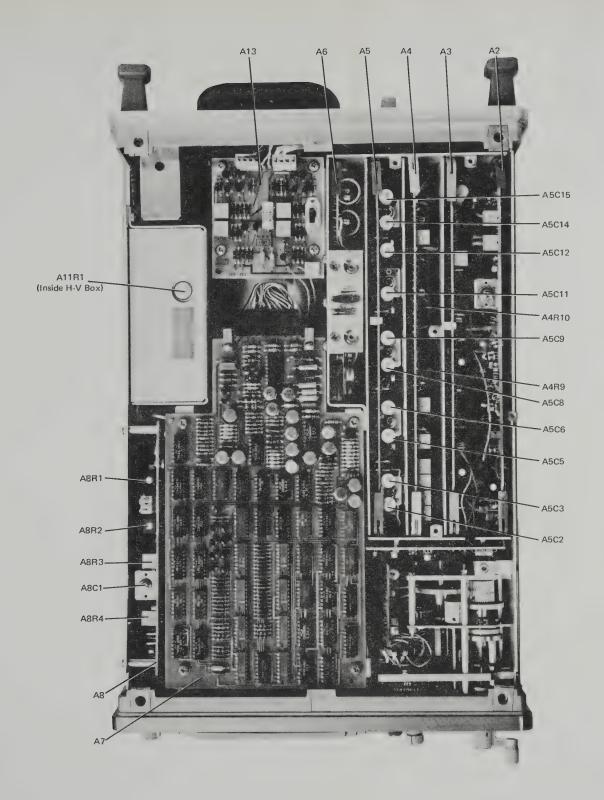
a. Position the following front panel controls: (Only those controls printed in BOLD require a change from the previous adjustments).

ADAPTIVE SWEEP OFF
(Fully CCW)
DISPLAY All pushbuttons released
AMPLITUDE MODE LOG 10 dBV/DIV
AMPLITUDE REF LEVELNORMAL
$dBv/LIN - dBm 600 \Omega \dots dBv/LIN$
INPUT SENSITIVITYCAL
VERNIER (Amplitude)CAL
(Fully CW)
FREQUENCY 00.0 kHz
START-CTRSTART
RESOLUTION BANDWIDTH 300 Hz
DISPLAY SMOOTHINGMIN
FREQ. SPAN/DIV
SWEEP TIME/DIV
SWEEP MODE MAN
Option 002: Set dBm 900 $\Omega/\text{LIN}$ - dBm 600 $\Omega$

switch to dBm 900  $\Omega$ ; set INPUT MODE switch to UNBAL.

- b. Adjust the MANUAL VERNIER control until the trace is at the peak of the 10 kHz signal.
- c. Momentarily press the DISPLAY-CLEAR WRITE button. A dot should remain at the top of the scope.
- d. Turn the ADAPTIVE SWEEP on and adjust A8R3 (SWEEP MARKER ADJ.) until the sweep marker (blank spot in trace) blanks out the dot at the top of the scope.





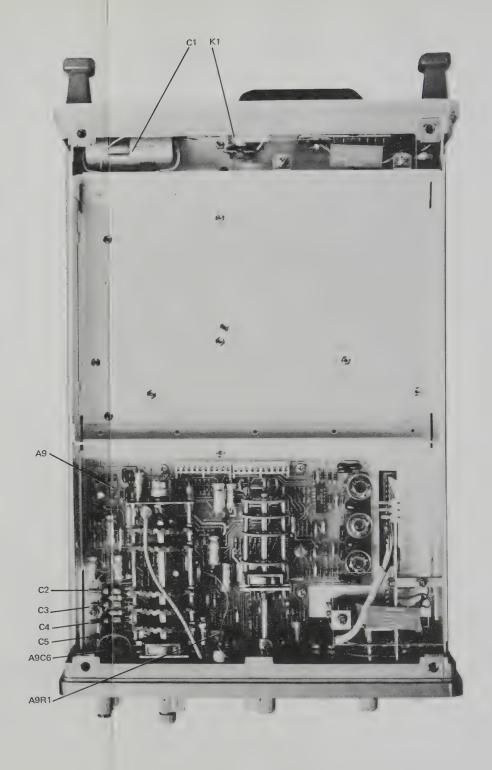


Figure 5-8. Test Point and Adjustment Locations.



### PERFORMANCE TEST CARD

Hewlett-Packard Model 3580A	Tests Performed By						
Spectrum Analyzer Serial No.	Date						
RANGE AND FREQUENCY DIAL ACC	URACY TESTS						
Ideal Frequency	Actual Setting		Test Limit				
Dial Setting	for a Peak						
10 kHz	kHz		± .1 kHz				
20 kHz	kHz		± .1 kHz				
30 kHz	kHz		± .1 kHz				
40 kHz 50 kHz	kHz kHz		± .1 kHz ± .1 kHz				
DISPLAY ACCURACY TESTS	ANA		- 11 1111				
The separation between the Zero Respharmonic should be 10 div. ± .2 div between any two adjacent responses	7. The separation	Pass	Fail				
± .04 div.							
FREQUENCY SPAN TESTS							
	Counter Reading						
Frequency Span/Div.	(Manual Vernier Fully CW)	Test Limits					
5 Hz	kHz	1000.50 kHz ±	.01 kHz				
10 Hz	kHz	1001.00 kHz ±					
20 Hz	kHz	1002.00 kHz ±					
50 Hz . 1 kHz	kHz	1005.00 kHz ±					
. 2 kHz	kHz kHz	1010.00 kHz ± 1020.00 kHz ±					
. 5 kHz	kHz	1050.00 kHz ±					
1 kHz	kHz	1100.00 kHz ±					
2 kHz	kHz	1200.00 kHz ±	4.00 kHz				
LOG SWEEP TEST							
The 20 kHz harmonic of the internal CA on the 20 kHz LOG SWEEP graticule (±		Pass	Fail				
SWEEP TIME TEST							
All sweep rates must work properly.		Pass	Fail				
BANDWIDTH SWITCHING ACCURACY	TEST						
Bandwidth	Display Indication	Test Limits					
Dalluwium	(0 dB full scale) (1 dB/div)	Test Emilis					
100 Hz	dB	- 1.0 dB ± .5 dB					
30 Hz	dB	$-1.0 \text{ dB} \pm .5 \text{ dB}$					
10 Hz	dB	$-1.0 \text{ dB} \pm .5 \text{ dB}$					
3 Hz	dB	$-1.0 \text{ dB} \pm .5 \text{ dB}$					
1 Hz	dB	$-1.0 \text{ dB} \pm 1 \text{ dB}$					

### LOG AMPLITUDE DISPLAY ACCURACY TESTS

Inpu	Level		
Standard	Option 002 900 Ω	Display Indication (0 dB full scale) (10 dB/div)	Test Limits
- 10 dBV	- 10 dBm 900 Ω	dB	$-10  \mathrm{dB} \pm 2  \mathrm{dB}$
- 20 dBV	- 20 dBm	dB	$-20 \text{ dB} \pm 2 \text{ dB}$
- 30 dBV	- 30 dBm	dB	$-30 \text{ dB} \pm 2 \text{ dB}$
- 40 dBV	- 40 dBm	dB	$-40 \text{ dB} \pm 2 \text{ dB}$
		- dD	$-50 \text{ dB} \pm 2 \text{ dB}$
- 50 dBV	- 50 dBm	dB	$-60 \text{ dB} \pm 2 \text{ dB}$
- 60 dBV	- 60 dBm		
- 70 dBV	- 70 dBm	dB	$-70 dB \pm 2 dB$
- 80 dBV	- 80 dBm	dB	$-80 \text{ dB} \pm 2 \text{ dB}$
LINEAR AMPI	ITUDE DISPLAY ACCURA	ACY TESTS	

Input Level	Display Indication (1 V full scale)	Test Limits
0.17	(10 dB/div)	00 1/ ( 02 1/
.9 V	V	$.90 \text{ V} \pm .02 \text{ V}$
.8 V	V	$.80 \text{ V} \pm .02 \text{ V}$
.7 V	V	$.70 \text{ V} \pm .02 \text{ V}$
.6 V	V	$.60 \text{ V} \pm .02 \text{ V}$
.5 V	V	$.50 \text{ V} \pm .02 \text{ V}$
.4 V	V	$.40 \text{ V} \pm .02 \text{ V}$
.3 V	V	$.30 \text{ V} \pm .02 \text{ V}$
.2 V	V	$.20 \text{ V} \pm .02 \text{ V}$
.1 V	V	.10 V ± .02 V

### AMPLITUDE REFERENCE TESTS (Linear Mode)

3580A Input (10 kHz)	Amp Rev Level	Display Indication (% of full scale)	Test Limits
200 mV	- 10		90% ± 3%
100 mV	- 20	%	(± .3 major divisions) 90% ± 3% (± .3 major divisions)
20 mV	- 30	%	90% ± 3%
10 mV	- 40	%	( $\pm$ .3 major divisions) 90% $\pm$ 3%
2 mV	- 50		( $\pm$ .3 major divisions) 90% $\pm$ 3%
1 mV	- 60	%	( $\pm$ .3 major divisions) 90% $\pm$ 3%
.2 mV	- 70		(± .3 major divisions) 90% ± 10% (± 1 major division)

### AMPLITUDE REFERENCE LEVEL TEST (Log Mode)

Amp Rev Level	Multimeter Reading	Test Limits
- 10 dB - 20 dB - 30 dB - 40 dB - 50 dB - 60 dB - 70 dB	V V V V V V V V V V V V V V V V V V V	2.00 V ± .02 V 2.50 V ± .02 V 3.00 V ± .03 V 3.50 V ± .03 V 4.00 V ± .04 V 4.50 V ± .04 V 5.00 V ± .05 V

### INPUT ATTENUATOR TESTS

Input	Amp Ref Level	Input Sensitivity (according to white marker)	Display Indication (% of full scale)	Test Limits (full scale ± .3 major div)
.2 V	- 30 dB	.2 V	%	100% ± 3%
.1 V	- 30 dB	.1 V	%	100% ± 3%
20 mV	- 30 dB	20 mV	%	100% ± 3%
.2 V	normal	.2 V	%	$100\% \pm 3\%$
.1 V	normal	.1 V	%	100% ± 3%
20 mV	normal	20 mV	%	100% ± 3%
10 mV	normal	10 mV	%	$100\% \pm 3\%$
2 mV	normal	2 mV	%	100% ± 3%
1 mV	normal	1 mV	%	100% ± 3%
.2 mV	normal	.2 mV	%	$100\% \pm 3\%$

### FREQUENCY RESPONSE TESTS

Input Le	evel				
Standard	Option 002 (900 Ω)	Input Sensitivity (according to white marker)	Frequency	Display Indication (0 dB = full scale 1 dB/div)	Test Limits
0 dBV 0 dBV 0 dBV 0 dBV 0 dBV	O dBM O dBM O dBM O dBM O dBM	0 dB 0 dB 0 dB 0 dB 0 dB	10 Hz 20 Hz 1 kHz 20 kHz 50 kHz	dB dB dB dB	0 dB ± .5 dB 0 dB ± .3 dB 0 dB ± .3 dB 0 dB ± .3 dB 0 dB ± .5 dB
- 10 dBV - 10 dBV - 10 dBV - 10 dBV - 10 dBV	- 10 dBM - 10 dBM - 10 dBM - 10 dBM - 10 dBM	- 10 dB - 10 dB - 10 dB - 10 dB - 10 dB	10 Hz 20 Hz 1 kHz 20 kHz 50 kHz	dB dB dB dB	0 dB ± .5 dB 0 dB ± .3 dB 0 dB ± .3 dB 0 dB ± .3 dB 0 dB ± .5 dB
- 20 dBV - 20 dBV - 20 dBV - 20 dBV - 20 dBV	- 20 dBM - 20 dBM - 20 dBM - 20 dBM - 20 dBM	- 20 dB - 20 dB - 20 dB - 20 dB - 20 dB	10 Hz 20 Hz 1 kHz 20 kHz 50 kHz	dB dB dB dB	0 dB ± .5 dB 0 dB ± .3 dB 0 dB ± .3 dB 0 dB ± .3 dB 0 dB ± .5 dB
- 30 dBV - 30 dBV - 30 dBV - 30 dBV - 30 dBV	- 30 dBM - 30 dBM - 30 dBM - 30 dBM - 30 dBM	- 30 dB - 30 dB - 30 dB - 30 dB - 30 dB	10 Hz 20 Hz 1 kHz 20 kHz 50 kHz	dB dB dB dB	0 dB ± .5 dB 0 dB ± .3 dB 0 dB ± .3 dB 0 dB ± .3 dB 0 dB ± .5 dB
- 40 dBV - 40 dBV - 40 dBV - 40 dBV - 40 dBV	- 40 dBM - 40 dBM - 40 dBM - 40 dBM - 40 dBM	- 40 dB - 40 dB - 40 dB - 40 dB - 40 dB	10 Hz 20 Hz 1 kHz 20 kHz 50 kHz	dB dB dB dB	0 dB ± .5 dB 0 dB ± .3 dB 0 dB ± .3 dB 0 dB ± .3 dB 0 dB ± .5 dB

### INTERNAL CALIBRATOR TEST

Display Indication	
(0 dB = full scale)	Test Limit
dB	$0 dB \pm .15 dB$

10 kHz Cal. Signal Level

### **BANDWIDTH TESTS**

Resolution Bandwidth	Lower 3 dB Frequency	Upper 3 dB Frequency	Test Limits
300 Hz 100 Hz 30 Hz 10 Hz	10 kHz 1 kHz 1 kHz 1 kHz	kHz kHz kHz kHz	10.3 kHz ± 45 Hz 1.1 kHz ± 15 Hz 1.030 kHz ± 4.5 Hz 1.010 kHz ± 1.5 Hz
Resolution Bandwidth	Lower 60 dB Frequency	Upper 60 dB Frequency	Test Limits
3 Hz 1 Hz	1 kHz 1 kHz	kHz kHz	1.030 kHz ± 4.5 Hz 1.010 kHz ± 1.5 Hz
NOISE LEVEL TESTS			
Bandwidth	Frequency	Noise (- 70 dB = full scale)	Test Limits
300 Hz 30 Hz 30 Hz 30 Hz 1 Hz 1 Hz 1 Hz	10 kHz 10 kHz 100 Hz 1 kHz 1 kHz 100 Hz	dBdBdBdBdBdB	<- 130 dB <- 140 dB <- 132 dB <- 140 dB <- 150 dB <- 143 dB <- 135 dB
NOISE SIDEBAND TEST			
Noise Sidebands must be a wave signal, ± 10 Hz away.	t least 70 dB below continuous	Pass	Fail ———
SPURIOUS RESPONSE TE	ST		
All non-line-related spurio 80 dB below a full scale refe	us responses must be at least erence.	Pass	Fail
LINE-RELATED SPURIOU	IS RESPONSE TEST		
All line-related spurious - 140 dBV (.1 $\mu$ V).	responses must be less than	Pass	Fail
IF FEEDTHRU TEST			
IF Feedthru must be at le reference.	ast - 70 dB below the full scale	Pass	Fail
ZERO BEAT RESPONSE T	EST		
The zero beat response mu full scale reference.	ast be at least 30 dB below the	Pass	Fail
INPUT IMPEDANCE TEST	S		
Frequency	Display Indication (0 dB = full scale) Without 1 M $\Omega$ With 1 M $\Omega$	Test	Limit
1 kHz	0 dB dB	- 3 dl	B ± 1 dB

#### INPUT IMPEDANCE TESTS (cont'd) Display Indication Test Limit Frequency (with 1 M $\Omega$ Resistor) 1 kHz 0 dB 10 kHz \_\_\_\_ dB $-3 dB \pm 1 dB$ TRACKING OSCILLATOR OUTPUT TESTS FREQUENCY RESPONSE: Instrument Frequency Multimeter Reading Test Limits Standard 50 Hz 2.00 volts rms \_\_\_\_ volts rms 50 kHz $2.00 \text{ volts} \pm .06 \text{ volts}$ 300 Hz Option 002 2.00 volts rms 20 kHz $2.00 \text{ volts} \pm .1 \text{ volt}$ \_\_\_\_ volts rms FREQUENCY ACCURACY: Resolution Bandwidth Display Indication Test Limit 2 V (full scale) 30 Hz 3 Hz ..... V 1 V - 2 V (half to full scale) DISTORTION: Distortion: \_\_\_\_\_dB Test Limit: less than - 40 dB RECORDER OUTPUT TESTS Display Indication Multimeter Reading Test Limits Recorder Output $0 \text{ V dc} \pm .15 \text{ V}$ X-Axis Manual Vernier fully CCW Manual Vernier fully CW 5 V dc ± .15 V 5 V dc ± .15 V Y-Axis Full Scale $0 \text{ V dc} \pm .15 \text{ V}$ Bottom Graticule **COMMON MODE REJECTION TEST (Option 002 only)** Display Indication (full scale = $0 \text{ dBM } 900 \Omega$ ) Common Mode Input Test Limit $60 \text{ Hz} - 0 \text{ dBM } 900 \Omega$ $\longrightarrow$ dBM 900 $\Omega$ Less than - 60 dBm 900 $\Omega$ FREQUENCY RESPONSE TEST (Option 002 only) Display Indication Test Limit Frequency $(-1 \text{ dB} = 0 \text{ dBM } 900 \Omega, 1 \text{ dB/div})$ (± .5 major div) \_\_\_ dBM 900 Ω 300 Hz $-1 dB \pm .5 dB$

\_\_\_\_\_ dBM 900 Ω

 $\square$  dBM 900  $\Omega$ 

 $-1 dB \pm .5 dB$ 

 $-1 dB \pm .5 dB$ 

1 kHz

20 kHz



# SECTION VI REPLACEABLE PARTS

## 6-1. INTRODUCTION.

- 6-2. This section contains information for ordering replacement parts. Table 6-1 lists parts in alphameric order of their reference designators and indicates the description, -hp-part number of each part, together with any applicable notes, and provides the following:
- a. Total quantity used in the instrument (Qty column). The total quantity of a part is given the first time the part number appears.
- b. Description of the part. (See list of abbreviations below.)
- c. Typical manufacturer of the part in a five-digit code. (See Appendix A for list of manufacturers.)
  - d. Manufacturers part number.
- 6-3. Miscellaneous parts are listed at the end of Table 6-1.

# 6-4. ORDERING INFORMATION.

6-5. To obtain replacement parts, address order or inquiry to your local Hewlett-Packard Field Office. (See Appendix B for list of office locations.) Identify parts by their Hewlett-Packard part numbers. Include instrument model and serial numbers.

#### 6-6. NON-LISTED PARTS.

- 6-7. To obtain a part that is not listed, include:
  - a. Instrument model number.
  - b. Instrument serial number.
  - c. Description of the part.
  - d. Function and location of the part.

## 6-8. PROPRIETARY PARTS.

6-9. Items marked by a dagger (†) in the reference designator column are available only for repair and service of Hewlett-Packard instruments.

			ABBREV	IATIONS			
Agsilver	He	hortz (	cycle(s) per second)		nenati	a positive zero	sl slide
Alaluminum			cycle(s) per second)	1110	(zero temperati		SPDT single-pole double-throw
A ampere(s)			inside diameter	ns	. nanosecond(s)		SPST single-pole single-throw
Au			impregnated		not separat		or or strigle-pole strigle-till ov
, a			incandescent	1101	not separat	ciy replaceable	Tatantalum
C capacitor			insulation(ed)	0		ahm(r)	TC temperature coefficient
cer			msuration(eu)		order		TiO <sub>2</sub> titanium dioxide
coef coefficient		kile	$hm(s) = 10^{+3} ohms$	OD			
com			ohertz = $10^{+3}$ hertz	00		utside diameter	togtoggle
			offertz - 10 - Hertz	_		and.	tol tolerance
comp composition			inductor				trimtrimmer
connconnection							TSTR transistor
des de la contraction de la co			linear taper				
dep deposited			. logarithmic taper		picofarad(		V
DPDT double-pole double-throw			.1.1 40-3		peak		vacw , , , . alternating current working voltage
DPST double-pole single-throw			e(s) = 10 <sup>-3</sup> amperes				var
			ahertz = 10 <sup>+6</sup> hertz				vdcw direct current working voltage
electelectrolytic			ohm(s) - 10 <sup>+6</sup> ohms				
encapencapsulated			metal film				W watt(s)
			manufacturer				w/ with
F			millisecond				wiv working inverse voltage
FET field effect transistor			mounting	prec p			w/o without
fxdfixed			$ivolt(s) = 10^{-3} volts$	long	term stability an	d/or tolerance)	wwwirewound
			microfarad(s)				
GaAs gallium arsenide			microsecond(s)				
GHz gigahertz = 10 <sup>+9</sup> hertz			$volt(s) = 10^{-6} volts$				
gd guard(ed)	my		Mylar(R)	rms	ro	ot-mean-square	* optimum value selected at factory,
Gegermanium			_	rot		son the mile	average value shown (part may be omitted)
				101		rotary	
gndground(ed)		nanoampe	re(s) = 10 <sup>-9</sup> amperes	,,,,,,,,,,		rotary	** no standard type number assigned
gndground(ed)	nA		re(s) = 10 <sup>-9</sup> amperes normally closed	Se		,	
gnd        ground(ed)           H        henry(ies)	nA					selenium	** no standard type number assigned selected or special type
	nA NC Ne		normally closed	Se		selenium	** no standard type number assigned
H henry(ies)	nA NC Ne		normally closedneon normally open	Sesect		selenium	** no standard type number assigned selected or special type
H henry(ies)	nA NC Ne		normally closedneon normally open	Se		selenium	** no standard type number assigned selected or special type
H henry(ies)	NC Ne NO	Symbols	normally closed	Se	Symbo Is	seleniumsection(s)silicon Multiplier	** no standard type number assigned selected or special type
H henry(ies)	NC Ne NO	Symbols	normally closed	Se		seleniumsection(s)silicon  Multiplier  10-2	** no standard type number assigned selected or special type
H henry(ies)	NC Ne NO	Symbols	normally closed	Se	Symbo Is	seleniumsection(s)silicon Multiplier	** no standard type number assigned selected or special type
H henry(ies)	NC	Symbols  T G	normally closed	SesectSiULTIPLIERS  Prefix  centi	Symbols c	seleniumsection(s)silicon	** no standard type number assigned selected or special type
H henry(ies)	nA	Symbols  T  G  M or Meg	normally closedneon neon neon DECIMAL M Multiplier 1012 109 106	Se	Symbols  c m	seleniumsection(s)silicon  Multiplier  10.2 10.3 10.6	** no standard type number assigned selected or special type
H henry(ies)	nA	Symbols  T G M or Meg K or k	normally closed	Se	Symbols  c  m		** no standard type number assigned selected or special type
H henry(ies)	nA	Symbols  T  G  M or Meg	normally closedneon neon neon DECIMAL M Multiplier 1012 109 106	Se	Symbols  c m	seleniumsection(s)silicon  Multiplier  10-2 10-3 10-6 10-9 10-12	** no standard type number assigned selected or special type
H henry(ies)	nA	Symbols  T G M or Meg K or k	normally closed	Se	Symbols  c  m		** no standard type number assigned selected or special type
H henry(ies)	nA	Symbols  T  G  M or Meg  K or k  h	normally closed	Se	Symbols  c m μ n p f	seleniumsection(s)silicon  Multiplier  10-2 10-3 10-6 10-9 10-12	** no standard type number assigned selected or special type  (R) Dupont de Nemours
H henry(ies)	nA	Symbols  T G M or Meg K or k h da	normally closed neon .	Se	Symbols  c m     n p	seleniumsection(s)silicon  Multiplier  10-2 10-3 10-6 10-9 10-12 10-15	** no standard type number assigned selected or special type
H henry (ies) Hg mercury	nA	Symbols  T G M or Meg K or k h da d	normally closed	Se	Symbols  c m  µ n p f a	seleniumsection(s)silicon  Multiplier  10-2 10-3 10-6 10-9 10-12 10-15 10-18	** no standard type number assigned selected or special type  R Dupont de Nemours  STD-B-2734
H henry (ies) Hg mercury  A assembly	nA NC NC NC NC NO Prefix tera giga mega kilo hecto deka deci	Symbols  T G M or Meg K or k h da d	normally closed	Se	C m	seleniumsection(s)silicon  Multiplier  10-2 10-3 10-6 10-9 10-12 10-15 10-18transistor	**
H	nA	Symbols  T G M or Meg K or k h da d	normally closed neon .	Se	Symbols  c m  µ n p f a	seleniumsection(s)silicon  Multiplier  10-2 10-3 10-6 10-9 10-12 10-15 10-18transistor-diode	**
Aassembly Bmotor battery	nA NC NC NC NO NO Prefix  tera giga mega kilo hecto deka deci	Symbols  T G M or Meg K or k h da d	normally closed neon normally open DECIMAL M Multiplier  1012 109 106 103 102 10 10-1  DESIGN filter heater integrated circuit	Se	Symbols  c m μ n p f a	seleniumsection(s)silicon  Multiplier  10-2 10-3 10-6 10-9 10-12 10-15 10-18transistor-dioderesistor	** no standard type number assigned selected or special type  R Dupont de Nemours  STD-8-2734  TS terminal strip U microcircuit V vacuum tube, neon bulb,photocell, etc
A assembly B motor BT battery C c capacitor	nA NC NC NC NC NO	Symbols  T G M or Meg K or k h da d	normally closed	Se	Symbols  C  m  µ  n  p  f  a		**
A assembly B motor BT battery C capacitor CR diode	nA NC	Symbols  T G M or Meg K or k h da d	normally closed neon normally open DECIMAL M Multiplier  1012 109 106 103 102 10 10-1 DESIGN filter heater integrated circuit jack relay	Se	Symbols  c m  µ n p f a		**
Aassembly B	nA NC NC NC NC NC NC NO NO NO NO NO NC	Symbols  T G M or Meg K or k h da d	normally closed	Se	Symbols  c m μ n p f a	seleniumsection(s)silicon  Multiplier  10-2 10-3 10-6 10-9 10-12 10-15 10-18transistor-dioderesistor- thermistorswitch	**
A	nA NC	Symbols  T G M or Meg K or k h da d	normally closed neon .	Se sect Si Sect Sect Si Sect Sect Sect Sect Sect Sect Sect Sect	Symbols  c m  µ n p f a	seleniumsection(s)silicon  Multiplier  10-2 10-3 10-6 10-9 10-12 10-15 10-18 transistor-dioderesistor thermistorswitchtransformer terminal board	**
Aassembly B	nA NC	Symbols  T G M or Meg K or k h da d	normally closed	Se sect Si Sect Sect Si Sect Sect Sect Sect Sect Sect Sect Sect	Symbols  c m  µ n p f a	seleniumsection(s)silicon  Multiplier  10-2 10-3 10-6 10-9 10-12 10-15 10-18 transistor-dioderesistorswitchtransformer terminal boardterminal boardterminalterminal boardterminalte	TS

Table 6-1. Replaceable Parts

Reference			ble 6-1. Replaceable Parts	Mfr	Mfr Part Numbe
Designation	HP Part Number	Oty	Description	Code	Wife Part Numbe
Al	03580-66501	1	BOARD ASSY:MOTHER	28480	03580-66501
A1CR1	1901-0040	110	DIODE: SILICON 50 MA 30 WV	07263	FDG1088
AIRI AIR2 AIR3 AIR24	0757-0280 0757-0280 0698-3228 0698-4489	24 22 3	R:FXD MET FLM 1K OHM 1% 1/8W R:FXD MET FLM 1K OHM 1% 1/8W R:FXD MET FLM 49.9K OHM 1% 1/8W R:FXD FLM 28K OHM 1% 1/8W	28480 28480 28480 28480	0757-0280 0757-0280 0698-3228 0698-4489
A2	03581-66502	1	BOARD ASSY, OSCILLATOR	28480	03581-66502
A 2C1 A 2C2 A 2C3	0140-0199 01600162 01801714	3 2 1	NOTE REPLACEMENT CIRCUIT BOARDS DO NOT CONTAIN A2Y1 AND A2R65. SEE PARAGRAPH 7.25 CAPACITOR-FXD 240PF+-5% 300WVDC CAPACITOR-FXD, 022UF+10% 200WVDC CAPACITOR-FXD; 330UF+-10% 6VDC TA-SOLID	72136 56289 56289	DM15F241J0300WV1CR 292P22392 150D337X9006S2
A 2C 4 A 2C 5 A 2C 6 A 2C 7 A 2C 8	0121-0426 0150-0084 0150-0084 0140-0149 0160-0154	6 17 1 3	CAPACITOR, VAR, TRMR, MICA, 50/380PF CAPACITOR-FXD .1UF+80-20% 100WVDC CAPACITOR-FXD .1UF+80-20% 100WVDC CAPACITOR-FXD .470PF4-5% 300WVDC CAPACITOR-FXD .0022UF+-10% 200WVDC	72136 28480 28480 72136 56289	T52517-7 0150-0084 0150-0084 DM15F471J0300WV1CR 292P22292
A2C9* A2C11 A2C12 A2C13 A2C14	0150-0029 0160-2150 0150-0050 0150-0093 0140-0156	17 1 2 40 2	CAPACITOR-FXD 1PF 500WVDC CAPACITOR-FXD 33PF+-5% 300WVDC CAPACITOR-FXD ***000 00 00 00 00 00 00 00 00 00 00 00	28480 28480 28480 28480 72136	0150-0029 0160-2150 0150-0050 0150-0093 0M15F151J0300WV1CR
A2C15 A2C16 A2C17 A2C18 A2C19	0140-0199 0140-0176 0160-2605 0140-0176 0180-0106	4 20 7	CAPACITOR-FXD 240PF+-5% 300WVDC CAPACITOR-FXD 100PF+-2% 300WVDC CAPACITOR-FXD .02UF+80-20% 25WVDC CAPACITOR-FXD 100PF+-2% 300WVDC CAPACITOR-FXD; 60UF+-20% 60VDC TA-SDLID	72136 72136 28480 72136 56289	DM15F241J0300WV1CR DM15F101G0300WV1CR 0160-2605 DM15F101G0300WV1CR 150D606X0006B2
A 2C 20 A 2C 21 A 2C 22 A 2C 23 A 2C 24	0160-0162 C16C-0160 0180-0228 0140-0196 0160-2605	2 13	CAPACITOR-FXD .022UF+-10% 200WVDC CAPACITOR-FXD .0082UF+-10% 200WVDC CAPACITOR-FXD; 22UF+-10% 15VDC TA-SOLID CAPACITOR-FXD 150PF+-5% 300WVDC CAPACITOR-FXD .02UF+80-20% 25WVDC	56289 56289 56289 72136 28480	292P22392 292P82292 1500226X9015B2 DM15F151J0300WV1CR 0160-2605
A 2C 25 A 2C 26 A2C27 A2C28 A2C29	0150-0084 0150-0084 0160-2939 0150-0116 0180-1701	1 1 2	CAPACITOR-FXD .1UF+80-20% 100WVDC CAPACITOR-FXD .1UF+80-20% 100WVDC CAPACITOR-FXD 420PF+-2% 550WVDC CAPACITOR-FXD 47PF+-10% 550WVDC CAPACITOR-FXD 6.8UF+-20% 6VDC TA-SOLID	28480 28480 28480 28480 56289	0150-0084 0150-0084 0160-2939 0150-0116 150D685X0006A2
A 2C31 A 2C 32 A 2C33 A 2C34 A 2C35	0160-2605 0150-0084 0140-0200 0150-0093 0160-0136	6	CAPACITOR-FXD .02UF+80-20% 25WVDC CAPACITOR-FXD .1UF+80-20% 100WVDC CAPACITOR-FXD .30UF+5% 300WVDC CAPACITOR-FXD .01UF+80-20% 100WVDC CAPACITOR-FXD .0025UF+1% 300WVDC	28480 28480 72136 28480 28480	0160-2605 0150-0084 DM15F591J0300WV1CR 0150-0093 0160-0136
A 2C 36 A 2C 37 A 2C 38 A 2C 39 A 2C 41	0150-0093 0180-0210 0150-0093 0180-0061 0160-2585	10 11 1	CAPACITOR-FXD .01UF+80-20% 100MVDC CAPACITOR-FXD; 3.3UF+-20% 15VDC TA CAPACITOR-FXD; 01UF+80-20% 100MVDC CAPACITOR-FXD; 100UF+75-10% 16VDC AL CAPACITOR-FXD; 000UF+-1% 100MVDC	28480 56289 28480 56289 28480	0150-0093 150D335X0015A2 0150-0093 30D107G016DC2 0160-2585
A 2C 42 A 2C 43 A 2C 44 A 2C 45 A 2C 46	0160-2206 0140-0233 0160-2587 0160-0841 0180-0106	2 3 1 1	CAPACITOR-FXD 160PF+-5% 300WVDC CAPACITOR-FXD 480PF+-1% 300WVDC CAPACITOR-FXD -004UF+-1% 100WVDC CAPACITOR-FXD -00174UF+-1% 300WVDC CAPACITOR-FXD; 60UF+-20% 6VDC TA-SOLID	28480 72136 23480 28480 56289	0160-2206 DM15F481F0300WV1C 0160-2587 0160-0841 150D606X000682
A2C47 A2C48 A2C49 A2C51 A2C52	0180-0210 0140-0176 0160-2960 0180-0210 0140-0199	18	CAPACITOR-FXD; 3.3UF+-20% 15VDC TA CAPACITOR-FXD 100PF+-2% 300WVDC CAPACITOR-FXD .05UF+-20% 100WVDC CAPACITOR-FXD; 3.3UF+-20% 15VDC TA CAPACITOR-FXD 240PF+-5% 300WVDC	56289 72136 28480 56289 72136	150D335X0015A2 DM15F101G0300WV1CF 0160-2960 150D335X0015A2 DM15F241J0300WV1CR
A 2C 53 A 2C 54 A 2C 55 A 2C 56 A 2C 57	0180-0228 0150-0022 0140-0176 0180-0063 0180-0228	1	CAPACITOR-FXD; 22UF+-10% 15VDC TA-SOLID CAPACITOR-FXD 3.3PF+-10% 500WVDC CAPACITOR-FXD; 000PF+-2% 300WVDC CAPACITOR-FXD; 500UF75-10% 3VDC AL CAPACITOR-FXD; 22UF+-10% 15VOC TA-SOLID	56289 95121 72136 56289 56289	150D226X901582 TYPE QC DM15F101G0300wV1CR 30D507G003DF2 150D226X901582
A 2C 58 A 2C 59 A 2C 61 A 2C 62 A 2C 63	0160-0174 0180-0228 0180-0228 0180-0106 0180-0106	5	CAPACITOR-FXD .47UF+80-20% 25WVDC CAPACITOR-FXD; 22UF+-10% 15VDC TA-SOLID CAPACITOR-FXD; 22UF+-10% 15VDC TA-SOLID CAPACITOR-FXD; 60UF+-20% 6VDC TA-SOLID CAPACITOR-FXD; 60UF+-20% 6VDC TA-SOLID	28480 56289 56289 56289 56289	0160-0174 150D226X9015B2 150D226X9015B2 150D606X0006B2 150D606X0006B2
A2C64 A2C65 A2C66 A2C67	0160-0174 0180-0106 0150-0084 0180-0228		CAPACITOR-FXD .47UF+80-20% 25WVDC CAPACITOR-FXD; 60UF+-20% 6VDC TA-SOLID CAPACITOR-FXD .1UF+80-20% 100WVDC CAPACITOR-FXD; 22UF+-10% 15VDC TA-SOLID	28480 56289 28480 56289	0160-0174 150D606X0006B2 0150-0084 150D226X9015B2

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
Designation A2C 68	0180-0210		CAPACITOR-FXD; 3-3UF+-20% 15VDC TA	56289	150D335X0015A2
A2C69 A2CR1 A2CR2 A2CR3 A2CR4	0180-0228 1901-0040 0122-0059 0122-0059 1901-0040	108	CAPACITOR-FXD; 22UF+-10% 15VDC TA-SOLID DIODE; SWITCHING; ; 30V MAX VRM 50MA DIODE:VOLTAGE VARIABLE CAPACITANCE DIODE:VOLTAGE VARIABLE CAPACITANCE DIODE; SWITCHING; ; 30V MAX VRM 50MA	56289 28480 28480 28480 28480	1500226X901582 1901-0040 0122-0059 0122-0059 1901-0040
A2CR5 A2CR6 A2CR7 A2CR8 A2CR9	1901-0040 1901-0040 1901-0040 1901-0040 1902-0041	5	DIODE; SWITCHING; ; 30V MAX VRM 50MA DIODE; ZENER; 5-11V VZ; -4W MAX PD	28480 28480 28480 28480 04713	1901-0040 1901-0040 1901-0040 1901-0040 SZ 10939-98
A2CR10 A2CR11 A2CR12 A2CR13 A2CR14	1901-0040 1901-0040 1902-0041 1902-0041		NOT ASSIGNED DIODE; SWITCHING; ; 30V MAX VRM 50MA DIODE; SWITCHING; ; 30V MAX VRM 50MA DIODE; ZENER; 5.11V VZ; .4W MAX PD DIODE; ZENER; 5.11V VZ; .4W MAX PD	28480 28480 04713 04713	1901-0040 1901-0040 SZ 10939-98 SZ 10939-98
A2CR15 A2CR16 A2L1 A2L2 A2L3	1901-0040 1901-0040 9100-3288 9140-0210 9100-0543	1 8 1	DIODE; SWITCHING; ; 30V MAX VRM 50MA DIODE; SWITCHING; ; 30V MAX VRM 50MA INDUCTOR:POT CORE 330 UH COLL; FXD; MOLDED RF CHOKE; 100UH 5% COLL:VAR 1000 UH 10%	28480 28480 28480 24226 28480	1901-0040 1901-0040 9100-3288 15/103 9100-0543
A 2 L 4 A 2 L 5 A 2 L 6 A 2 L 7 A 2 L 8	9140-0137 \$100-3278 9100-3277 9140-0210 9140-0210	8 1 4	COIL; FXD; MOLDED RF CHOKE; 1MH 5% INDUCTOR:POT CORE INDUCTOR:POT CORE COIL; FXD; MOLDED RF CHOKE; 100UH 5% COIL; FXD; MOLDED RF CHOKE; 100UH 5%	24226 28480 28480 24226 24226	19/104 9100-3278 9100-3277 15/103 15/103
A 2 L 9 A 2 L 1 1 A 2 L 1 2 A 2 L 1 3 A 2 L 1 4	9140-0210 \$140-0210 9140-0210 9140-0210 9140-0210		COIL; FXD; MOLDED RF CHOKE; 100UH 5%	24226 24226 24226 24226 24226	15/103 15/103 15/103 15/103 15/103
A 2MP1 A 2MP2 A 2MP3 A 2Q1 A 2Q2	4040-0750 03580-00609 03580-00610 1855-0081 1853-0010	2 1 1 5 30	EXTRACTOR:PC BOARD, RED SHIELD, OSCILLATOR SHIELD, CRYSTAL TRANSISTOR; J-FET N-CHAN, D-MODE SI TRANSISTOR PNP SI CHIP PD=360MW	28480 28480 28480 01295 28480	4040-0750 03580-00609 03580-00610 2N5245 1853-0010
A2Q3 A2Q4 A2Q5 A2Q6 A2Q7	1853-0010 1854-0071 1654-0071 1855-0234 1855-0081	63 1 1	TRANSISTOR PNP SI CHIP PD=360MW TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR; JFET; DUAL; N-CHAN D-MODE SI TRANSISTOR; J-FET N-CHAN, D-MODE SI	28480 28480 28480 28480 01295	1853-0010 1854-0071 1854-0071 1855-0234 2N5245
A2Q8 A2Q9 A2Q11 A2Q12 A2Q13	1854-0354 1853-0010 1853-0010 1854-0071 1854-0345	7	TRANSISTOR NPN SI PD=360MW FT=350MHZ TRANSISTOR PNP SI CHIP PD=360MW TRANSISTOR PNP SI CHIP PD=360MW TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN 2N5179 SI PD=200MW	28480 28480 28480 28480 04713	1854-0354 1853-0010 1853-0010 1854-0071 2N5179
A2Q14 A2Q15 A2Q16 A2Q17 A2Q18	1854-0351 1853-0010 1853-0010 1854-0071 1854-0071	1	TRANSISTOR NPN SI PD=360MW FT=300MHZ TRANSISTOR PNP SI CHIP PD=360MW TRANSISTOR PNP SI CHIP PD=360MW TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ	28480 28480 28480 28480 28480	1854-0351 1853-0010 1853-0010 1854-0071 1854-0071
A2Q19 A2Q21 A2Q22 A2Q23 A2Q24	1854-0071 1854-0071 1853-0010 1854-0071 1853-0010		TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR PNP SI CHIP PD=360MW TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR PNP SI CHIP PD=360MW	28480 28480 28480 28480 28480	1854-0071 1854-0071 1853-0010 1854-0071 1853-0010
A2Q25 A2R1 A2R2 A2R3 A2R4	1854-0071 0757-0457 0757-0477 0698-5542 0757-0488	1 1 2 1	TRANSISTOR NPN SI PD=300MW FT=200MHZ RESISTOR-FXD 47-5K 1% .125W F TUBULAR RESISTOR-FXD 332K 1% .125W F TUBULAR RESISTOR-FXD 20K 1% .125W F TUBULAR RESISTOR-FXD 909K 1% .125W F TUBULAR	28480 24546 30983 19701 19701	1854-0071 C4-1/8-T0-4752-F MF4C1/8-T0-3323-F MF4C1/8-T9-2002-F MFF-1/8+T-1
A2R 5 A2R 6 A2R 7 A2R 8 A2R 9	2100-3352 0698-4536 0757-0430 0757-0440 0698-3274	4 1 7 2 4	RESISTOR, VAR, TRMR, 1KOHM 10% C RESISTOR-FXD 340K 1% •125M F TUBULAR RESISTOR-FXD 2•21K 1% •125M F TUBULAR RESISTOR-FXD 7•5K 1% •125M F TUBULAR RESISTOR-FXD 10K 1% •125M F TUBULAR	73138 19701 24546 24546 19701	72XR102 MF4C1/8-T0-3403-F C4-1/8-T0-2211-F C4-1/8-T0-7501-F MF4C1/8-T9-1002-F
A 2R 10 A 2R 11 A 2R 12 A 2R 13 A 2R 14	0757-0430 0757-0438 0757-0438 0757-0438 0757-0416	25 5	RESISTOR-FXD 2.21k 1% .125W F TUBULAR RESISTOR-FXD 5.11k 1% .125W F TUBULAR	24546 24546 24546 24546 24546	C4-1/8-T0-2211-F C4-1/8-T0-5111-F C4-1/8-T0-5111-F C4-1/8-T0-5111-F C4-1/8-T0-511R-F
A 2R 15 A 2R 16 A 2R 17 A 2R 18 A 2R 19	0698-4481 0684-1051 0757-0427 0698-3497 0698-4443	5 2 6 6 2	RESISTOR-FXD 16.5K 1% .125W F TUBULAR RESISTOR-FXD 1M 10% .25W CC TUBULAR RESISTOR-FXD 1.5K 1% .125W F TUBULAR RESISTOR-FXD 6.04K 1% .125W F TUBULAR RESISTOR-FXD 4.53K 1% .125W F TUBULAR	24546 01121 24546 16299 16299	C4-1/8-T0-1652-F CB1051 C4-1/8-T0-1501-F C4-1/8-T0-604R-F C4-1/8-T0-4531-F
A 2R 21 A 2R 22	0757-0430 0757-0280	17	RESISTOR-FXD 2.21K 1% .125W F TUBULAR RESISTOR-FXD 1K 1% .125W F TUBULAR	24546 24546	C4-1/8-T0-2211-F C4-1/8-T0-1001-F

Table 6-1. Replaceable Parts(Cont'd)

	Table 6-1. Replaceable Parts(Cont'd)					
Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number	
A 2R 23 A 2R 24 A 2R 25	0757-0442 0757-0427 0757-0415	25 2	RESISTOR-FXD 10K 1% -125W F TUBULAR RESISTOR-FXD 1.5K 1% -125W F TUBULAR RESISTOR-FXD 475 OHM 1% -125W F TUBULAR	24546 24546 24546	C4-1/3-T0-1002-F C4-1/8-T0-1501-F C4-1/8-T0-475R-F	
A 2R 26 A 2R 27 A 2R 28 A 2R 29 A 2R 31	0757-0407 0684-1041 0684-1031 0684-4731 0757-0449	13 28 19 2	RESISTOR-FXD 200 DHM 1% -125W F TUBULAR RESISTOR-FXD 100K 10% -25W CC TUBULAR RESISTOR-FXD 10K 10% -25W CC TUBULAR RESISTOR-FXD 47K 10% -25W CC TUBULAR RESISTOR-FXD 20K 1% -125W F TUBULAR	24546 01121 01121 01121 24546	C4-1/8-T0-201-F C81041 C81031 C84731 C4-1/8-T0-2002-F	
A 2R 32 A 2R 33 A 2R 34 A 2R 35 A 2R 36	0757-0449 0698-3274 0698-3450 698-3274 0698-3274	1	RESISTOR-FXD 20K 1% -125W F TUBULAR RESISTOR-FXD 10K 1% -125W F TUBULAR RESISTOR-FXD 42.2K 1% -125W F TUBULAR RESISTOR-FXD 10K 1% -125W F TUBULAR RESISTOR-FXD 10K 1% -125W F TUBULAR	24546 19701 16299 19701 19701	C4-1/8-T0-2002-F MF4C1/8-T9-1002-F C4-1/8-T0-4222-F MF4C1/8-T9-1002-F MF4C1/8-T9-1002-F	
A 2R 37 A 2R 38 A 2R 39 A 2R 41 A 2R 42	0698-5542 0698-6338 0684-4721 0684-4721	1 4	RESISTOR-FXD 20K 1% -125W F TUBULAR RESISTOR-FXD 5K 1% -125W F TUBULAR RESISTOR-FXD 4-7K 10% -25W CC TUBULAR RESISTOR-FXD 4-7K 10% -25W CC TUBULAR RESISTOR-FXD 4-7K 10% -25W CC TUBULAR	19701 19701 01121 01121 01121	MF4C1/8-T9-2002-F MF4C1/8-T9-5001-F C84721 CB4721 CB4721	
A 2R 43 A 2R 44 A 2R 45 A 2R 46 A 2R 47	0757-0438 0757-0438 0698-0064 0684-1031 0684-1031	1	RESISTOR-FXD 5.11K 1% .125W F TUBULAR RESISTOR-FXD 5.11K 1% .125W F TUBULAR RESISTOR-FXD 9.31K 1% .125W F TUBULAR RESISTOR-FXD 10K 10% .25W CC TUBULAR RESISTOR-FXD 10K 10% .25W CC TUBULAR	24546 24546 91637 01121 01121	C4-1/8-T0-5111-F C4-1/8-T0-5111-F CMF-1/8-T1-9311-F C81031 C81031	
A 2R 48 A 2R 49 A 2R 51 A 2R 52 A 2R 53	C757-0446 0757-0446 C698-4447 0757-0427 0698-4447	8 2	RESISTOR-FXD 15K 1% -125W F TUBULAR RESISTOR-FXD 15K 1% -125W F TUBULAR RESISTOR-FXD 280 0HM 1% -125W F TUBULAR RESISTOR-FXD 1.5K 1% -125W F TUBULAR RESISTOR-FXD 280 0HM 1% -125W F TUBULAR	24546 24546 24546 24546 24546	C4-1/8-T0-1502-F C4-1/8-T0-1502-F C4-1/8-T0-280R-F C4-1/8-T0-1501-F C4-1/8-T0-280R-F	
A 2R 54 A 2R 55 A 2R 56 A 2R 57 A 2R 58	0757-0442 0698-4435 0757-0438 0757-0381 0683-0825	3 1 1	RESISTOR-FXD 10K 1% -125W F TUBULAR RESISTOR-FXD 2-49K 1% -125W F TUBULAR RESISTOR-FXD 5-11K 1% -125W F TUBULAR RESISTOR-FXD 15 0HM 1% -125W F TUBULAR RESISTOR-FXD 8-2 0HM 5% -25W CC TUBULAR	24546 16299 24546 30983 01121	C4-1/8-T0-1002-F C4-1/8-T0-2491-F C4-1/8-T0-5111-F MF4C1/8-T0-15R0-F C882G5	
A 2R 59 A 2R 61 A 2R 62 A 2R 63 A 2R 64	0757-0438 0757-0438 0757-0416 0757-0280 0698-3449	2	RESISTOR-FXD 5.11K 1% .125W F TUBULAR RESISTOR-FXD 5.11K 1% .125W F TUBULAR RESISTOR-FXD 511 OHM 1% .125W F TUBULAR RESISTOR-FXD 1K 1% .125W F TUBULAR RESISTOR-FXD 28.7K 1% .125W F TUBULAR	24546 24546 24546 24546 16299	C4-1/8-T0-5111-F C4-1/8-T0-5111-F C4-1/8-T0-511R-F C4-1/8-T0-1001-F C4-1/8-T0-12872-F	
A2R65* A2R66 A2R67 A2R68	0698-4387 C698-4505 0757-0442 0757-0446	6	RESISTOR-FXD 60.4 OHM 1% .125W F FACTORY SELECTED PART RESISTOR-FXD 71.5K 1% .125W F TUBULAR RESISTOR-FXD 10K 1% .125W F TUBULAR RESISTOR-FXD 15K 1% .125W F TUBULAR	16299 24546 24546 24546	C4-1/8-T0-60R4-F C4-1/8-T0-7152-F C4-1/8-T0-1002-F C4-1/8-T0-1502-F	
A 2R 69 A 2R 71 A 2R 72 A 2R 73 A 2R 74	0684-2231 0757-0416 0684-4741 0698-3558 0698-3558	4 3 5	RESISTOR-FXD 22K 10% -25W CC TUBULAR RESISTOR-FXD 511 OHM 1% -125W F TUBULAR RESISTOR-FXD 470K 10% -25W CC TUBULAR RESISTOR-FXD 4-02K 1% -125W F TUBULAR RESISTOR-FXD 4-02K 1% -125W F TUBULAR	01121 24546 01121 16299 16299	C82231 C4-1/8-T0-511R-F C84741 C4-1/8-T0-4021-F C4-1/8-T0-4021-F	
A 2R 75 A 2R 76 A 2R 77 A 2R 78 A 2R 79	2100-3054 0698-4486 0757-0280 0698-4486 0757-0416	1 13	RESISTOR, VAR, TRMR 50K OHM 10% C RESISTOR-FXD 24.9K 1% .125W F TUBULAR RESISTOR-FXD 1K 1% .125W F TUBULAR RESISTOR-FXD 24.9K 1% .125W F TUBULAR RESISTOR-FXD 511 OHM 1% .125W F TUBULAR	32997 24546 24546 24546 24546	3006P-1-503 C4-1/8-T0-2492-F C4-1/8-T0-1001-F C4-1/8-T0-2492-F C4-1/8-T0-511R-F	
A 2R 81 A 2R 82 A 2R 83 A 2R 64 A 2R 85	0757-0416 0757-0280 0757-0280 0757-0421 0757-0446	1	RESISTOR-FXD 511 OHM 1% -125W F TUBULAR RESISTOR-FXD 1K 1% -125W F TUBULAR RESISTOR-FXD 1K 1% -125W F TUBULAR RESISTOR-FXD 825 OHM 1% -125W F TUBULAR RESISTOR-FXD 15K 1% -125W F TUBULAR	24546 24546 24546 24546 24546	C4-1/8-T0-511R-F C4-1/8-T0-1001-F C4-1/8-T0-1001-F C4-1/3-T0-825R-F C4-1/8-T0-1502-F	
A 2R 86 A 2R 87 A 2R 88 A 2R 89 A 2R 91	C698-3497 0698-4425 0684-2231 0684-1041 0684-1001	1	RESISTOR-FXD 6.04K 1% .125W F TUBULAR RESISTOR-FXD 1.54K 1% .125W F TUBULAR RESISTOR-FXD 22K 10% .25W CC TUBULAR RESISTOR-FXD 100K 10% .25W CC TUBULAR RESISTOR-FXD 100 HM 10% .25W CC TUBULAR	16299 16299 01121 01121 01121	C4-1/8-T0-604R-F C4-1/8-T0-1541-F C82231 C81041 CB1001	
A 2R 92 A 2R 93 A 2R 94 A 2R 95 A 2R 96	0684-1031 0698-4484 0757-0430 0684-3921 0698-4461	2 2 2	RESISTOR-FXD 10K 10% -25W CC TUBULAR RESISTOR-FXD 19-1K 1% -125W F TUBULAR RESISTOR-FXD 2-21K 1% -125W F TUBULAR RESISTOR-FXD 3-9K 10% -25W CC TUBULAR RESISTOR-FXD 698 0HM 1% -125W F TUBULAR	01121 24546 24546 01121 24546	C81031 C4-1/8-T0-1912-F C4-1/8-T0-2211-F C83921 C4-1/8-T0-698R-F	
A2R57 A2R98 A2R59 A2R100 A2R101	0698-4461 0757-0458 0684-1011 2100-3207 0757-0427	4 4 1	RESISTOR-FXD 698 OHM 1% .125w F TUBULAR RESISTOR-FXD 51.k 1% .125w F TUBULAR RESISTOR-FXD 100 OHM 10% .25w CC RESISTOR, VAR, TRMR, 5KOHM 10% C RESISTOR-FXD 1.5K 1% .125w F TUBULAR	24546 24546 01121 28480 24546	C4-1/8-T0-698R-F C4-1/3-T0-5112-F C81011 2100-3207 C4-1/8-T0-1501-F	
A 2R 102 A 2R 103 A 2R 104 A 2R 105 A 2R 106	0757-0446 0757-0280 0698-3488 0757-0448 0757-0401	5 3 9	RESISTOR-FXD 15K 1% .125W F TUBULAR RESISTOR-FXD 1K 1% .125W F TUBULAR RESISTOR-FXD 442 DHM 1% .125W F TUBULAR RESISTOR-FXD 18.2K 1% .125W F TUBULAR RESISTOR-FXD 100 DHM 1% .125W F TUBULAR	24546 24546 16299 24546 24546	C4-1/8-T0-1502-F C4-1/8-T0-1001-F C4-1/8-T0-422R-F C4-1/8-T0-1822-F C4-1/8-T0-101-F	

Section VI

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A 2R 107 A 2R 108 A 2R 109 A 2R 111 A 2R 112	0757-0401 0698-3446 0684-1031 0684-1011	1	RESISTOR—FXD 100 OHM 1% .125W F TUBULAR RESISTOR—FXD 383 OHM 1% .125W F TUBULAR RESISTOR—FXD 10K 10% .25W CC TUBULAR RESISTOR—FXD 10O OHM 10% .25W CC RESISTOR—FXD 47 OHM 10% .25W CC TUBULAR	24546 16299 01121 01121 01121	C4-1/8-TO-101-F C4-1/8-TO-383R-F C81031 C81011 C84701
A 2R 113 A 2R 114 A 2R 115	2100-3357 0684-5631 0684-2211	1 1 1	RESISTOR, VAR, TRMR, 500KDHM 10% C RESISTOR-FXD 56K 10% -25W CC TUBULAR RESISTOR-FXD 220 DHM 10% -25W CC	73138 01121 01121	72XR504 CB5631 CB2211
A2U1	1826-0043	15	IC;LIN;GPERATIONAL AMPLIFIER	27014	LM307H
A2U2 A2U3 A2U4 A2U5 A2U5 A2U7 A2U8 A2U9 A2U11 A2U12 A2U13 A2V13 A2Y1	1820-0600 1826-0043 1826-0043 1820-0600 1820-0594 1820-0427 1820-0600 1820-0587 1820-0587 1820-0099 1820-00475	1 3 3 1 1	INTEGRATED CIRCUIT, DGTL, TTL DECADE IC;LIN;OPERATIONAL AMPLIFIER IC;LIN;OPERATIONAL AMPLIFIER INTEGRATED CIRCUIT, DGTL, TTL DECADE INTEGRATED CIRCUIT, DGTL, TTL LP J-K IC;LIN;MISCELLANEOUS (LINEAR) INTEGRATED CIRCUIT, DGTL, TTL DECADE IC;LIN;OPERATIONAL AMPLIFIER IC;DGTL;GATE IC;DGTL;COUNTER INTEGRATED CIRCUIT, DGTL, VOLTAGE CRYSTAL:NOT FIELD REPLACEABLE (SEE PARA. 7-25)	27014 27014 27014 27014 27014 04713 27014 07263 27014 01295 27014 28480	DM74L90N LM307H LM307H DM74L90N DM74L72N MC1496G DM74L90N 709HC DM74L10N SM7493N LM306H
<b>A3</b>	03580-66503	ī	BOARD ASSY:SWEEP	28480	03580-66503
A3C1 A3C2 A3C3	0180-1743 0150-0093 0180-0197	6 28	C:FXD ELECT 0.1 UF 10% 35VDCW C:FXD CER 0.01 UF +80-20% 100VDCW C:FXD ELECT 2.2 UF 10% 20VDCW	562 89 729 82 562 89	150D104X9035A2-DYS 801-K800011 150D225X9020A2-DYS
A3C4 A3C5 A3C6 A3C7 A3C8	0150-0050 0150-0050 0150-0050 0180-1701 0150-0050		C:FXD CER 1000 PF +80-20% 1000VDCW C:FXD CER 1000 PF +80-20% 1000VDCW C:FXD CER 1000 PF +80-20% 1000VDCW C:FXD ELECT 6.8 UF 20% 6VDCW C:FXD CER 1000 PF +80-20% 1000VDCW	56289 56289 56289 28480 56289	C067B102E102ZS26-CDH C067B102E102ZS26-CDH C067B102E102ZS26-CDH 0180-1701 C067B102E102ZS26-CDH
A3C9 A3C10 A3C11 A3C12 A3C13	0150-0093 0160-2150 0150-0050 0180-0197 0160-2150		C:FXD CER 0.01 UF +80-20% 100VDCW C:FXD MICA 33 PF 5% C:FXD CER 1000 PF +80-20% 1000VDCW C:FXD ELECT 2.2 UF 10% 20VDCW C:FXD MICA 33 PF 5%	72982 28480 56289 56289 28480	801-K800011 0160-2150 C0678102E102Z\$26-CDH 15002Z\$X9020A2-DY\$ 0160-2150
A3C14 A3C15 A3C16 A3C17 A3C18	0170-0042 0180-1743 0160-2611 0160-0168 0150-0050	5 1 2	C:FXD MY 0.33UF 5% 100VDCM C:FXD ELECT 0.1 UF 10% 35VDCW C:FXD MY 1 UF 10% 50VDCW C:FXD MY 0.1 UF 10% 200VDCW C:FXD CER 1000 PF +80-20% 1000VDCW	99515 56289 84411 56289 56289	E1-334D TYPE E120 150D104X9035A2-DYS HEW 101 192P10492-PTS C0678102E102ZS26-CDH
A3C19 A3C20 A3C21 A3C22 A3C23	0180-0197 0150-0050 0160-0170 0160-0170 0160-0170	3	C:FXD ELECT 2.2 UF 10% 20VDCW C:FXD CER 1000 PF +80-20% 1000VDCW C:FXD CER 0.22 UF +80-20% 25VDCW C:FXD CER 0.22 UF +80-20% 25VDCW C:FXD CER 0.22 UF +80-20% 25VDCW	562 89 562 89 562 89 562 89 562 89	150D225X9020A2-DYS C067B102E102ZS26-CDH 5C9BS-CML 5C9BS-CML 5C9BS-CML
A3C24 A3CR1 A3CR2 A3CR3 A3CR4	0140-0149 1901-0040 1902-3128 1910-0016	2 4	C:FXD MICA 470 PF 5% DIODE:SILICON 50 MA 30 WV DIODE:T.32V 5% DIODE:GE 60 MIV DIODE:GE 60 MIV	72136 07263 28480 28480 28480	0M15F471J3S F0G1088 1902-3128 1910-0016 1910-0016
A3CR5 A3CR6 A3CR7 A3CR8 A3CR9	1910-0016 1901-0040 1901-0040 1901-0040 1901-0040		DIODE:GE 60 WIV DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV	28480 07263 07263 07263 07263	1910-0016 FDG1088 FDG1088 FDG1088 FDG1088
A3CR11 A3CR12 A3CR13 A3CR14 A3CR15	1901-0040 1910-0016 1901-0040 1901-0040 1901-0040		DIODE:SILICON 50 MA 30 WV DIODE:GE 60 WIV DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV	07263 28480 07263 07263 07263	FDG1088 1910-0016 FDG1088 FDG1088 FDG1088
A3CR16 A3CR17 A3CR18 A3CR19 A3CR21	1901-0586 1902-3182 1901-0040 1901-0040 1902-3128	1 2	DIODE:SI 30 WV 10 PA LEAKAGE DIODE BREAKDOWN:SILICON 12-1V 5% DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV DIODE:7-32V 5%	28480 28480 07263 07263 28480	1901-0586 1902-3182 F0G1088 FDG1088 1902-3128
A3CR22 A3CR23 A3CR24 A3Q1 A3Q2	1901-0040 1901-0040 1902-3085 1855-0237 1854-0071	1 2	DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV DIODE BREAKOOWN:4-75V 5% 400MW TSTR:SI FET TSTR:SI FET	07263 07263 28480 28480 28480	FDG1088 FDG1088 1902-3085 1855-0237 1854-0071
A303 A304 A305 A306 A307	1855-0368 1853-0010 1855-0082 1854-0071 1854-0071	4	TSTR:FET SI NPN N-CHANNEL TSTR:SI PMP(SELECTED FROM 2N3251) TSTR:SI FET P-CHANNEL TSTR:SI NPN(SELECTED FROM 2N3704) TSTR:SI NPN(SELECTED FROM 2N3704)	28480 28480 28480 28480 28480	1855-0368 1853-0010 1855-0082 1854-0071

Model 3580A

Table 6-1. Replaceable Parts(Cont'd

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A308 A309 A3011 A3012 A3013	1854-0071 1854-0071 1854-0087 1854-0071	4	TSTR:SI NPN(SELECTED FROM 2N3704) TSTR:SI NPN(SELECTED FROM 2N3704) TSTR:SI NPN TSTR:SI NPN(SELECTED FROM 2N3704) TSTR:SI NPN(SELECTED FROM 2N3704)	28480 28480 80131 28480 28480	1854-0071 1854-0071 2N3417 1854-0071
A3014 A3015 A3016 A3017 A3018	1855-0308 1855-0386 1853-0010 1854-0071	2	TSTR:SI NPN DUAL TSTR:FET N-CHANNEL TSTR:SI PNP(SELECTED FROM 2N3251) TSTR:SI NPN(SELECTED FROM 2N3704) TSTR:SI NPN(SELECTED FROM 2N3704)	28480 80131 28480 28480 28480	1855-0308 2N4392 1853-0010 1854-0071 1854-0071
A3019 A3021 A3022 A3023 A3024	1854-0071 1853-0010 1854-0087 1853-0010 1854-0071		TSTR:SI NPN(SELECTED FROM 2N3704) TSTR:SI PNP(SELECTED FROM 2N3251) TSTR:SI NPN TSTR:SI PNP(SELECTED FROM 2N3251) TSTR:SI NPN(SELECTED FROM 2N3704)	28480 28480 80131 28480 28480	1854-0071 1853-0010 2N3417 1853-0010 1854-0071
A3025 A3026 A3027 A3028 A3029	1854-0354 1853-0010 1853-0010 1853-0010 1853-0010		TSTR:SI NPN TSTR:SI PNP(SELECTED FROM 2N3251) TSTR:SI PNP(SELECTED FROM 2N3251) TSTR:SI PNP(SELECTED FROM 2N3251) TSTR:SI PNP(SELECTED FROM 2N3251)	28480 28480 28480 28480 28480	1854-0354 1853-0010 1853-0010 1853-0010 1853-0010
A3031 A3032 A3033 A3034 A3035	1853-0010 1855-0368 1855-0237 1854-0071 1855-0368		TSTR: SI PNP (SELECTED FROM 2N3251) TSTR: SI NPN N-CHANNEL TSTR: FET SI TSTR:SI NPN(SELECTED FROM 2N3704) TSTR:FET SI NPN N-CHANNEL	28480 28480 28480 28480 28480	1853-0010 1855-0368 1855-0237 1854-0071 1855-0368
A3036 A3037 A3038 A3039 A3041	1855-0368 1853-0016 1854-0071 1853-0016 1853-0016	5	TSTR:FET SI NPN N-CHANNEL TSTR:SI PNP TSTR:SI NPN(SELECTED FROM 2N3704) TSTR:SI PNP TSTR:SI PNP	28480 80131 28480 80131 80131	1855-0368 2N3638 1854-0071 2N3638 2N3638
A3042 A3043 A3044 A3R1 A3R2	1854-0087 1854-0087 1853-0016 0698-4479 0757-0426	2 4	TSTR:SI NPN TSTR:SI NPN TSTR:SI PNP R:FXD FLM 14K OHM 1% 1/8W R:FXD FLM 1.3K OHM 1% 1/8W	80131 80131 80131 28480 28480	2N3417 2N3417 2N3618 0698-4479 0757-0426
A3R3 A3R4 A3R5 A3R6 A3R7	0698-4479 0757-0272 0684-1031 0684-5641 0684-1041	3	R:FXD FLM 14K DHM 1% 1/8W R:FXD FLM 52.3K DHM 1% 1/8W R:FXD COMP 10K DHM 10% 1/4W R:FXD COMP 560K DHM 10% 1/4W R:FXD COMP 560K DHM 10% 1/4W	28480 28480 01121 01121 01121	0698-4479 0757-0272 CB 1031 CB 5641 CB 1041
A3R8 A3R9 A3R11 A3R12 A3R13	0684-1041 0684-3331 0757-0457 0698-3228 0698-4486	12	R:FXD COMP 100K OHM 10% 1/4W R:FXD COMP 33K OHM 10% 1/4W R:FXD MET FLM 47-5K OHM 1% 1/8W R:FXD MET FLM 49-9K OHM 1% 1/8W R:FXD MET FLM 24-9K OHM 1% 1/8W	01121 01121 28480 28480 28480	CB 1041 CB 3331 0757-0457 0698-3228 0698-4486
A3R14 A3R15 A3R16 A3R17 A3R18 A3R19 A3R21 A3R22 A3R22 A3R23 A3R23	2100-3273 0757-0483 0684-1031 0684-4731 0684-6831 0684-1041 0757-0442 0698-4486 0684-1061 0757-0442	3	R:VAR TRIMMER 2K OHM 10% TYPE VI 1/2W R:FXD MET FLM 562K OHM 1% 1/8W R:FXD COMP 10K OHM 10% 1/4W R:FXD COMP 68K OHM 10% 1/4W R:FXD COMP 68K OHM 10% 1/4W R:FXD COMP 100K OHM 10% 1/4W R:FXD MET FLM 10.0K OHM 1% 1/8W R:FXD MET FLM 24.9K OHM 1% 1/8W R:FXD MET FLM 24.9K OHM 1% 1/8W R:FXD MET FLM 10.0K OHM 1% 1/8W	28480 01121 01121 01121 01121 01121 28480 01121 28480 01121 28480 28480	2100-3273 0757-0483 CB 1031 CB 4731 CB 6831 CB 1041 0757-0442 0698-4486 CB 1061 0757-0442
A3R 26 A3R 27 A3R 28 A3R 29 A3R 31	0684-1041 0684-1041 0684-1041 0698-4484 0698-4484		R:FXD COMP 100K OHM 10% 1/4W R:FXD COMP 100K OHM 10% 1/4W R:FXD COMP 100K OHM 10% 1/4W R:FXD FLM 19.1K OHM 1% 1/8W R:FXD FLM 19.1K OHM 1% 1/8W	01121 01121 01121 28480 28480	CB 1041 CB 1041 CB 1041 0698-4484 0698-4484
A 3R 32 A 3R 33 A 3R 34 A 3R 35 A 3R 36	0684-1031 0698-4489 0684-1011 0684-1041 0684-1041		R:FXD COMP 10K OHM 10% 1/4W R:FXD FLM 28K OHM 1% 1/8W R:FXD COMP 100 OHM 10% 1/4W R:FXD COMP 100K OHM 10% 1/4W R:FXD COMP 100K OHM 10% 1/4W	01121 28480 01121 01121	CB 1031 0698-4489 CB 1011 CB 1041
A3R37 A3R38 A3R39 A3R41 A3R42	0684-4731 0684-2251 0684-3331 0684-1531 0684-5621	3 15 10	R:FXD COMP 47K OHM 10% 1/4W R:FXD COMP 2.2 MEGOHM 10% 1/4W R:FXD COMP 33K OHM 10% 1/4W R:FXD COMP 15K OHM 10% 1/4W R:FXD COMP 5.6K OHM 10% 1/4W	01121 01121 01121 01121 01121	CB 4731 CB 2251 CB 3331 CB 1531 CB 5621
A3R43 A3R44 A3R45 A3R46 A3R46	0684-1041 0684-1031 0684-1041 0684-4731 0684-1031		R:FXD COMP 100K OHM 10% 1/4W R:FXD COMP 10K OHM 10% 1/4W R:FXD COMP 100K OHM 10% 1/4W R:FXD COMP 47K OHM 10% 1/4W R:FXD COMP 10K OHM 10% 1/4W	01121 01121 01121 01121 01121	CB 1041 CB 1031 CB 1041 CB 4731 CB 1051

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A3R48 A3R49 A3R51 A3R52 A3R53	0684-1041 0684-4731 0684-4731 0684-2231 0684-1041		R:FXD COMP 100K OHM 10% 1/4W R:FXD COMP 47K OHM 10% 1/4W R:FXD COMP 47K OHM 10% 1/4W R:FXD COMP 27K OHM 10% 1/4W R:FXD COMP 100K OHM 10% 1/4W	01121 01121 01121 01121 01121	CB 1041 CB 4731 CB 4731 CB 2231 CB 1041
A3R54 A3R55 A3R56 A3R56 A3R57 A3R58	2100-3273 0684-1041 0684-4731 0684-1041 0684-1041		R:VAR TRIMMER 2K OHM 10% TYPE VI 1/2W R:FXD COMP 100K OHM 10% 1/4W R:FXD COMP 47K OHM 10% 1/4W R:FXD COMP 100K OHM 10% 1/4W R:FXD COMP 100K OHM 10% 1/4W	28480 01121 01121 01121 01121	2100-3273 CB 1041 CB 4731 CB 1041 CB 1041
A3R59 A3R61 A3R62 A3R63 A3R64	0684-1041 0684-4731 0684-4741 0684-4731 0684-1041		R:FXD COMP 100K OHM 10% 1/4W R:FXD COMP 47K OHM 10% 1/4W R:FXD COMP 47OK OHM 10% 1/4W R:FXD COMP 47K OHM 10% 1/4W R:FXD COMP 10K OHM 10% 1/4W	01121 01121 01121 01121 01121	CB 1041 CB 4731 CB 4741 CB 4731 CB 1041
A3R65 A3R66 A3R67 A3R68 A3R69	0684-1041 0698-5922 0698-3572 0698-3499 0757-0449	1 3 6 36	R:FXD COMP 100K OHM 10% 1/4W R:FXD MET FLM 1.8 MEGOHM 1.0% 1/2W R:FXD FLM 60.4K OHM 1% 1/8W R:FXD FLM 40.2K OHM 1% 1/8W R:FXD FLM 40.2K OHM 1% 1/8W	01121 28480 28480 28480 28480	CB 1041 0698-5922 0698-3572 0698-3499 0757-0449
A3R 71 A3R 72 A3R 73 A3R 74 A3R 75	0757-0449 0757-0426 0757-0272 0757-0449 0684-1041		R:FXD FLM 20K OHM 1% 1/8W R:FXD FLM 1.3K OHM 1% 1/8W R:FXD FLM 52.3K OHM 1% 1/8W R:FXD FLM 20K OHM 1% 1/8W R:FXD COMP 100K OHM 10% 1/4W	28480 28480 28480 28480 01121	0757-0449 0757-0426 0757-0272 0757-0449 CB 1041
A3R 76 A3R 77 A3R 78 A3R 79 A3R 81	2100-3357 0698-0077 0698-0077 0757-0277 0757-0475	5 1 1	R:VAR CERMET 500K OHM 10% TYPE VI 1/2W R:FXD MET FLM 93.1K OHM 1% 1/8W R:FXD MET FLM 93.1K OHM 1% 1/8W R:FXD MET FLM 49.9 OHM 1% 1/8W R:FXD MET FLM 274K OHM 1% 1/8W	28480 28480 28480 28480 28480	2100-3357 0698-0077 0698-0077 0757-0277 0757-0475
A3R 82 A3R 83 A3R 84 A3R 85 A3R 86	0757-0346 0698-4497 0684-2231 0684-1041 0684-1031	1	R:FXD MET FLM 10 OHM 1% 1/8W R:FXD FLM 48.7K OHM 1% 1/8W R:FXD COMP 22K OHM 10% 1/4W R:FXD COMP 100K OHM 10% 1/4W R:FXD COMP 10K OHM 10% 1/4W	28480 28480 01121 01121 01121	0757-0346 0698-4497 CB 2231 CB 1041 CB 1031
A3R87 A3R88 * A3R89 A3R91 A3R92	0698-0077 0757-0199 0684-2231 0684-4731 0684-1041	1	R:FXD MET FLM 93.1K OHM 1% 1/8W R:FXD MET FLM 21.5K OHM 1% 1/8W SELECTEDPART R:FXD COMP 22K OHM 10% 1/4W R:FXD COMP 47K OHM 10% 1/4W R:FXD COMP 100K OHM 10% 1/4W	28480 28480 01121 01121 01121	0698-0077 0757-0199 CB 2231 CB 4731 CB 1041
A3R 93 A3R 94 A3R 95 A3R 96, R97 A3R 98	0684-3331 0684-1041 0698-3279 0684-1041 0757-0442	9	R:FXD COMP 33K OHM 10% 1/4W R:FXD COMP 100K OHM 10% 1/4W R:FXD MET FLM 4990 OHM 1% 1/8W R:FXD COMP 100K OHM 10% 1/4W R:FXD MET FLM 10.0K OHM 1% 1/8W	01121 01121 28480 01121 28480	C8 3331 C8 1041 0698-3279 C8 1041 0757-0442
A3R99 A3R101 A3R102, 103 A3R104 A3R105	0684-2231 0684-2231 0684-1041 0684-2231 0684-5641		R:FXD COMP 22K OHM 10% 1/4W R:FXD COMP 22K OHM 10% 1/4W R:FXD COMP 100K OHM 10% 1/4W R:FXD COMP 22K OHM 10% 1/4W R:FXD COMP 560K OHM 10% 1/4W	01121 01121 01121 01121 01121	CB 2231 CB 2231 CB 1041 CB 2231 CB 5641
A3R106 A3R107 A3R108 A3R109 A3R110 A3R112	0684-1041 0684-1041 0684-1041 0684-1041 0698-3279 0684-1041		R:FXD COMP 100K OHM 10% 1/4W R:FXD MET FLM 4990 OHM 1% 1/8W R:FXD COMP 100K OHM 10% 1/4W	01121 01121 01121 01121 28480 01121	CB 1041 CB 1041 CB 1041 CB 1041 CB 1041 0698-3279 CB 1041
A3S1 A3U1 A3U2 A3U3	3101-1312 1826-0043 1820-0223 1826-0043	1 5	SWITCH:SLIDE SPDT 0.5A 125V AC/DC IC:LINEAR OPERATIONAL AMPLIFIER INTEGRATED CIRCUIT:OPERATIONAL AMPL. IC:LINEAR OPERATIONAL AMPLIFIER	79727 28480 28480 28480	G132-0003 1826-0043 1820-0223 1826-0043
A3U4 A3U5 A3U6 A3U7 A3U8	18 20 - 0 22 3 18 20 - 0 77 7 18 20 - 0 5 9 5 18 20 - 0 5 9 4 18 20 - 0 5 8 3	1 1	INTEGRATED CIRCUIT: OPERATIONAL AMPL. IC:TTL LOW POWER BCD TO DECODER IC:TTL LP DUAL J-K MASTER SLAVE F/F IC:TTL J-K MASTER SLAVE F/F IC:TTL LP QUAD 2-INPT NAND GATE	28480 28480 12040 12040 12040	1820-0223 1820-0777 DM74173N DM74172N DM74L00N
A3U9 A3U11 A3U12 A3U13 A3U14	1820-0588 1820-0584 1820-0587 1820-0588 1820-0588	3 1	IC:TTL LP 4-INPT NAND GATE IC:TTL LP QUAD 2-INPT NOR GATE IC:TTL LP TRIPLE 3-INPT NAND GATE IC:TTL LP 4-INPT NAND GATE IC:TTL LP 4-INPT NAND GATE	12040 12040 12040 12040 12040	DM74L2ON DM74L02N DM74L1ON DM74L2ON DM74L2ON
A3U15	1826-0043		IC:LINEAR OPERATIONAL AMPLIFIER	28480	1826-0043

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A4	03581-66504	1	BOARD ASSY, DETECTOR	28480	03581-66504
A4C1 A4C2 A4C3	0180-0210 0180-0210 0150-0093		CAPACITCR-FXD; 3.3UF+-20% 15VDC TA CAPACITOR-FXD; 3.3UF+-20% 15VDC TA CAPACITOR-FXD .01UF+80-20% 100WVDC	56289 56289 28480	1500335X0015A2 1500335X0015A2 0150-0093
A 4C 4 A 4C 5 A 4C 6 A 4C 7 A 4C 8	0150-0093 0150-0093 0150-0093 0150-0093 0150-0093		CAPACITOR-FXD .01UF+80-20% 100WVDC	28480 28480 28480 28480 28480	0150-0093 0150-0093 0150-0093 0150-0093 0150-0093
A4C9 A4C11 A4C12 A4C13 A4C14	0180-1735 0160-0363 0150-0093 0140-0159 0150-0093	2 1	CAPACITOR-FXD; .22UF+-10% 35VDC TA CAPACITOR-FXD 620FF+-5% 300WVDC CAPACITOR-FXD .01UF+80-20% 100WVDC CAPACITOR-FXD .003UF+-2% 300WVDC CAPACITOR-FXD .01UF+80-20% 100WVDC	56289 28480 28480 72136 28480	150D224X9035A2 0160-0363 0150-0093 DM19F302G0300WV1CR 0150-0093
A4C15 A4C16 A4C17 A4C18 A4C19	0180-0197 0160-0153 0150-0084 0160-0763 0160-2204	26 1 12 10	CAPACITOR-FXD; 2.2UF+10% 20VDC TA CAPACITOR-FXD .001UF+10% 200WVDC CAPACITOR-FXD .1UF+80-20% 100WVDC CAPACITOR-FXD 5PF+-10% 500WVDC CAPACITOR-FXD 100PF+5% 300WVDC	56289 56289 28480 28480 28480	150D225X9020A2 292P10292 0150-0084 0160-0763 0160-2204
A 4C 21 A 4C 22 A 4C 23 A 4C 24 A 4C 25	0150-0084 0150-0084 0160-0763 0160-2204 0150-0084		CAPACITOR-FXD .1UF+80-20% 100WVDC CAPACITOR-FXD .1UF+80-20% 100WVDC CAPACITOR-FXD 5PF*-10% 500WVDC CAPACITOR-FXD 100PF+-5% 300WVDC CAPACITOR-FXD .1UF+80-20% 100WVDC	28480 28480 28480 28480 28480 28480	0150-0084 0150-0084 0160-0763 0160-2204 0150-0084

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A4C26 A4C27 A4C28 A4C29 A4C31	0150-0084 0160-0763 0160-2204 0150-0084 0150-0084		CAPACITOR-FXD .1UF+80-20% 100WVDC CAPACITOR-FXD 5PF+-10% 500WVDC CAPACITOR-FXD 100PF+-5% 300WVDC CAPACITOR-FXD .1UF+80-20% 100WVDC CAPACITOR-FXD .1UF+80-20% 100WVDC	28480 28480 28480 28480 28480	0150-0084 0160-0763 0160-2204 0150-0084 0150-0084
A4C32 A4C33 A4C34 A4C35 A4C36	0160-0763 0160-2204 0160-3094 0180-0210 0160-2960		CAPACITOR-FXD 5PF+-10% 500WVDC CAPACITOR-FXD 100PF+-5% 300WVDC CAPACITOR-FXD .1UF +-10% 100WVDC CER CAPACITOR-FXD 3.3UF +-20% 15VDC TA CAPACITOR-FXD .05UF +-20% 100WVDC	2 84 80 2 84 80 28480 56289 28480	0160-0763 0160-2204 0160-3094 1500335X0015A2 0160-2960
A4C37 A4C38 A4C39 A4C41 A4C42	0180-0106 0180-0197 016C-2605 016C-2605 0150-0093		CAPACITOR-FXD; 60UF+-20% 6VDC TA-SQLID CAPACITOR-FXD; 2-2UF+-10% 20VDC TA CAPACITOR-FXD -02UF+80-20% 25WVDC CAPACITOR-FXD -02UF+80-20% 25WVDC CAPACITOR-FXD -01UF+80-20% 100WVDC	56289 56289 28480 28480 28480	150D606X0006B2 150D225X9020A2 0160-2605 0160-2605 0150-0093
A 4C 43 A 4C 44 A 4C 45 A 4C 46 A 4C 47	0160-2204 0150-0022 0150-0093 0150-0093		CAPACITOR+FXD 100PF+-5% 300WVDC CAPACITOR-FXD 3-3PF+-10% 500WVDC CAPACITOR-FXD -01UF+80-20% 100WVDC CAPACITOR-FXD -01UF+80-20% 100WVDC CAPACITOR-FXD -01UF+80-20% 100WVDC	28480 95121 28480 28480 28480	0160-2204 TYPE QC 0150-0093 0150-0093 0150-0093
A4C48 A4C49 A4C51 A4C52 A4C53	0150-0093 0180-0291 0180-0291 0180-0210 0160-2605	10	CAPACITOR-FXD .01UF+80-20% 100WVDC CAPACITOR-FXD; 1UF+-10% 35VDC TA-SOLID CAPACITOR-FXD; 1UF+-10% 35VDC TA-SOLID CAPACITOR-FXD; 3.3UF+-20% 15VDC TA CAPACITOR-FXD; 0.2UF+80-20% 25WVDC	28480 56289 56289 56289 28480	0150-0093 150D105X9035A2 150D105X9035A2 150D35X0015A2 0160-2605
A4C54 A4C55 A4C56 A4C57 A4C58	0160-2204 0150-0022 0180-1743 0150-0093 0150-0093		CAPACITOR-FXD 100PF+-5% 300WVDC CAPACITOR-FXD 3-3PF+-10% 500WVDC CAPACITOR-FXD; .1UF+-10% 35VDC TA-SOLID CAPACITOR-FXD .01UF+80-20% 100WVDC CAPACITOR-FXD .01UF+80-20% 100WVDC	28480 95121 56289 28480 28480	0160-2204 TYPE QC 1500104X9035A2 0150-0093 0150-0093
A4C59 A4C61 A4C62 A4C63 A4C64	0160-2605 0160-2605 0160-2960 0160-0763 0150-0084		CAPACITOR-FXD .02UF+80-20% 25WVDC CAPACITOR-FXD .02UF+80-20% 25WVDC CAPACITOR-FXD .05UF+-20% 100WVDC CAPACITOR-FXD 5PF+-10% 500WVDC CAPACITOR+FXD .1UF+80-20% 100WVDC	28480 28480 28480 28480 28480	0160-2605 0160-2605 0160-2960 0160-0763 0150-0084
A4C65 A4C66 A4C67 A4C68 A4C69	0160-2960 0160-0154 0160-0154 0160-0157 0140-0198	1	CAPACITOR-FXD .05UF+-20% 100MVDC CAPACITOR-FXD .0022UF+-10% 200MVDC CAPACITOR-FXD .0022UF+-10% 200MVDC CAPACITOR-FXD .0047UF+-10% 200MVDC CAPACITOR-FXD 200PFP-5% 300MVDC	28480 56289 56289 56289 72136	0160-2960 292P22292 292P22292 292P47292 DM15F201J0300WV1CR
A4C70 A4C71 A4C72 A4C73 A4C74	0160-2960 0180-0197 0180-1746 0180-1746 0180-0197	18	CAPACITOR-FXD =05UF+-20% 100MVDC CAPACITOR-FXD; 2=2UF+-10% 20VDC TA CAPACITOR-FXD; 15UF+-10% 20VDC TA-SOLID CAPACITOR-FXD; 15UF+-10% 20VDC TA-SOLID CAPACITOR-FXD; 2=2UF+-10% 20VDC TA	28480 56289 56289 56289 56289	0160-2960 150D225X9020A2 150D156X9020B2 150D156X9020B2 150D225X9020A2
A4C75 A4C76 A4C77 A4C78 A4C79	0180-0228 0180-0197 0180-1746 0180-1746 0180-1746		CAPACITOR-FXD; 22UF+-10% 15VDC TA-SOLID CAPACITOR-FXD; 2°2UF+-10% 20VDC TA-SOLID CAPACITOR-FXD; 15UF+-10% 20VDC TA-SOLID CAPACITOR-FXD; 15UF+-10% 20VDC TA-SOLID CAPACITOR-FXD; 15UF+-10% 20VDC TA-SOLID	56289 56289 56289 56289 56289	150D226X9015B2 150D225X9020A2 150D156X9020B2 150D156X9020B2 150D156X9020B2
A4C81 A4C82 A4CR1 A4CR2 A4CR3	0180-0197 0180-0228 1901-0040 1901-0040 1901-0040		CAPACITOR-FXD; 2-2UF+-10% 20VDC TA CAPACITOR-FXD; 22UF+-10% 15VDC TA-SOLID DIODE; SWITCHING; ; 30V MAX VRM 50MA DIODE; SWITCHING; ; 30V MAX VRM 50MA DIODE; SWITCHING; ; 30V MAX VRM 50MA	56289 56289 28480 28480 28480	150D225X9020A2 150D226X901582 1901-0040 1901-0040 1901-0040
A4CR4 A4CR5 A4CR6 A4CR7 A4CR8	1901-0040 1901-0040 1901-0040 1901-0040 1901-0040		DIODE; SWITCHING; ; 30V MAX VRM 50MA	28480 28480 28480 28480 28480	1901-0040 1901-0040 1901-0040 1901-0040 1901-0040
A4CR9 A4CR11 A4CR12 A4CR13 A4CR14	1901-0040 1901-0040 1901-0040 1901-0179 1901-0179	8	DIODE; SWITCHING; ; 30V MAX VRM 50MA DIODE; SWITCHING; ; 30V MAX VRM 50MA DIODE; SWITCHING; ; 30V MAX VRM 50MA DIODE; SWITCHING; ; 15V MAX VRM 50MA DIODE; SWITCHING; ; 15V MAX VRM 50MA	28480 28480 28480 28480 28480	1901-0040 1901-0040 1901-0040 1901-0179 1901-0179
A4CR15 A4CR16 A4CR17 A4CR18 A4CR19	1901-0179 1901-0179 1901-0179 1901-0179 1901-0179		DIODE; SWITCHING; ; 15V MAX VRM 50MA	28480 28480 28480 28480 28480	1901-0179 1901-0179 1901-0179 1901-0179 1901-0179
A4CR21 A4CR22 A4CR23 A4CR24 A4CR25	1901-0179 1901-0040 1901-0040 1901-0040 1901-0040		DIODE; SWITCHING; ; 15V MAX VRM 50MA DIODE; SWITCHING; ; 30V MAX VRM 50MA	28480 28480 28480 28480 28480	1901-0179 1901-0040 1901-0040 1901-0040 1901-0040

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Numbe
A4CR26 A4CR27 A4CR28 A4CR29 A4CR31	1901-0040 1901-0040 1901-0040 1902-0048 1901-0347	ì	DIODE; SWITCHING; ; 30V MAX VRM 50MA DIODE; SWITCHING; ; 30V MAX VRM 50MA DIODE; SWITCHING; ; 30V MAX VRM 50MA DIODE; ZENER; 6.81V VZ; .4W MAX PD DIODE; SCHOTTKY; ; 8V MAX VRM	28480 28480 28480 28480 28480	1901-0040 1901-0040 1901-0040 1901-0040 1902-0048 1901-0347
A4CR32 A4L1 A4L2 A4L3 A4L4	1901-0347 9100-3261 9100-0541 9140-0129 9140-0129	1 2 7	DIODE; SCHOTTKY; ; 8V MAX VRM COIL:FXD 846 UH COIL, FXD, MOLDED RF CHOKE, 250UH 10% COIL; FXD; MOLDED RF CHOKE; 220UH 5% COIL; FXD; MOLDED RF CHOKE; 220UH 5%	28480 28480 04213 24226 24226	1901-0347 9100-3261 1670-1 15/223 15/223
A4L5 A4L6 A4L7 A4L8 A4L8	9140-0129 9100-0541 9140-0129 9140-0129 9140-0129		COIL; FXD; MOLDED RF CHOKE; 220UH 5% COIL, FXD, MOLDED RF CHOKE, 250UH 10% COIL; FXD; MOLDED RF CHOKE; 220UH 5% COIL; FXD; MOLDED RF CHOKE; 220UH 5% COIL; FXD; MOLDED RF CHOKE; 220UH 5%	24226 04213 24226 24226 24226	15/223 1670-1 15/223 15/223 15/223
A4L11 A4MP1 A4MP2 A4MP3 A4Q1	9140-0129 4040-0752 1200-0462 6960-0080 1854-0071	2 18	COIL; FXD; MOLDED RF CHOKE; 220UH 5% EXTRACTOR:PC BOARD, YELLOW SOCKET, ELEC, IC 1-CONT STRIP PKG DIP PLUG, HOLE, STANDARD HD185 DIA TRANSISTOR NPN SI PD=300MW FT=200MHZ	24226 28480 24995 98291 28480	15/223 4040-0752 3-116141-2 119-0052-00-0-009 1854-0071
A4Q2 A4Q3 A4Q4 A4Q5 A4Q6	1854-0071 1854-0071 1853-0010 1854-0071 1853-0010		TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR PNP SI CHIP PD=360MW TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW	28480 28480 28480 28480 28480	1854-0071 1854-0071 1853-0010 1854-0071 1853-0010
A4Q7 A4Q8 A4Q9 A4Q11 A4Q12	1854-0071 1854-0071 1854-0071 1854-0071 1854-0071		TRANSISTOR NPN SI PD=300MW FT=200MHZ	28480 28480 28480 28480 28480	1854-0071 1854-0071 1854-0071 1854-0071 1854-0071
A4Q13 A4Q14 A4Q15 A4Q16 A4R1 Δ A4R2 Δ	1854-0071 1854-0071 1854-0071 1853-0010 2100-3350 2100-3349	1 1	TRANSISTOR NPN SI PD=300Mw FT=200MHZ TRANSISTOR NPN SI PD=300Mw FT=200MHZ TRANSISTOR NPN SI PD=300Mw FT=200MHZ TRANSISTOR PNP SI CHIP PD=360Mw RESISTOR, VAR, 200 OHM 1/2W. RESISTOR, VAR, 100 OHM 1/2W	28480 28480 28480 28480 28480 28480	1854-0071 1854-0071 1854-0071 1853-0010 2100-3350 2100-3349
A4R3 A4R4 A4R5 A4R6	2100-3352 2100-3352 2100-3353 2100-3351	1	RESISTOR, VAR, TRMR, 1KOHM 10% C RESISTOR, VAR, TRMR, 1KOHM 10% C RESISTOR, VAR, TRMR, 20KOHM 10% C RESISTOR, VAR, TRMR, 500 OHM 10% C	73138 73138 73138 73138	72XR102 72XR102 2XR203 72XR501
A4R7 A4R8 A4R9 A4R10 A4R11	2100-3273 2100-3273 2100-3354 2100-3354 2100-3273	2	RESISTOR, VAR, TRMR, 2KOHM 10% C RESISTOR, VAR, TRMR, 2KOHM 10% C RESISTOR, VAR, TRMR, 50KOHM 10% C RESISTOR, VAR, TRMR, 50KOHM 10% C RESISTOR, VAR, TRMR, 2KOHM 10% C	28480 28480 73138 73138 73138	2100-3273 2100-3273 72XR504 72XR504 72XR202
A4R12 A4R13 A4R14 A4R15 A4R16	0757-0449 0757-0449 0757-0274 0757-0438 0698-3449	1	RESISTOR-FXD 20K 1% -125W F TUBULAR RESISTOR-FXD 20K 1% -125W F TUBULAR RESISTOR-FXD 1-21K 1% -125W F TUBULAR RESISTOR-FXD 5-11K 1% -125W F TUBULAR RESISTOR-FXD 28-7K 1% -125W F TUBULAR	24546 24546 24546 24546 16299	C4-1/8-T0-2002-F C4-1/8-T0-2002-F C4-1/8-T0-1213-F C4-1/8-T0-5111-F C4-1/8-T0-2872-F
A4R17 A4R18* A4R19 A4R20 A4R21*	0698-4436 0757-0282 0684-1031 0684-1031 0698-3443	1	RESISTOR-FXD 2.8K 1% .125M F TUBULAR RESISTOR-FXD FLM 221 OHM 1% 1/8W RESISTOR-FXD 10K 10% .25M CC TUBULAR RESISTOR-FXD 10K 10% .25M CC TUBULAR RESISTOR-FXD MET FLM 287 OHM 1% 1/8W	16299 24546 01121 01121 28480	C4-1/8-T0-2801-F C4-1/8-T0-221R-F C81031 C81031 0698-3443
A 4R 22 A 4R 23 A 4R 24 A 4R 25 A 4R 26	0757-0280 0757-0469 0757-0469 0757-0280 0757-0449	3	RESISTOR-FXO 1K 1% -125W F TUBULAR RESISTOR-FXO 150K 1% -125W F TUBULAR RESISTOR-FXO 150K 1% -125W F TUBULAR RESISTOR-FXO 1K 1% -125W F TUBULAR RESISTOR-FXO 20K 1% -125W F TUBULAR	24546 24546 24546 24546 24546	C4-1/8-T0-1001-F C4-1/8-T0-1503-F C4-1/8-T0-1503-F C4-1/8-T0-1001-F C4-1/8-T0-2002-F
A 4R 27 A 4R 28 A 4R 29 A 4R 31 A 4R 32	0757-0449 0684-3331 0684-1031 0684-1031 0684-3331	9	RESISTOR-FXD 20K 1% -125W F TUBULAR RESISTOR-FXD 33K 10% -25W CC TUBULAR RESISTOR-FXD 10K 10% -25W CC TUBULAR RESISTOR-FXD 10K 10% -25W CC TUBULAR RESISTOR-FXD 33K 10% -25W CC TUBULAR	24546 01121 01121 01121 01121	C4-1/8-T0-2002-F CB3331 CB1031 CB1031 CB3331
A 4R 33 A 4R 34 A 4R 35 A 4R 36 A 4R 37	0684-1031 0684-1031 0684-3331 0684-3331 0684-1831	1	RESISTOR-FXD 10K 10% .25W CC TUBULAR RESISTOR-FXD 10K 10% .25W CC TUBULAR RESISTOR-FXD 33K 10% .25W CC TUBULAR RESISTOR-FXD 33K 10% .25W CC TUBULAR RESISTOR-FXD 18K 10% .25W CC TUBULAR	01121 01121 01121 01121 01121	C81031 C81031 C83331 C83331 C81831
A 4R 38 A 4R 39 A 4R 41 A 4R 42 A 4R 43	0684-1531 0757-0426 0757-0394 0757-0401 0698-3488	15 3	RESISTOR-FXD 15K 10% .25M CC TUBULAR RESISTOR-FXD 1.3K 1% .125M F TUBULAR RESISTOR-FXD 51-1 0HM 1% .125M F RESISTOR-FXD 100 0HM 1% .125M F TUBULAR RESISTOR-FXD 442 0HM 1% .125M F TUBULAR	01121 24546 24546 24546 16299	CB1531 C4-1/8-T0-1301-F C4-1/8-T0-51R1-F C4-1/8-T0-101-F C4-1/8-T0-422R-F

Model 3580A Section VI

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A4R44 A4R45 A4R46 A4R47 A4R48	0757-0401 C757-0401 0698-4483 0757-0465 0698-4483	4 7	RESISTOR-FXD 100 OHM 1% .125W F TUBULAR RESISTOR-FXD 100 OHM 1% .125W F TUBULAR RESISTOR-FXD 18.7K 1% .125W F TUBULAR RESISTOR-FXD 100K 1% .125W F TUBULAR RESISTOR-FXD 18.7K 1% .125W F TUBULAR	24546 24546 24546 24546 24546	C4-1/8-T0-101-F C4-1/8-T0-101-F C4-1/8-T0-1872-F C4-1/8-T0-1003-F C4-1/8-T0-1872-F
A4R49 A4R51 A4R52 A4R53 A4R54	0684-5641 0684-1531 0684-2221 0684-1031 0684-4731	1	RESISTOR-FXD 560K 10% .25W CC TUBULAR RESISTOR-FXD 15K 10% .25W CC TUBULAR RESISTOR-FXD 2.2K 10% .25W CC TUBULAR RESISTOR-FXD 10K 10% .25W CC TUBULAR RESISTOR-FXD 47K 10% .25W CC TUBULAR	01121 01121 01121 01121 01121	C85641 C81531 C82221 C81031 C84731
A4R55 A4R56 A4R57 A4R58 A4R59	0684-1031 0698-4434 0757-0346 0757-0280 0757-0273	1 1 5	RESISTOR-FXD 10K 10% -25M CC TUBULAR RESISTOR-FXD 2-32K 1% -125M F TUBULAR RESISTOR-FXD 10 OHM 1% 125W F TUBULAR RESISTOR-FXD 1K 1% .125W F TUBULAR RESISTOR-FXD 3.01K 1% .125W F TUBULAR	01121 16299 24546 24546 24546	C81031 C4-1/8-T0-2321-F C4-1/8-T0-100-F C4-1/8-T0-1001-F C4-1/8-T0-3011-F
A4R61 A4R62 A4R63 A4R64 A4R65	0698-3245 0698-4488 0757-0273 0698-3245 0698-4488	8	RESISTOR-FXD 20.5K 1% .125W F TUBULAR RESISTOR-FXD 26.7K 1% .125W F TUBULAR RESISTOR-FXD 3.01K 1% .125W F TUBULAR RESISTOR-FXD 20.5K 1% .125W F TUBULAR RESISTOR-FXD 20.5K 1% .125W F TUBULAR	24546 24546 24546 24546 24546	C4-1/8-TO-2052-F C4-1/8-TO-2672-F C4-1/8-TO-3011-F C4-1/8-TO-2052-F C4-1/8-TO-2672-F
A4R66 A4R67 A4R68 A4R69 A4R71	0757-0273 0698-3245 0698-3279 0757-0273 0757-0434	7	RESISTOR-FXD 3.01K 1% .125W F TUBULAR RESISTOR-FXD 20.5K 1% .125W F TUBULAR RESISTOR-FXD 4.99K 1% .125W F TUBULAR RESISTOR-FXD 3.01K 1% .125W F TUBULAR RESISTOR-FXD 3.66K 1% .125W F TUBULAR	24546 24546 16299 24546 16299	C4-1/8-T0-3011-F C4-1/8-T0-2052-F· C4-1/8-T0-4991-F C4-1/8-T0-3011-F C4-1/8-T0-3651-Fト
A4R72 A4R73 A4R74 A4R75 A4R76	C698-3558 0698-3497 0757-0430 C698-3228 0698-3516	3 6	RESISTOR-FXO 4.02K 1% .125W F TUBULAR RESISTOR-FXD 6.04K 1% .125W F TUBULAR RESISTOR-FXD 2.21K 1% .125W F TUBULAR RESISTOR-FXD 49.9K 1% .125W F TUBULAR RESISTOR-FXD 6.34K 1% .125W F TUBULAR	16299 16299 24546 07716 16299	C4-1/8-T0-4021-F C4-1/8-T0-604R-F C4-1/8-T0-2211-F CEA1/8-T0-4991-F C4-1/8-T0-6341-F
A4R77 A4R78 A4R79 A4R81 A4R82	0757-0434 0757-0449 0684-1511 0684-1511 0757-0442	3 2	RESISTOR-FXD 3.65K 1% .125W F TUBULAR RESISTOR-FXD 20K 1% .125W F TUBULAR RESISTOR-FXD 150 0HM 10% .25W CC RESISTOR-FXD 150 0HM 10% .25W CC RESISTOR-FXD 10K 1% .125W F TUBULAR	24546 24546 01121 01121 24546	C4-1/8-T0-3651-F C4-1/8-T0-2002-F C81511 C81511 C4-1/8-T0-1002-F
A4R83 * A4R84 A4R85 A4R86 A4R86	0698-4403 0684-3331 0684-3331 0757-0465 0757-0427	1	RESISTOR-FX0 102 OHM 1% -125W F TUBULAR RESISTOR-FXD 33K 10% -25W CC TUBULAR RESISTOR-FXD 33K 10% -25W CC TUBULAR RESISTOR-FXD 100K 1% -125W F TUBULAR RESISTOR-FXD 1-5K 1% -125W F TUBULAR	16299 01121 01121 24546 24546	C4-1/8-T0-102R-F C83331 C83331 C4-1/8-T0-1003-F C4-1/8-T0-1501-F
A4R88 A4R89 A4R91 A4R92 A4R93	0698-3557 0757-0465 0757-0449 0684-3331 0684-3331	4	RESISTOR-FXD 806 OHM 1% -125W F TUBULAR RESISTOR-FXD 100K 1% -125W F TUBULAR RESISTOR-FXD 20K 1% -125W F TUBULAR RESISTOR-FXD 33K 10% -25W CC TUBULAR RESISTOR-FXD 33K 10% -25W CC TUBULAR	16299 24546 24546 01121 01121	C4-1/8-T0-806R-F C4-1/8-T0-1003-F C4-1/8-T0-2002-F C83331 C83331
A4R94 A4R95 A4R56 A4R57 A4R98	0684-4741 0684-4741 0684-1041 0684-3331 0757-0442		RESISTOR-FXD 470K 10% -25W CC TUBULAR RESISTOR-FXD 470K 10% -25W CC TUBULAR RESISTOR-FXD 100K 10% -25W CC TUBULAR RESISTOR-FXD 30K 10% -25W CC TUBULAR RESISTOR-FXD 10K 1% -125W F TUBULAR	01121 01121 01121 01121 01121 24546	CB4741 CB4741 CB1041 CB3331 C4-1/8-T0-1002-F
A4R99 A4R101 A4R102 A4R103 * A4R104 *	0757-0442 0757-0442 0698-4475 0698-4442 0698-4466	1 1 1	RESISTOR-FXD 10K 1% -125W F TUBULAR RESISTOR-FXD 10K 1% -125W F TUBULAR RESISTOR-FXD 9-76K 1% -125W F TUBULAR RESISTOR-FXD 4-42K 1% -125W F TUBULAR RESISTOR-FXD 976 OHM 1% -125W F TUBULAR	24546 24546 03888 16299 24546	C4-1/8-T0-1002-F C4-1/8-T0-1002-F PME55-1/8-T0-9761-F C4-1/8-T0-976N-F C4-1/8-T0-976N-F
A4R105 * A4R106 A4R107 A4R108 A4R109 *	0698-4419 0757-0401 0757-0465 0698-4435	1	RESISTOR-FXO 210 OHM 1% -125W F TUBULAR RESISTOR-FXD 100 OHM 1% -125W F TUBULAR RESISTOR-FXD 100K 1% -125W F TUBULAR RESISTOR-FXD 2-49K 1% -125W F TUBULAR RESISTOR-FXD 1.91K 1% -125W F TUBULAR	24546 24546 24546 16299 16299	C4-1/8-T0-210R-F C4-1/8-T0-101-F C4-1/8-T0-1003-F C4-1/8-T0-2491-F C4-1/8-T0-1911-F
A4R111 A4R112 A4R113 A4R114 A4R115	0698-3279 0684-2241 0757-0465 0757-0446 0757-0427	1	RESISTOR-FXD 4.99K 1% .125W F TUBULAR RESISTOR-FXD 220K 10% .25W CC TUBULAR RESISTOR-FXD 100K 1% .125W F TUBULAR RESISTOR-FXD 15K 1% .125W F TUBULAR RESISTOR-FXD 1.5K 1% .125W F TUBULAR	16299 01121 24546 24546 24546	C4-1/8-T0-4991-F C82241 C4-1/8-T0-1003-F C4-1/8-T0-1502-F C4-1/8-T0-1501-F
A4R116 A4R117 A4R118 A4R119 A4R120 A4R121 A4R122A A4R122A A4R122B* A4R123 A4R124 A4R125	0757-0407 0684-1531 0684-1031 0684-3341 0684-4721 0698-3499 0698-4509 0757-0465 0698-4539 0757-0462 0757-0280	1 4 1 1	RESISTOR-FX0 200 OHM 1% -125W F TUBULAR RESISTOR-FX0 15K 10% -25W CC TUBULAR RESISTOR-FX0 10K 10% -25W CC TUBULAR RESISTOR-FX0 330K 10% -25W CC TUBULAR RESISTOR-FX0 4-7K 10% -25W CC TUBULAR RESISTOR-FXD 40.2K 1% .125W F TUBULAR RESISTOR 80.6K 1% .125W RESISTOR 100K 1% .125W RESISTOR 100K 1% .125W RESISTOR-FX0 40.2K 1% .125W F TUBULAR RESISTOR-FX0 10K 1% .125W F TUBULAR RESISTOR-FX0 10K 1% .125W F TUBULAR RESISTOR-FX0 10K 1% .125W F TUBULAR	24546 01121 01121 01121 01121 16299 24546 24546 19701 24546 24546	C4-1/8-T0-201-F C81531 C81031 C83341 C84721 C4-1/8-T0-4022-F C4-1/8-T0-1003-F MF4C1/8-T0-4023-F C4-1/8-T0-1002-F C4-1/8-T0-1001-F

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Numbe
A4R126 A4R127 A4R128 A4R129 A4R130	0757-0449 0757-0449 0757-0280 0757-0280 0698-3499		RESISTOR-FXD 20K 1% -125W F TUBULAR RESISTOR-FXD 20K 1% -125W F TUBULAR RESISTOR-FXD 1K 1% -125W F TUBULAR RESISTOR-FXD 1K 1% -125W F TUBULAR RESISTOR-FXD 40.2K 1% -125W F TUBULAR	24546 24546 24546 24546 24546 16299	C4-1/8-T0-2002-F C4-1/8-T0-2002-F C4-1/8-T0-1001-F C4-1/8-T0-1001-F C4-1/8-T0-4022-F
A4R 131 A4R 132 A4R 133 A4R 134 A4R 135	C698-3499 0698-4473 0757-0458 0698-3279 0757-0317	5	RESISTOR-FXD 40.2K 1% .125W F TUBULAR RESISTOR-FXD 8.06K 1% .125W F TUBULAR RESISTOR-FXD 51.1K 1% .125W F TUBULAR RESISTOR-FXD 4.99K 1% .125W F TUBULAR RESISTOR-FXD 1.33K 1% .125W F TUBULAR	16299 24546 24546 16299 24546	C4-1/8-T0-4022-F C4-1/8-T0-8061-F C4-1/8-T0-5112-F C4-1/8-T0-4991-F C4-1/8-T0-1331-F
A4R136 A4R137 A4R138 A4R139 A4R140	0698-3264 0757-0280 0757-0438 0757-0288 0698-4484	1	RESISTOR-FXD 11.8K 1% .125M F TUBULAR RESISTOR-FXD 1k 1% .125M F TUBULAR RESISTOR-FXD 5.11K 1% .125M F TUBULAR RESISTOR-FXD 9.09K 1% .125M F TUBULAR RESISTOR-FXD 19.1K 1% .125M F TUBULAR	16299 24546 24546 30983 24546	C4-1/8-T0-1182-F C4-1/8-T0-1001-F C4-1/8-T0-5111-F MF4C1/8-T0-9091-F C4-1/8-T0-1912-F
A4R141 A4R142 A4R143 A4R144 A4R145	0757-0453 0757-0458 0757-0439 0698-3268 0757-0438	3 1 1	RESISTOR-FXD 30-1K 1% -125W F TUBULAR RESISTOR-FXD 51-1K 1% -125W F TUBULAR RESISTOR-FXD 6-81K 1% -125W F TUBULAR RESISTOR-FXD 11-5K 1% -125W F TUBULAR RESISTOR-FXD 5-11K 1% -125W F TUBULAR	24546 24546 24546 16299 24546	C4-1/8-T0-3012-F C4-1/8-T0-5112-F C4-1/8-T0-6811-F C4-1/8-T0-1152-F C4-1/8-T0-5111-F
A4R146 A4R147 A4R148 A4R149 A4R150	0684-6831 0684-5621 0698-4307 0757-0444 0684-1531	1 1 1 2	RESISTOR-FXD 68K 10% -25M CC TWBULAR RESISTOR-FXD 5-6K 10% -25M CC TUBULAR RESISTOR-FXD 14.3K 1% 125W F TUBULAR RESISTOR-FXD 12.1K 1% 125W F TUBULAR RESISTOR-FXD 15K 10% .25W CC TUBULAR	01121 01121 24546 24546 01121	C86831 C85621 C4-1/8-TO-1432-F C4-1/8-TO-1212-F C81531
A4RT1 A4U1 A4U2 A4U3 A4U4	0837-0050 1826-0109 1826-0109 1826-0109 1826-0109	4	THERMISTOR, NEG TC, 1K DISC IC;LIN;OPERATIONAL AMPLIFIER IC;LIN;OPERATIONAL AMPLIFIER IC;LIN;OPERATIONAL AMPLIFIER IC;LIN;OPERATIONAL AMPLIFIER	831 86 34371 34371 34371 34371	31D10 HA2-2625-80593 HA2-2625-80593 HA2-2625-80593 HA2-2625-80593
A4U5* A4U6 A4U7 A4U8 A4U9	1813-0017 1820-0058 1820-0058 1826-0043 1826-0043	1	LOG AMPLIFIER IC;LIN;OPERATIONAL AMPLIFIER IC;LIN;OPERATIONAL AMPLIFIER IC;LIN;OPERATIONAL AMPLIFIER IC;LIN;OPERATIONAL AMPLIFIER	28480 07263 07263 27014 27014	1813-0017 709HC 709HC LM307H LM307H
A4U10 A4U11	1826-0043 1826-0043		IC;LIN;OPERATIONAL AMPLIFIER IC;LIN;OPERATIONAL AMPLIFIER	27014 27014	LM307H LM307H
\5 \5° \5°*	0358066505 0358069515 0358069505		BOARD ASSY:IF FILTER (NOT FIELD REPLACEABLE) KIT:BOARD ASSY:IF FILTER REBUILT EXCHANGE ASSEMBLY	28480 28480 28480	03580-66505 03580-69515
.5C1 .5C2 .5C3	0121-0426 0121-0059 0121-0105	6	C:VAR MICA 50-380 PF 175VDCW C:VAR CFR 2-8 PF 300VDCW C:VAR CFR 9-35 PF NPD	72136 28480	03580-69505 T52517-7 0121-0059
15C4 15C5 15C6 15C7 15C8	0121-0426 0121-0059 0121-0105 0121-0426 0121-0059		C:VAR MICA 50-380 PF 175VDCW C:VAR CFR 2-8 PF 300VDCW C:VAR CFR 9-35 PF NPO C:VAR MICA 50-380 PF 175VDCW C:VAR CFR 2-8 PF 300VDCW	28480 72136 28480 28480 72136 28480	0121-0105 T52517-7 0121-0059 0121-0105 T52517-7 0121-0059
	0121-0105 0121-0426		C:VAR CER 9-35 PF NPO C:VAR MICA 50-380 PF 175VDCW C:VAR CER 2-8 PF 300VDCW	28480 72136 28480	0121-0105 T52517-7 0121-0059

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A5C14 A5C15 A5C17 A5C18 A5C19	0121-0059 0121-0105 0140-0200 0160-0763 0140-0218	5	C:VAR CER 2-8 PF 300VDCW C:VAR CER 9-35 PF NPO C:FXD MICA 390 PF 5% C:FXD MICA 5 PF 10% 500VDCW C:FXD MICA 160 PF 2%	28480 28480 72136 00853 28480	0121-0059 0121-0105 RDM15F391-J3C RDM15C050K5S 0140-0218
A5C21 A5C22 A5C23 A5C25 A5C26	0160-2960 0160-2605 0180-0291 0150-0093 0160-2605		C:FXD CER 0.05 UF 20% 100VDCW C:FXD CER 0.02 MFD +80-20% 25VDCW C:FXD ELECT 1.0 UF 10% 35VDCW C:FXD CER 0.01 UF +80-20% 100VDCW C:FXD CER 0.02 MFD +80-20% 25VDCW	562 89 72 982 562 89 72 982 72 982	29C212A9-CDH 5835000-Y5U 203Z 1500105X9035A2-DYS 801-K800011 5835000-Y5U 203Z
A5C27 A5C28 A5C29 A5C31 A5C32	0160-2960 0140-0200 0160-0763 0140-0218 0160-2960		C:FXD CER 0.05 UF 20% 100VDCW C:FXD MICA 390 PF 5% C:FXD MICA 5 PF 10% 500VDCW C:FXD MICA 160 PF 2% C:FXD CER 0.05 UF 20% 100VDCW	562 89 721 36 00853 28480 562 89	29C212A9-CDH RDM15F391-J3C RDM15C050K5S 0140-0218 29C212A9-CDH
A5C33 A5C34 A5C36 A5C37 A5C38	0160-2605 0180-0291 0150-0093 0160-2605 0160-2960		C:FXD CER 0.02 MFD +80-20% 25VDCW C:FXD ELECT 1.0 UF 10% 35VDCW C:FXD CER 0.01 UF +80-20% 100VDCW C:FXD CER 0.02 MFD +80-20% 25VDCW C:FXD CER 0.05 UF 20% 100VDCW	72982 56289 72982 72982 56289	5835000-Y5U 203Z 1500105X9035A2-DYS 801-K800011 5835000-Y5U 203Z 29C212A9-CDH
A5C39 A5C41 A5C42 A5C43 A5C44	0140-0200 0160-0763 0140-0218 0160-2960 0160-2605		C:FXD MICA 390 PF 5% C:FXD MICA 5 PF 10% 500VDCW C:FXD MICA 160 PF 2% C:FXD CER 0.05 UF 20% 100VDCW C:FXD CER 0.02 MFD +80-20% 25VDCW	72136 00853 28480 56289 72982	RDM15F391-J3C RDM15C050K5S 0140-0218 29C212A9-CDH 5835000-Y5U 203Z
A5C45 A5C47 A5C48 A5C49 A5C51	0180-0291 0150-0093 0160-2605 0160-2960 0140-0200		C:FXD ELECT 1.0 UF 10% 35VDCW C:FXD CER 0.01 UF +80-20% 100VDCW C:FXD CER 0.02 MFD +80-20% 25VDCW C:FXD CER 0.05 UF 20% 100VDCW C:FXD MICA 390 PF 5%	56289 72982 72982 72982 56289 72136	1500105X9035A2~DYS 801~K800011 5835000~Y5U 203Z 29C212A9~CDH RDM15F391~J3C
A5C52 A5C53 A5C54 A5C55 A5C56	0160-0763 0140-0218 0160-2960 0160-2605 0180-0291		C:FXD MICA 5 PF 10% 500VDCW C:FXD MICA 160 PF 2% C:FXD CER 0.05 UF 20% 100VDCW C:FXD CER 0.02 MFD +80-20% 25VDCW C:FXD ELECT 1.0 UF 10% 35VDCW	00853 28480 56289 72982 56289	RDM15C050K5S 0140-0218 29C212A9-CDH 5835000-Y5U 203Z 150D105X9035A2-DYS
A5C58 A5C59 A5C61 A5C62 A5C63	0150-0093 0160-2605 0160-2960 0140-0200 0160-0763		C:FXD CER 0.01 UF +80-20% 100VDCW C:FXD CER 0.02 MFD +80-20% 25VDCW C:FXD CER 0.05 UF 20% 100VDCW C:FXD MICA 390 PF 5% C:FXD MICA 5 PF 10% 500VDCW	72982 72982 56289 72136 00853	801-K800011 5835000-Y5U 203Z 29C21ZA9-CDH RDM15F391-J3C RDM15C050K5S
A5C64 A5C65 A5C66 A5C67 A5C68	0140-0218 0160-2960 0160-0195 0180-0291 0180-0291	1	C:FXD MICA 160 PF 2% C:FXD CER 0.05 UF 20% 100VDCW C:FXD CER 1000 PF 20% 250WAC C:FXD ELECT 1.0 UF 10% 35VDCW C:FXD ELECT 1.0 UF 10% 35VDCW	28480 56289 56289 56289 56289	0140-0218 29C212A9-CDH 19C251A1-CDH 150D105X9035A2-DYS 150D105X9035A2-DYS
A5C69 A5C71 A5C72 A5C73 A5C74	0150-0093 0150-0093 0160-2605 0160-2960 0160-2605		C:FXD CER 0.01 UF +80-20% 100VDCW C:FXD CER 0.01 UF +80-20% 100VDCW C:FXD CER 0.02 MFD +80-20% 25VDCW C:FXD CER 0.05 UF 20% 100VDCW C:FXD CER 0.02 MFD +80-20% 25VDCW	72982 72982 72982 72982 56289 72982	801-K800011 801-K800011 5835000-Y5U 203Z 29C212A9-COH 5835000-Y5U 203Z
A5C75 A5C76 A5C77 A5C78 A5C79	0160-2960 0160-2960 0160-2960 0180-0061 0180-0061		C:FXD CFR 0.05 UF 20% 100VDCW C:FXD CFR 0.05 UF 20% 100VDCW C:FXD CFR 0.05 UF 20% 100VDCW C:FXD AL ELECT 100 UF +75-10% 16VDCW C:FXD AL ELECT 100 UF +75-10% 16VDCW	562 89 562 89 562 89 562 89 562 89	29C212A9-CDH 29C212A9-CDH 29C212A9-CDH 30D107G016DC2-DSM 30D107G016DC2-DSM
A5C81 A5C82 A5CR1 A5CR2 A5CR3	0180-0061 0180-0061 1901-0040 1901-0040 1901-0040		C:FXD AL ELECT 100 UF +75-10% 16VDCW C:FXD AL ELECT 100 UF +75-10% 16VDCW DIDDE:SILICON 50 MA 30 WV DIDDE:SILICON 50 MA 30 WV	56289 56289 07263 07263 07263	300107G016DC2-DSM 300107G016DC2-DSM FDG1088 FDG1088 FDG1088
ASCR4 ASCR5 ASCR6 ASCR7 ASCR8	1901-0040 1901-0040 1901-0040 1901-0040 1901-0040		DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV	07263 07263 07263 07263 07263	FDG1088 FDG1088 FDG1088 FDG1088 FDG1088
A5CR9 A5CR11 A5CR12 A5CR13 A5CR14	1901-0040 1901-0040 1901-0040 1901-0040 1901-0040		DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV	07263 07263 07263 07263 07263	FDG1 088 FDG1 088 FDG1 088 FDG1 088 FDG1 088
A5CR15 A5CR16 A5CR17 A5CR18 A5CR19	1901-0040 1901-0040 1901-0040 1901-0040		DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV	07263 07263 07263 07263 07263	FDG1088 FDG1088 FDG1088 FDG1088 FDG1088

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A5CR21 A5CR22 A5CR23 A5CR24 A5CR25	1901-0040 1901-0040 1901-0040 1901-0040 1901-0040		DIODE:SILICON 50 MA 30 WV	07263 07263 07263 07263 07263	FDG1088 FDG1088 FDG1088 FDG1088 FDG1088
A5CR26 A5CR27 A5CR28 A5CR29 A5CR31	1901-0040 1901-0040 1901-0040 1901-0040 1901-0040		DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV	07263 07263 07263 07263 07263	FDG1 088 FDG1 088 FDG1 088 FDG1 088 FDG1 088
A5CR32 A5L1 A5L2 A5L3 A5L4	1901-0040 9100-3276 9100-3276 9100-3276 9100-3276	5	DIODE:SILICON 50 MA 30 WV INDUCTOR:POT CORE INDUCTOR:POT CORE INDUCTOR:POT CORE INDUCTOR:POT CORE	07263 28480 28480 28480 28480	FDG1088 9100-3276 9100-3276 9100-3276 9100-3276
A5L 5 A5L 6 A5L 7 A5O 1 A5O 2	9100-3276 9140-0137 9140-0137 1855-0081 1853-0010		INDUCTOR: POT CORE COLL: FXD RF 1000 UH 5% COLL: FXD RF 1000 UH 5% TSTR:SI FET TSTR:SI FPT TSTR:SI PNP(SELECTED FROM 2N 3251)	28480 28480 28480 80131 28480	9100-3276 9140-0137 9140-0137 2N5245 1853-0010
A503 A504 A505 A506 A507	1854-0071 1854-0071 1855-0081 1853-0010 1854-0071		TSTR:SI NPN(SELECTED FROM 2N3704) TSTR:SI NPN(SELECTED FROM 2N3704) TSTR:SI FET TSTR:SI PPN(SELECTED FROM 2N3251) TSTR:SI NPN(SELECTED FROM 2N3704)	28480 28480 80131 28480 28480	1854-0071 1854-0071 2N5245 1853-0010 1854-0071
A508 A509 A5011 A5012 A5013	1854-0071 1855-0081 1853-0010 1854-0071 1854-0071		TSTR:SI NPN(SELFCTED FROM 2N3704) TSTR:SI FET TSTR:SI PMP(SELECTED FROM 2N3251) TSTR:SI NPN(SELFCTED FROM 2N3704) TSTR:SI NPN(SELECTED FROM 2N3704)	28480 80131 28480 28480 28480	1854-0071 2N5245 1853-0010 1854-0071 1854-0071
A5014 A5015 A5016 A5017 A5018	1855-0081 1853-0010 1854-0071 1854-0071 1854-0226	7	TSTR:SI FET TSTR:SI PMP(SELECTED FROM 2N3251) TSTR:SI NPN(SELECTED FROM 2N3704) TSTR:SI NPN(SFLECTED FROM 2N3704) TSTR:SI NPN	80131 28480 28480 28480 80131	2N5245 1853-0010 1854-0071 1854-0071 2N4384
A5019 A5021 A5022 A5R1* A5R2	1853-0010 1854-0071 1854-0071 0698-4387 0698-4399	5 5	TSTR:SI PNP(SELECTED FROM 2N3251) TSTR:SI NPN(SELECTED FROM 2N3704) TSTR:SI NPN(SELECTED FROM 2N3704) R:FXD FLM 60-4 OHM 1% 1/8W R:FXD FLM 88-7 OHM 1% 1/8W	28480 28480 28480 28480 28480 28480	1853-0010 1854-0071 1854-0071 0698-4387 0698-4399
A5R3 A5R4 A5R5 A5R6 A5R7	0698-4517 0698-4486 0698-3382 0757-0283 0698-4481	5 6 14	R:FXO FLM 127K OHM 1% 1/8W R:FXD MET FLM 24.9K OHM 1% 1/8W R:FXD MET FLM 5.49K OHM 1% 1/8W R:FXD MET FLM 2.00K OHM 1% 1/8W R:FXD FLM 16.5K OHM 1% 1/8W	28480 28480 28480 28480 28480	0698-4517 0698-4486 0698-3382 0757-0283 0698-4481
A5R8 A5R9 A5R10 A5R11 A5R12	0684-1041 0757-0460 0684-1531 0757-0445 0698-4441	6 5 10	R:FXD COMP 100K OHM 10% 1/4W R:FXD MET FLM 61.5K OHM 1% 1/8W R:FXD COMP 15K OHM 10% 1/4W R:FXD FLM 13K OHM 1% 1/8W R:FXD MET FLM 3.74K OHM 1% 1/8W	01121 28480 01121 28480 28480	C8 1041 0757-0460 C8 1531 0757-0445 0698-4441
A5R 13 A5R 14 A5R 15 A5R 16 A5R 17	0698-3495 0757-0403 0698-3516 0698-4462 0684-2731	7 6 5 11	R:FXD MET FLM 866 OHM 1% 1/8W R:FXD MET FLM 121 OHM 1% 1/8W R:FXD FLM 6340 OHM 1% 1/8W R:FXD FLM 768 OHM 1% 1/8W R:FXD COMP 27K OHM 10% 1/4W	28480 28480 28480 28480 01121	0698-3495 0757-0403 0698-3516 0698-4462 CB 2731
A5R18 A5R19 A5R21 A5R22 A5R23	0684-2731 0684-1531 0684-1531 0684-1041 0684-1021	11	R:FXD COMP 27K OHM 10% 1/4W R:FXD COMP 15K OHM 10% 1/4W R:FXD COMP 15K OHM 10% 1/4W R:FXD COMP 100K OHM 10% 1/4W R:FXD COMP 1000 OHM 10% 1/4W	01121 01121 01121 01121 01121	C8 2731 CB 1531 CB 1531 CB 1041 CB 1021
A5R 24 A5R 25 A5R 26 * A5R 27 A5R 28	0684-1021 0684-1021 0698-4387 0698-4399 0698-4517		R:FXD COMP 1000 OHM 10% 1/4W R:FXD COMP 1000 OHM 10% 1/4W R:FXD FLM 60.4 OHM 1% 1/8W R:FXD FLM 88.7 OHM 1% 1/8W R:FXD FLM 88.7 OHM 1% 1/8W	01121 01121 28480 28480 28480	CB 1021 CB 1021 0698-4387 0698-4399 0698-4517
A5R 29 A5R 31 A5R 32 A5R 33 A5R 34	0698-4486 0698-3382 0757-0283 0698-4481 0684-1041		R:FXD MET FLM 24.9K OHM 1% 1/8W R:FXD MET FLM 5.49K OHM 1% 1/8W R:FXD MET FLM 2.00K OHM 1% 1/8W R:FXD FLM 16.5K OHM 1% 1/8W R:FXD COMP 100K OHM 10% 1/4W	28480 28480 28480 28480 01121	0698-4486 0698-3382 0757-0283 0698-4481 CB 1041
A5R 35 A5R 36 A5R 37 A5R 38 A5R 39	0757-0460 0757-0445 0698-4441 0698-3495 0757-0403		R:FXD MET FLM 61.9K OHM 1% 1/8W R:FXD FLM 13K OHM 1% 1/8W R:FXD MET FLM 3.74K OHM 1% 1/8W R:FXD MET FLM 866 OHM 1% 1/8W R:FXD MET FLM 121 OHM 1% 1/8W	28480 28480 28480 28480 28480	0757-0460 0757-0445 0698-4441 0698-3495 0757-0403

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Oty	Description	Mfr Code	Mfr Part Number
A5R41	0698-3516		R:FXD FLM 6340 OHM 1% 1/8W	28480	0698-3516
A5R42	0698-4462		R:FXD FLM 768 OHM 1% 1/8W	28480	0698-4462
A5R43	0684-2731		R:FXD COMP 27K OHM 10% 1/4W	01121	CB 2731
A5R44	0684-2731		R:FXD COMP 27K OHM 10% 1/4W	01121	CB 2731
A5R45	0684-1531		R:FXD COMP 15K OHM 10% 1/4W	01121	CB 1531
A5R46	0684-1531		R:FXD COMP 15K OHM 10% 1/4W	01121	CB 1531
A5R47 A5R48 * A5R49 A5R51 A6R62 A6R63 A6R64 A5R55 A5R56 A5R56	0684-1041 0698-4387 0698-4387 0698-4399 0698-4486 0698-3382 0757-0283 0698-4481 0684-1041 0757-0460		R:FXD COMP 100K OHM 10% 1/4W R:FXD FLM 60.4 OHM 1% 1/8W R:FXD FLM 88.7 OHM 1% 1/8W R:FXD FLM 127K OHM 1% 1/8W R:FXD MET FLM 24.9K OHM 1% 1/8W R:FXD MET FLM 54.9K OHM 1% 1/8W R:FXD MET FLM 5.49K OHM 1% 1/8W R:FXD MET FLM 2.00K OHM 1% 1/8W R:FXD TLM 16.5 K OHM 1% 1/8W R:FXD COMP 100K OHM 1% 1/4W R:FXD MET FLM 61.9K OHM 1% 1/4W	01121 28480 28480 28480 28480 28480 28480 28480 21121 28480	CB 1041 0698-4387 0698-4399 0698-4417 0698-4486 0698-3382 0757-0283 0698-4481 CB 1041 0797-0460
A5R58 A5R59 A5R61 A5R62 A5R63	0757-0445 0698-4441 0698-3495 0757-0403 0698-3516		R:FXD FLM 13K OHM 1% 1/8W R:FXD MET FLM 3.74K OHM 1% 1/8W R:FXD MET FLM 866 OHM 1% 1/8W R:FXD MET FLM 121 OHM 1% 1/8W R:FXD FLM 6340 OHM 1% 1/8W	28480 28480 28480 28480 28480 28480	0757-0445 0698-4441 0698-3495 0757-0403 0698-3516
A5R64	0698-4462		R:FXD FLM 768 OHM 1% 1/8W	28480	0698-4462
A5R65	0684-2731		R:FXD COMP 27K OHM 10% 1/4W	01121	CB 2731
A5R66	0684-2731		R:FXD COMP 27K OHM 10% 1/4W	01121	CB 2731
A5R67	0684-1531		R:FXD COMP 15K OHM 10% 1/4W	01121	CB 1531
A5R68	0684-1531		R:FXD CGMP 15K OHM 10% 1/4W	01121	C8 1531
A5R69	0684-1041		R:FXD CGMP 100K GHM 10% 1/4W	.01121	C8 1041
A5R71 *	0698-4387		R:FXD FLM 60.4 GHM 1% 1/8W	28480	0698-4387
A5R72	0698-4399		R:FXD FLM 88.7 GHM 1% 1/8W	28480	0698-4399
A5R73	0698-4517		R:FXD FLM 127K GHM 1% 1/8W	28480	0698-4517
A5R 74	0698-4486		R:FXD MET FLM 24.9K OHM 1% 1/8W	28480	0698-4486
A5R 75	0698-3382		R:FXD MET FLM 5.49K OHM 1% 1/8W	28480	0698-3382
A5R 76	0757-0283		R:FXD NET FLM 2.00K OHM 1% 1/8W	28480	0757-0283
A5R 77	0698-4481		R:FXD FLM 16.5K OHM 1% 1/8W	28480	0698-4481
A5R 78	0684-1041		R:FXD COMP 100K OHM 10% 1/4W	01121	CB 1041
A5R 79	0757-0460		R:FXD MET FLM 61.9K DHM 1% 1/8W	28480	0757-0460
A5R 81	0757-0445		R:FXD FLM 13K DHM 1% 1/8W	28480	0757-0445
A5R 82	0698-4441		R:FXD MET FLM 3.74K DHM 1% 1/8W	28480	0698-4441
A5R 83	0698-3495		R:FXD MET FLM 866 DHM 1% 1/8W	28480	0698-3495
A5R 84	0757-0403		R:FXD MET FLM 121 DHM 1% 1/8W	28480	0757-0403
A5R 85 A5R 86 A5R 87 A5R 88 A5R 89	0698-3516 0698-4462 0684-2731 0684-2731 0684-1531		R:FXD FLM 6340 OHM 1% 1/8W R:FXD FLM 768 OHM 1% 1/8W R:FXD COMP 27K OHM 10% 1/4W R:FXD COMP 27K OHM 10% 1/4W R:FXD COMP 15K OHM 10% 1/4W	28480 28480 01121 01121	0698-3516 0698-4462 CR 2731 CB 2731 CB 1531
A5R 91	0684-1531		R:FXD COMP 15K OHM 10% 1/4W	01121	CB 1531
A5R 92	0684-1041		R:FXD COMP 100K OHM 10% 1/4W	01121	CB 1041
A5R 93 *	0698-4387		R:FXD FLM 60-4 OHM 1% 1/8W	28480	0698-4387
A5R 94	0698-4399		R:FXD FLM 88-7 OHM 1% 1/8W	28480	0698-4399
A5R 95	0698-4517		R:FXD FLM 127K OHM 1% 1/8W	28480	0698-4517
A5R 96	0757-0401	1	R:FXD MET FLM 100 OHM 1% 1/8W	28480	0757-0401
A5R 97	0698-4486		R:FXD MET FLM 24-0K CHM 1% 1/8W	28480	0698-4486
A5R 98	0698-3223		R:FXD FLM 1-24K OHM 1% 1/8W	28480	0698-3223
A5R 99	0757-0283		R:FXD MET FLM 2-00K CHM 1% 1/8W	28480	0757-0283
A5R 101	0698-3155		R:FXD MET FLM 4-64K CHM 1% 1/8W	28480	0698-3155
A5R 102	0684-1041		R:FXD COMP 100K 0HM 10% 1/4W	01121	CB 1041
A5R 103	0757-0460		R:FXD MET FLM 61.5K 0HM 1% 1/8W	28480	0757-0460
A5R 104	0757-0445		R:FXD FLM 13K 0HM 1% 1/8W	28480	0757-0445
A5R 105	0698-4441		R:FXD MET FLM 3.74K 0HM 1% 1/8W	28480	0698-4441
A5R 106	0698-3495		R:FXD MET FLM 866 0HM 1% 1/8W	28480	0698-3495
A5R107	0757-0403		R:FXD MET FLM 121 OHM 1% 1/8W	28480	0757-0403
A5R108	0698-3516		R:FXD FLM 6340 OHM 1% 1/8W	28480	0698-3516
A5R109	0698-4462		R:FXD FLM 768 OHM 1% 1/8W	28480	0698-4462
A5R111	0684-2731		R:FXD COMP 27K OHM 10% 1/4W	01121	CB 2731
A5R112	0684-2731		R:FXD COMP 27K OHM 10% 1/4W	01121	CB 2731
A5R113	0684-2731		R:FXD COMP 27K OHM 10% 1/4W	01121	CB 2731
A5R114	0684-1531		R:FXD COMP 15K OHM 10% 1/4W	01121	CB 1531
A5R115	0684-1531		R:FXD COMP 15K OHM 10% 1/4W	01121	CB 1531
A5R116	0757-0442		R:FXD MET FLM 10.0K OHM 1% 1/4W	28480	0757-0442
A5R117	0684-1041		R:FXD COMP 100K OHM 10% 1/4W	01121	CB 1041
A5R118 A5R119 A5RT1 A5RT2 A5RT3	0757-0394 0757-0394 0837-0086 0837-0086	5	R:FXD MET FLM 51.1 OHM 1% 1/8W R:FXD MET FLM 51.1 OHM 1% 1/8W THERMISTOR:DISC TYPE 200 OHM 10% THERMISTOR:DISC TYPE 200 OHM 10% THERMISTOR:DISC TYPE 200 OHM 10%	28480 28480 15801 15801 15801	0757-0394 0757-0394 KB22J1 KB22J1 KB22J1

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A5RT4 A5RT5 A5T1 A5T2 A5T3	0837-0086 0837-0086 9100-3262 9100-3262 9100-3262	5	THERMISTOR: DISC TYPE 200 OHM 10% THERMISTOR: DISC TYPE 200 OHM 10% TRANSFORMER TRANSFORMER TRANSFORMER TRANSFORMER	15801 15801 28480 28480 28480	KB22J1 KB22J1 9100-3262 9100-3262 9100-3262
A5T4 A5T5 A5Y1-Y5**	9100-3262 9100-3262	1	TRANSFORMER TRANSFORMER CRYSTAL:SET:NOT FIELD REPLACEABLE (SEE PARA. 7-25)	28480 28480 28480	9100-3262 9100-3262
A6	03580-66506	1	BOARD ASSY:LOW VOLTAGE POWER SUPPLY	28480	03580-66506
A6C1 A6C2 A6C3,C4	0180-1746 0180-0291 0180-1943		C:FXD FLFCT 15 UF 10% 20 VDCW C:FXD FLFCT 1.0 UF 10% 35 VDCW C:FXD FLFCT 1000 UF +75-10% 25 VDCW	28480 56289 56289	0180-1746 150D105X9035A2-DYS 39D108G025GL4-DSB
A6C5 A6C6 A6C7 A6C8	0180-0291 0180-1746 0180-0274 0180-0197	1	C:FXD ELECT 1.0 UF 10% 35VDCW C:FXD ELFCT 15 UF 10% 20VDCW C:FXD AL FLECT 10 UF +75-10% 16V C:FXD FLECT 2.2 UF 10% 20VDCW	56289 28480 56289 56289	150D105X9035A2-DYS 0180-1746 30D1066016BA2-DSM 150D225X9020A2-DYS
A6C9 A6C11 A6C12 A6C13 A6C14	0180-0197 0140-0206 0150-0022 0180-0197 0140-0217	3	C:FXD ELECT 2.2 UF 10% 20VDCW C:FXD MICA 270 PF 5% C:FXD TI 3.3 PF 10% 500VDCW C:FXD ELECT 2.2 UF 10% 20VDCW C:FXD MICA 140 PF 2%	56289 72136 78488 56289 28480	150D225X9020A2-DYS RDM15F2715 500V GA 150D225X9020A2-DYS 0140-0217
A6C15 A6C16 A6C17 A6C18 A6C19	0150-0022 0160-0161 0160-0161 0180-0061 0180-1746	3	C:FXD TI 3.3 PF 10% 500VDCW C:FXD MY 0.01 UF 10% 200VDCW C:FXD MY 0.01 UF 10% 200VDCW C:FXD AL FLECT 100 UF +75-10% 16VDCW C:FXD ELFCT 15 UF 10% 20VDCW	78488 56289 56289 56289 28480	GA 192P10392-PTS 192P10392-PTS 300107G016DC2-DSM 0180-1746
A6C21 A6C22 A6CR1 A6CR2 A6CR3	0180-0061 0180-1746 1902-3149 1901-0040 1901-0040	1	C:FXD AL FLECT 100 UF +75-10% 16VOCW C:FXD FLECT 15 UF 10% 20VOCW DIODE REFAKDOWN:9,09V 5% DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV	56289 28480 28480 07263 07263	30D107G016DC2-DSM 0180-1746 1902-3149 FDG1088 FDG1088
A6CR4 A6CR5 A6CR6 A6CR7 A6CR8	1901-0040 1901-0045 1901-0045 1901-0045 1901-0045	8	DIGDE:SILICON 50 MA 30 WV DIGDE:SILICON 0.75A 100PIV DIGDE:SILICON 0.75A 100PIV DIGDE:SILICON 0.75A 100PIV DIGDE:SILICON 0.75A 100PIV	07263 04713 04713 04713 04713	FDG1088 SR1358-7 SR1358-7 SR1358-7 SR1358-7
A6CR9 A6CR11 A6CR12 A6CR13 A6CR15	1901-0045 1901-0045 1901-0045 1901-0045 1902-0025	3	DIODE:SILICON 0.75A 100PIV DIODE:SILICON 0.75A 100PIV DIODE:SILICON 0.75A 100PIV DIODE:SILICON 0.75A 100PIV DIODE:SILICON 0.75A 100PIV DIODF-BREAKDOWN:10.0V 5% 400 MW	04713 04713 04713 04713 28480	SR1358-7 SR1358-7 SR1358-7 SR1358-7 1902-0025
A6CR16 A6CR17—19 A6CR20 A6CR21 A6CR22,23 A6CR24 A6F1,F2 A6K1 A6K1 A6K2	1902-0777 1901-0040 1902-3190 1902-0025 1901-0040 1902-3190 2110-0343 0490-0366 0490-0366	2 2 2	DIODE:BREAKDOWN 6.2V 5% DIODE:SI 50 MA 30 WV DIODE:BKDN 13V DIODE:BREAKDOWN: 10.0V 5% 400 MW DIODE:SI 50 MA 30 WV DIODE:BKDN 13V FUSE: 0.250 AMP AT 125V SWITCH:REED RELAY SPNO CONTACT COIL ASSY:REED RELAY SPNO CONTACT	04713 07263 28480 28480 07263 28480 79515 28480 09026 28480	IN825 FDG1088 1902—3190 1902—0025 FDG1088 1902—3190 275.250 0490—0366 P.S.3101—10X 0490—0366
A6K2 A601 A602 A603 A604	0490-0515 1853-0010 1854-0404 1853-0052 1854-0404	7 ?	COLL ASSY:REED RELAY TSTR:SI PNP(SELECTED FROM 2N3251) TSTR:SI PNP TSTR:SI PNP TSTR:SI PNP	09026 28480 28480 80131 28480	P.S.3101-10X 1853-0010 1854-0404 2N3740 1854-0404
A605 A606 A607 A608 A609	1853-0010 1854-0404 1853-0010 1853-0010 1854-0072	2	TSTR:SI PNP(SELECTED FROM 2N3251) TSTR:SI NPN TSTR:SI PNP(SELECTED FROM 2N3251) TSTR:SI PNP(SELECTED FROM 2N3251) TSTR:SI NPN	28480 28480 28480 28480 80131	1853-0010 1854-0404 1853-0010 1853-0010 2N3054
A6011 A6012 A6013 A6014 A6015	1853-0010 1853-0010 1854-0404 1853-0010 1854-0072		TSTR:SI PNP(SFLECTED FROM 2N3251) TSTR:SI PNP(SELECTED FROM 2N3251) TSTR:SI NPN TSTR:SI PNP(SELECTED FROM 2N3251) TSTR:SI NPN	28480 28480 28480 28480 80131	1853-0010 1853-0010 1854-0404 1853-0010 2N3054

Section VI

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A6016 A6017 A6018 A6019 A6020	1853-0010 1854-0404 1854-0404 1853-0052 1854-0404		TSTR:SI PNP(SELFCTFD FROM 2N3251) TSTK:SI NPN TSTR:SI NPN TSTR:SI PNP TSTR:SI PNP	28480 28480 28480 80131 28480	1853-0010 1854-0404 1854-0404 2N3740 1854-0404
A6R1 A6R2 A6R3 A6R4 A6R5	0757-0433 0698-4308 0757-0438 0698-4123 0757-1092	2 2 4 1	R:FXD MFT FLM 3-32K OHM 1% 1/8W R:FXD MET FLM 16-9K OHM 1% 1/8W R:FXD MET FLM 5-11K OHM 1% 1/8W R:FXD MET FLM 499 OHM 1% 1/8W R:FXD MET FLM 287 OHM 1% 1/2W	28489 28480 28480 28460 28480	0757-0433 0698-4308 0757-0438 0698-4123 0757-1092
A6R6 A6R7 A6R8 A6R9 A6R11	0757-028? 0757-0799 0766-0014 0757-0799 0766-0014	2 ?	R:FXD MET FLM 221 OHM 1% 1/8W R:FXD MET FLM 121 OHM 1% 1/2W R:FXD MET FLM 12 OHM 2% 3W R:FXD MET FLM 121 OHM 1% 1/2W R:FXD MET FLM 121 OHM 2% 3W	28480 28480 28480 28480 28480	0757-0282 0757-0799 0766-0014 0757-0799 0766-0014
A6R12 A6R13 A6R14 A6R15 A6R16	0757-0433 0698-4308 0757-0438 0698-4123 0757-0465		R:FXD MET FLM 3.32K OHM 1% 1/8W R:FXD MET FLM 16.9K OHM 1% 1/8W R:FXD MET FLM 5.11K OHM 1% 1/8W R:FXD MET FLM 499 OHM 1% 1/8W R:FXD MET FLM 100K OHM 1% 1/8W	28480 28480 28480 28480 28480	0757-0433 0698-4308 0757-0438 0698-4123 0757-0465
A6R 17 A6R 18 A6R 19 A6R 21 A6R 22	0757-0442 0757-0442 0757-0809 0757-0469 0757-0458	2	R:FXD MFT FLM 10.0K DHM 1% 1/8W R:FXD MET FLM 10.0K DHM 1% 1/8W R:FXD MET FLM 332 DHM 1.0% 1/2W R:FXD FLM 150K DHM 1% 1/8W R:FXD MET FLM 51.1K DHM 1% 1/8W	28480 28480 28480 28480 28480	0757-0442 0757-0442 0757-0809 0757-0469 0757-0458
A 6R 23 A 6R 24 A 6R 25 A 6R 26 A 6R 27	0757-0809 0757-0465 0698-3488 0698-4435 0698-3499		R:FXD MET FLM 332 OHM 1.0% 1/2W R:FXD MET FLM 100K OHM 1% 1/8W R:FXD MET FLM 442 OHM 1% 1/8W R:FXD FLM 2.49K OHM 1% 1/8W R:FXD FLM 40.2K OHM 1% 1/8W	28480 28480 28480 28480 28480	0757-0809 0757-0465 0698-3488 0698-4435 0698-3499
A6R 28 A6R 29 A6R 31 A6R 32 A6R 33	0757-0283 0698-3558 0757-0161 0811-3069 0698-4123	11 1	R:FXD MET FLM 2.00K 0HM 1% 1/8W R:FXD MFT FLM 4.02K 0HM 1% 1/6W R:FXD FLM 604 0HM 1% 1/8W R:FXD WW 1.0 0HM 5% R:FXD MET FLM 499 0HM 1% 1/8W	28480 28480 28480 28480 28480	0757-0283 0698-3558 0757-0161 0811-3069 0698-4123
A6R34 A6R35 A6R36 A6R37 A6R38	0757-0283 0698-3245 0757-0442 0698-3245 0698-5323	4	R:FXD MET FLM 2.00K OHM 1% 1/8W R:FXD MET FLM 20.5K OHM 1% 1/8W R:FXD MET FLM 10.0K OHM 1% 1/8W R:FXD MET FLM 20.5K OHM 1% 1/8W R:FXD FLM 4K OHM 0.5% 1/8W	28480 28480 28480 28480 28480	0757-0283 0698-3245 0757-0442 0698-3245 0698-323
A6R39 A6R41 * A6R42 A6R43 A6R44	0698-6846 0698-4467 0698-3279 0698-4509 0757-0283	1	R:FXD FLM 5.42K OHM 0.05% 1/8W R:FXD FLM 1.05 K OHM 1% 1/8W SELECTED PART R:FXD MFT FLM 4990 OHM 1% 1/8W R:FXD FLM 80.6K OHM 1% 1/8W R:FXD MFT FLM 2.00K OHM 1% 1/8W	28480 28480 28480 28480 28480 28480	0698-6846 0698-4467 0698-3279 0698-4509 0757-0283
A6R45 A6R46 A6R47 A6R48 A6R49	0698-3558 0757-0161 0698-4123 0811-3069 0698-3245	1	R:FXD MET FLM 4.02K 0HM 1% 1/8W R:FXD FLM 604 0HM 1% 1/8W R:FXD MET FLM 499 0HM 1% 1/8W R:FXD WW 1.0 0HM 5% 1W R:FXD MET FLM 20.5K 0HM 1% 1/8W	28480 28480 28480 28480 28480 28480	0698-3558 0757-0161 0698-4123 0811-3069 0698-3245
A6R 51 A6R 52 A6R 53 A6U 1 A6U2	0757-0283 0698-3193 0698-3193 1870-0723 1870-0223 0340-0162 03580-21101	<b>6</b> 4 1	R:FXD MET FLM 2.00K 0HM 1% 1/8W R:FXD FLM 10K 0HM 0.25% 1/8W R:FXD FLM 10K 0HM 0.25% 1/8W INTEGRATED CIRCUIT:OPERATIONAL AMPL. INTEGRATED CIRCUIT:OPERATIONAL AMPL. INSULATOR:TRANSISTOR HEAT SINK:TRANSISTOR	28480 28480 28480 28480 28480 28480 28480	0757-0283 0698-3193 0698-3193 1820-0223 1820-0223 0340-0162 03580-21101
A <b>7</b> A7	03580-66507 03580-69507	1	BOARD ASSY:LOGIC REBUILT EXCHANGE ASSEMBLY	284 80 28480	03580-66507 03580-69507
A7C1 A7C2 A7C3 A7C4 A7C5	0180-0791 0160-2530 0160-2012 0160-0127 0160-0297	1 1 2 1	C:FXD FLECT 1.0 UF 10% 35VDCW C:FXD MICA 180 PF 2% 300V C:FXD MICA 330 PF 5% 500VDCW C:FXD CFR 1.0 UF 20% 25VDCW C:FXD MY 0.0012 UF 10% 200VDCW	56289 28480 00853 56289 56289	150D105X9035A2-DYS 0160-2530 RDM15F331J5S 5C13CS-CML 192P12292-PTS
A7C6 A7C7 A7C8 A7C9 A7CR1	0180-1746 0160-0127 0180-0229 0180-1746 1902-0551	5 2	C:FXD ELECT 15 UF 10% 20VDCW C:FXD CER 1.0 UF 20% 25VDCW C:FXD ELECT 33 UF 10% 10VDCW C:FXD FLECT 15 UF 10% 20VDCW DIODE BREAKDOWN:6.19V 5%	28480 56289 28480 28480 28480	0180-1746 5C13CS-CML 0180-0229 0180-1746 1902-0551
A7CR2 A7L1 A7L2 A7L3 A7O1	1902-0551 9100-0541 9140-0129 9100-0541 1854-0071		DIODE BREAKDOWN:6.19V 5% COIL:FXD 0.25 MH 10% COIL:FXD 0.25 MH 10% COIL:FXD 0.25 MH 10% TSTR:SI NPN(SELECTED FROM 2N3704)	28480 28480 28480 28480 28480	1902-0551 9100-0541 9140-0129 9100-0541 1854-0071

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A702 A703 A704 A705 A706	1853-0010 1854-0071 1853-0010 1854-0071 1853-0010	6	TSTR:SI PNP TSTR:SI NPN(SELECTED FROM 2N3704) TSTR:SI PNP TSTR:SI NPN(SELECTED FROM 2N3704) TSTR:SI NPN(SELECTED FROM 2N3704)	80131 28480 80131 28480 80131	2N4917 1854-0071 2N4917 1854-0071 2N4917
A707 A708 A709 A7011 A7012	1854-0071 1853-0010 1853-0010 1854-0071 1854-0071		TSTR:SI NPN(SELECTED FROM 2N3704) TSTR:SI PNP TSTR:SI PPN TSTR:SI NPN(SELECTED FROM 2N3704) TSTR:SI NPN(SELECTED FROM 2N3704)	28480 80131 80131 28480 28480	1854-0071 2N4917 2N4917 1854-0071 1854-0071
A7013 A7014 A7R1 A7R2 A7R3	1853-0012 1854-0039 0684-1031 0684-3931 0757-0465	1 1 7	TSTR:SI PNP TSTR:SI NPN R:FXD COMP 10K OHM 10% 1/4W R:FXD COMP 39K OHM 10% 1/4W R:FXD MET FLM 100K OHM 1% 1/8W	80131 80131 01121 01121 28480	2N2904A 2N3053 CB 1031 CB 3931 0757-0465
A7R 4 A7R 5 A7R 6 A7R 7 A7R 8	0698-0077 0698-3228 0698-0077 0698-3228 0698-5575	6	R:FXD MET FLM 93.1K OHM 1% 1/8W R:FXD MET FLM 49.9K OHM 1% 1/8W R:FXD MET FLM 93.1K OHM 1% 1/8W R:FXD MET FLM 49.9K OHM 1% 1/8W R:FXD FLM 100K OHM 0.5% 1/8W	28480 28480 28480 28480 28480	0698-0077 0698-3228 0698-0077 0698-3228 0698-5575
A7R 9 A7R 10 A7R 11 A7R 12 A7R 13	0698-3228 0684-1031 0698-5575 0698-5573 0698-3445	7 6	R:FXD MET FLM 49.9K OHM 1% 1/8W R:FXD COMP 10K OHM 10% 1/4W R:FXD FLM 100K OHM 0.5% 1/8W R:FXD FLM 50K OHM 0.5% 1/8W R:FXD MET FLM 348 OHM 1% 1/8W	28 4 80 01 1 2 1 28 4 80 28 4 80 28 4 80	0698-3228 CB 1031 0698-5575- 0698-5573 0698-3445
A7R14 A7R15 A7R16 A7R17 A7R18	0811-1794 0698-5573 0698-3445 0811-1794 0698-7973	7	R:FXD WW 99.25K OHM 0.01% 1/40W R:FXD FLM 50K OHM 0.5% 1/8W R:FXD WET FLM 348 OHM 1% 1/8W R:FXD WW 99.25K OHM 0.01% 1/40W R:FXD FLM 50K OHM 0.05% 1/8W	28480 28480 28480 28480 28480	0811-1794 0698-5573 0698-3445 0811-1794 0698-7973
A7R19 A7R21 A7R22 A7R23 A7R24	0698-4158 0698-7973 0698-4158 0684-3931 0684-3931	5	R:FXD FLM 100K OHM 0.1% 1/8W R:FXD FLM 50K OHM 0.05% 1/8W R:FXD FLM 100K OHM 0.1% 1/8W R:FXD COMP 39K OHM 10% 1/4W R:FXD COMP 39K OHM 10% 1/4W	28480 28480 28480 01121	0698-4158 0698-7973 0698-4158 CB 3931 CB 3931
A7R 25 A7R 26 A7R 27 A7R 28 A7R 29	0684-3931 0684-3931 0698-3268 0757-0442 0684-5621		R:FXD COMP 39K OHM 10% 1/4W R:FXD COMP 39K OHM 10% 1/4W R:FXD FLM 11.5K OHM 1% 1/8W R:FXD MET FLM 10.0K OHM 1% 1/8W R:FXD COMP 5.6K OHM 10% 1/4W	01121 01121 28480 28480 01121	CB 3931 CB 3931 0698-3268 0757-0442 CB 5621
A7R31 A7R32 A7R33 A7R34 A7R35	0757-0280 0698-4469 0698-3268 0757-0442 0684-5621	4	R:FXD MET FLM 1K OHM 1% 1/8W R:FXD FLM 1.15K OHM 1% 1/8W R:FXD FLM 11.5K OHM 1% 1/8W R:FXD MET FLM 10.0K OHM 1% 1/8W R:FXD COMP 5.6K OHM 10% 1/4W	28480 28480 28480 28480 01121	0757-0280 0698-4469 0698-3268 0757-0442 CB 5621
A7R 36 A7R 37 A7R 38 A7R 39 A7R 41	0757-0280 0698-4469 0698-3268 0757-0442 0684-5621		R:FXD MET FLM 1K OHM 1% 1/8W R:FXD FLM 1.15K OHM 1% 1/8W R:FXD FLM 11.5K OHM 1% 1/8W R:FXD MET FLM 10.0K OHM 1% 1/8W R:FXD COMP 5.6K OHM 10% 1/4W	28480 28480 28480 28480 01121	0757-0280 0698-4469 0698-3268 0757-0442 CB 5621
A7R42 A7R43 A7R44 A7R45 A7R46	0757-0280 0698-4469 0698-3268 0757-0442 0684-5621		R:FXD MET FLM 1K OHM 1% 1/8W R:FXD FLM 1.15K OHM 1% 1/8W R:FXD FLM 11.5K OHM 1% 1/8W R:FXD MET FLM 10.0K OHM 1% 1/8W R:FXD COMP 5.6K OHM 10% 1/4W	28480 28480 28480 28480 01121	0757-0280 0698-4469 0698-3268 0757-0442 CB 5621
A7R 47 A7R 48 A7R 49 A7R 51 A7R 52	0757-0280 0698-4469 0757-0464 0698-3228 0757-0978	1 4	R:FXD MET FLM 1K OHM 1% 1/8W R:FXD FLM 1.15K OHM 1% 1/8W R:FXD MET FLM 90.9K OHM 1% 1/8W R:FXD MET FLM 49.9K OHM 1% 1/8W R:FXD FLM 95.3K OHM 1% 1/8W	28480 28480 28480 28480 28480	0757-0280 0698-4469 0757-0464 0698-3228 0757-0978
A7R 53 A7R54 A7R55 A7R56 A7R57	0698-3228 0757-0978 0698-3228 0698-5575 0698-5573		R:FXD MET FLM 49.9K OHN 1% 1/8W R:FXD FLM 95.3K OHM 1% 1/8W R:FXD MET FLM 49.9K OHM 1% 1/8W R:FXD FLM 100K OHM 0.5% 1/8W R:FXD FLM 50K OHM 0.5% 1/8W	28 4 80 28 4 80 28 4 80 28 4 80 28 4 80	0698-3228 0757-0978 0698-3228 0698-5575 0698-5573
A7R58 A7R59 A7R61 A7R62 A7R63	0698-5575 0698-5573 0698-3445 0811-1794 0698-7973		R:FXD FLM 100K 0HM 0.5% 1/8W R:FXD FLM 50K 0HM 0.5% 1/8W R:FXD MET FLM 348 0HM 1% 1/8W R:FXD MW 99.25K 0HM 0.01% 1/40W R:FXD FLM 50K 0HM 0.05% 1/8W	28480 28480 28480 28480 28480	0698-5575 0698-5573 0698-3445 0811-1794 0698-7973
A7R64 A7R65 A7R66 A7R67 A7R68	0698-3445 0811-1794 0698-7973 0698-4158 0698-7973		R:FXD MET FLM 348 OHM 1% 1/8W R:FXD WW 99_25K OHM 0.01% 1/40W R:FXD FLM 50K OHM 0.05% 1/8W R:FXD FLM 100K OHM 0.1% 1/8W R:FXD FLM 50K OHM 0.05% 1/8W	28480 28480 28480 28480 28480	0698-3445 0811-1794 0698-7973 0698-4158 0698-7973

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A7R69	06 98-7975	2	R:FXD FLM 100K OHM 0.05% 1/8W	28 4 80	0698-7975
A7R71	06 98-7973		R:FXD FLM 50K OHM 0.05% 1/8W	28 4 80	0698-7973
A7R72	06 98-7975		R:FXD FLM 100K OHM 0.05% 1/8W	28 4 80	0698-7975
A7R73	0811-1794		R:FXD WW 99.25K OHM 0.01% 1/40W	28 4 80	0811-1794
A7R74	06 98-4456		R:FXD FLM 549 OHM 1.0% 1/8W	28 4 80	0698-4456
A7R75	0757-0449		R:FXD FLM 20K OHM 1% 1/8W	28480	0757-0449
A7R76	0757-0449		R:FXD FLM 20K OHM 1% 1/8W	28480	0757-0449
A7R77	0757-0449		R:FXD FLM 20K OHM 1% 1/8W	28480	0757-0449
A7R78	0757-0449		R:FXD FLM 20K OHM 1% 1/8W	28480	0757-0449
A7R79	0757-0449		R:FXD FLM 20K OHM 1% 1/8W	28480	0757-0449
A7R81	0757-0449		R:FXD FLM 20K OHM 1% 1/8W	28480	0757-0449
A7R82	0757-0449		R:FXD FLM 20K OHM 1% 1/8W	28480	0757-0449
A7R83	0757-0449		R:FXD FLM 20K OHM 1% 1/8W	28480	0757-0449
A7R84	0757-0449		R:FXD FLM 20K OHM 1% 1/8W	28480	0757-0449
A7R85	0757-0449		R:FXD FLM 20K OHM 1% 1/8W	28480	0757-0449
A7R86	0757-0449		R:FXO FLM 20K OHM 1% 1/8W	28480	0757-0449
A7R87	0757-0449		R:FXD FLM 20K OHM 1% 1/8W	28480	0757-0449
A7R88	0757-0449		R:FXD FLM 20K OHM 1% 1/8W	28480	0757-0449
A7R89	0757-0449		R:FXD FLM 20K OHM 1% 1/8W	28480	0757-0449
A7R91	0757-0449		R:FXD FLM 20K OHM 1% 1/8W	28480	0757-0449
A7R92	0757-0449		R:FXD FLM 20K OHM 1% 1/8W	28480	0757-0449
A7R93	0757-0449		R:FXD FLM 20K OHM 1% 1/8W	28480	0757-0449
A7R94	0757-0449		R:FXD FLM 20K OHM 1% 1/8W	28480	0757-0449
A7R95	0757-0449		R:FXD FLM 20K OHM 1% 1/8W	28480	0757-0449
A7R96	0757-0449		R:FXD FLM 20K OHM 1% 1/8W	28480	0757-0449
A7R97	0684-5621		R:FXD COMP 5.6K OHM 10% 1/4W	01121	CB 5621
A7R98	0684-5621		R:FXD COMP 5.6K OHM 10% 1/4W	01121	CB 5621
A7R99	0684-5621		R:FXD COMP 5.6K OHM 10% 1/4W	01121	CB 5621
A7R101	0684-5621		R:FXD COMP 5.6K OHM 10% 1/4W	01121	CB 5621
A7R102	0757-0438		R:FXD MET FLM 5.11K OHM 1% 1/8W	28480	O757-0438
A7R103	0757-0438		R:FXD MET FLM 5.11K OHM 1% 1/8W	28480	0757-0438
A7R104	0757-0442		R:FXD MET FLM 10.0K OHM 1% 1/8W	28480	0757-0442
A7R105	0684-3931		R:FXD COMP 39K OHM 10% 1/4W	01121	CB 3931
A7R106	0684-1031		R:FXD COMP 10K OHM 10% 1/4W	01121	CB 1031
A7R107	0684-3931		R:FXD COMP 39K OHM 10% 1/4W	01121	CB 3931
A7R108	0684-1031	2 3	R:FXD COMP 10K OHM 10% 1/4W	01121	CB 1031
A7R109 *	0698-3160		R:FXD MFT FLM 31.6K OHM 1% 1/8W SELECTED PART	28480	0698-3160
A7R111 *	0698-4492		R:FXD FLM 32.4K OHM 1% 1/8W SELECTED PART	28480	0698-4492
A7R112	0698-3228		R:FXD MFT FLM 49.9K OHM 1% 1/8W	28480	0698-3228
A7R113	0757-0438		R:FXD MET FLM 5.11K OHM 1% 1/8W	28480	0757-0438
A7R114	0698-3498	1	R:FXD MFT FLM 8.66K OHM 1% 1/8W	28480	0698-3498
A7R115	0757-0438		R:FXD MFT FLM 5.11K OHM 1% 1/8W	28480	0757-0438
A7R116	0757-0978		R:FXO FLM 95.3K OHM 1% 1/8W	28480	0757-0978
A7R117	0757-0465		R:FXD MFT FLM 100K OHM 1% 1/8W	28480	0757-0465
A7R118	0698-3228		R:FXD MFT FLM 49.9K OHM 1% 1/8W	28480	0698-3228
A7R119 A7R121 A7R122 A7R123 A7R124	0757-0978 0698-3228 0698-5575 0698-3228 0698-5575		R:FXD FLM 95.3K	28480 28480 28480 28480 28480	0757-0978 0698-3228 0698-5575 0698-3228 0698-5575
A7R125	0698-5573		R:FXD FLM 50K 0HM 0.5% 1/8W	28480	0698-5573
A7R126	0811-1794		R:FXD WW 99.25K 0HM 0.01% 1/40W	28480	0811-1794
A7R127	0698-3445		R:FXD MET FLM 348 0HM 1% 1/8W	28480	0698-3445
A7R128	0811-1794		R:FXD WW 99.25K 0HM 0.01% 1/40W	28480	0811-1794
A7R129	0698-3445		R:FXD MET FLM 348 0HM 1% 1/8W	28480	0698-3445
A7R 131	0698-5573		R:FXD FLM 50K 0HM 0.5% 1/8W	28480	0698-5573
A7R 132	0698-7973		R:FXD FLM 50K 0HM 0.05% 1/8W	28480	0698-7973
A7R 133	0698-4158		R:FXD FLM 100K 0HM 0.1% 1/8W	28480	0698-4158
A7R 134	0698-7973		R:FXD FLM 50K 0HM 0.05% 1/8W	28480	0698-7973
A7R 135	0698-4158		R:FXD FLM 100K 0HM 0.1% 1/8W	28480	0698-4158
A7R136 A7R137 A7R138 A7R139 A7R141 A7U1 A7U2 A7U3 A7U4 A7U5 A7U6	0687-3301 0687-3301 0698-3193 0698-3193 1826-0026 1820-0939 1820-0949 1820-0949 1820-1114	3 4 2 6	R:FXD COMP 33 OHM 10% 1/2W R:FXD COMP 33 OHM 10% 1/2W R:FXD FLM 10K OHM 0.25% 1/8W R:FXD FLM 10K OHM 0.25% 1/8W R:FXD FLM 10K OHM 0.25% 1/8W IC:LINEAR COMPARATOR IC:COMOS DUAL PD* F/F W/SET-RESET IC:CMOS QUAD 2-INPT NAND GATE IC:C/MOS TRIPLE 3-INPT NAND GATE IC:C/MOS IC:C/MOS	01121 01121 28480 28480 28480 12040 02735 02735 02735 28480 28480	E8 3301 E8 3301 0698-3193 0698-3193 0698-3193 LM311H CD4013AE CD4011AE CD4023AE 1820-1114
A7U7 A7U8 A7U9 A7U11 A7U12	1820-0938 1820-0943 1820-0949 1820-0928 1820-1145	3 1	IC:CMOS DUAL J-K M/S F/F W/SFT-RESET IC:CMOS TRIPLF 3-INPT NAND GATE IC:CMOS QUAD 2-INPT NAND GATE IC:DIGITAL IC:CMOS	02735 02735 02735 02735 02735	CD4027AF CD4023AE CD4011AF CD4041AE CD4049AF

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
Designation					
A7U13 A7U14 A7U15 A7U16 A7U17	1870-0949 1820-0203 1870-0947 1870-0938 1870-0946	5 1 2	IC:CMOS QUAD 2-INPT NAND GATF IC:OPERATIONAL AMPLIFIER IC:CMOS QUAD EXCLOR GATE IC:CMOS QUAD JK M/S F/F W/SET-RESET IC:DIGITAL CMOS QUAD 2-INPT NOR GATE	02735 07263 02735 02735 28480	CD4011AE SL8940 CD4030AE CD4027AE 1820-0946
A7U18 A7U19 A7U21 A7U22 A7U23	1826-0021 1826-0026 1826-0021 1820-0951 1820-0946	12	IC:VOLTAGE FOLLOWER O TO 70 C TD-99 IC:LINEAR COMPARATOR IC:VOLTAGE FOLLOWER O TO 70 C TD-99 IC:CHORS OUAD 2-INPT WULTIPLEXER IC:DIGITAL CMOS QUAD 2-INPT NOR GATE	12 040 12 040 12 040 02 7 3 5 28 4 8 0	LM310H LM311H LM310H C04019AE 1820-0946
A7U24 A7U25 A7U26 A7U27 A7U28	1820-0938 1820-0951 1820-0951 1820-0203 1820-1114		IC:CMOS DUAL J-K M/S F/F W/SET-RESET IC:CMOS QUAD 2-INPT MULTIPLEXER IC:CMOS QUAD 2-INPT MULTIPLEXER IC:OPERATIONAL AMPLIFIER IC:C/MOS	02735 02735 02735 02735 07263 28480	CD4027AE CD4019AE CD4019AE SL8940 1820-1114
A7U29 A7U31 A7U32 A7U33 A7U34	1820-1114 1820-0958 1820-0926 1820-0958 1820-0926	2 2	IC:C/MOS IC:CMOS OUAD CLOCKED *D* LATCH IC:CMOS 4-BIT FULL ADDER IC:CMOS OUAD CLOCKED *D* LATCH IC:CMOS 4-BIT FULL ADDER	28480 02735 02735 02735 02735	1820-1114 CD4042AE CD4008AE CD4042AE CD4008AE
A7U35 A7U36 A7U37 A7U38 A7U39	1820-1078 1820-1078 1820-1078 1820-1078 1820-1078	8	IC: MOS IC: MOS IC: MOS IC: MOS IC: MOS IC: MOS	28480 28480 28480 28480 28480	1820-1078 1820-1078 1820-1078 1820-1078 1820-1078
A7U41 A7U42 A7U43 A7U44 A7U45	1820-1078 1820-1078 1820-1078 1826-0021 1826-0021		IC:MOS IC:MOS IC:MOS IC:MOS IC:VOLTAGE FOLLOWER 0 TO 70 C TO-99 IC:VOLTAGE FOLLOWER 0 TO 70 C TO-99	28480 28480 28480 12040 12040	1820-1078 1820-1078 1820-1078 LM310H LM310H
A7U46 A7U47 A7U48 A7U49 A7U51	1826-0021 1826-0021 1820-0939 1820-0951 1820-0951		IC:VOLTAGE FOLLOWER O TO 70 C TO-99 IC:VOLTAGE FOLLOWER O TO 70 C TO-99 IC:CMOS DUAL"O" F/F W/SET-RESET IC:CMOS QUAD 2-INPT MULTIPLEXER IC:CMOS OUAD 2-INPT MULTIPLEXER	12040 12040 02735 02735 02735	LM31 0H LM31 0H CD4013AE CD4019AE CD4019AE
A7U52 A7U53 A7U54 A7U55 A7U56	1820-0951 1826-0021 1820-0949 1820-0939 1820-1114		IC:CMOS QUAD 2-INPT MULTIPLEXER IC:VOLTAGE FOLLOWER O TO 70 C TO-99 IC:CMOS QUAD 2-INPT NAND GATE IC:CMOS DUAL*D** F/F W/SET-RESET IC:C/MOS	02735 12040 02735 02735 28480	CD4019AE LM310H CD4011AE CD4013AE 1820-1114
A7U57 A7U58 A7U59 A7U61 A7U62	1820-1114 1820-0951 1820-0730 1826-0071 1826-0021	1	IC:C/MOS IC:CMOS OUAD 2-INPT MULTIPLEXER IC:TTL LP RE-TRIG/RE-SET MONO-MULTI IC:YOLTAGE FOLLOWER 0 TO 70 C TO-99 IC:YOLTAGE FOLLOWER 0 TO 70 C TO-99	28 480 02 735 28 480 12 040 12 040	1820-1114 CD4019AE 1820-0730 LM310H LM310H
A7U63 A7U64 A7U65	1820-0928 1820-0928 1820-0203		IC:DIGITAL IC:DIGITAL IC:OPERATIONAL AMPLIFIER	02735 02735 07263	CD4041AE CD4041AE SL8940
A 8	03580-66508	1	BOARD ASSY:CONTROL	28480	03580-66508
A8C1 A8C2	0121-0426 0140-0206		C:VAR MICA 50-380 PF 175VDCW C:FXD MICA 270 PF 5%	72136 72136	T52517-7 RDM15F2715 500V
A8C3 A8C4 A8C5 A8C6 A8C7	0150-0093 0160-0945 0160-0945 0160-0363 0150-0093	2	C:FXD CER 0.01 UF +80-20% 100VDCW C:FXD MICA 910 PF 5% C:FXD MICA 910 PF 5% C:FXD MICA 620PF 5% C:FXD CER 0.01 UF +80-20% 100VDCW	72982 28480 28480 28480 72982	801-K800011 0160-0945 0160-0945 0160-0363 801-K800011
A8C8 A8C9 A8C11 A8C12 A8C13	0140-0206 0150-0084 0160-3556 0150-0093 0160-0161	1	C:FXD MICA 270 PF 5% C:FXD CER 0.1 UF +80-20% 100VDCW C:FXD POLY 0.087 UF 5% 50VDCW C:FXD CER 0.01 UF +80-20% 100VDCW C:FXD MY 0.01 UF 10% 200VDCW	72136 72982 28480 72982 56289	RDM15F2715 500V 8131-100-651-104Z 0160-3556 801-K800011 192P10392-PTS
A8C14 A8C15 A8C16 A8C18 A8C19	0160-0363 0170-0055 0150-0093 0150-0093 0160-0164	1	C:FXD MICA 620PF 5% C:FXD MY 0-1UF 20% 200VDCW C:FXD CER 0-01 UF +80-20% 100VDCW C:FXD CER 0-01 UF +80-20% 100VDCW C:FXD MY 0-039 UF 10% 200VDCW	28480 56289 72982 72982 56289	0160-0363 192P10402 801-K800011 801-K800011 192P39392-PTS
A8C21 A8C22 A8C23 A8C24 A8C25	0180-0374 0160-0166 0160-2960 0180-0376 0180-0197	3 1 2	C:FXD TANT. 10 UF 10% 20VDCW C:FXD MY 0.068 UF 10% 200VDCW C:FXD CFR 0.05 UF 20% 100VDCW C:FXD FLECT 0.47 UF 10% 35VDCW C:FXD FLECT 2.2 UF 10% 20VDCW	56289 56289 56289 56289 56289	1500106X9020B2-DYS 192P68392-PTS 29C212A9-CDH 1500474X9035A2-DYS 1500225X9020A2-DYS

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A8C26 A8C27 A8C28 A8C29 A8C31	0150-0122 0150-0122 0180-1746 0180-1746 0180-0141	2	C:FXD CER 2000 PF 20% 500VDCW C:FXD CER 2000 PF 20% 500VDCW C:FXD ELECT 15 UF 10% 20VDCW C:FXD ELECT 15 UF 10% 20VDCW C:FXD ELECT 50 UF +75-10% 50VDCW	72982 72982 28480 28480 56289	801-000-Y5S-202M 801-000-Y5S-202M 0180-1746 0180-1746 3005066050002-DSM
A8C32 A8C33 A8CR1 A8CR2 A8CR3	0180-1746 0180-1746 1901-0040 1901-0040 1901-0040		C:FXD ELECT 15 UF 10% 20VDCW C:FXD ELECT 15 UF 10% 20VDCW DIODE:SILICON 50 MA 30 MV DIODE:SILICON 50 MA 30 MV DIODE:SILICON 50 MA 30 MV	28480 28480 07263 07263 07263	0180-1746 0180-1746 FDG1088 FDG1088 FDG1088
A8CR4 A8CR5 A8CR6 A8CR7 A8CR8	1901-0040 1902-0041 1901-0040 1902-3182 1901-0040		DIODE:SILICON 50 MA 30 MV DIODE:BREAKDOWN 5.11V 5% DIODE:SILICON 50 MA 30 MV DIODE BREAKDOWN:SILICON 12.1V 5% DIODE:SILICON 50 MA 30 MV	07263 04713 07263 28480 07263	FDG1088 SZ10939-98 FDG1088 1902-3182 FDG1088
A8CR9 A8CR11 A8CR12 A8CR13,CR14 A8CR15	1901-0040 1901-0040 1901-0040 1902-3311 1901-0033	2 5	DIODE:SILICON 50 MA 30 MV DIODE:SILICON 50 MA 30 MV DIODE:SILICON 50 MA 30 MV DIODE PREAKOOMN:38-39 5% 400MM DIODE:SILICON 100MA 180MV	07263 07263 07263 28480 07263	FDG1088 FDG1088 FDG1088 1902-3311 FD3369
A8CR16 A8CR17 A8CR18 A8CR19 A8CR21	1901-0033 1901-0033 1901-0033 1901-0033	1	DIODE:SILICON 100MA 180MV DIODE:SILICON 100MA 180MV DIODE:SILICON 100MA 180MV DIODE:SILICON 100MA 180MV DIODE:SILICON 100MA 180MV	07263 07263 07263 07263 07263	FD3369 FD3369 FD3369 FD3369 FDA 6308
A8CR22 A8CR23 A8L1 A8L2 A8L3	1901-0040 1901-0040 9140-0129 9140-0129 9100-3282	2	DIODE:SILICON 50 MA 30 WV DIODE:SILICON 50 MA 30 WV COIL:FXD RF 220 UH COIL:FXD RF 220 UH INDUCTOR:POT CORE	07263 07263 28480 28480 28480	FDG1088 FDG1088 9140-0129 9140-0129 9100-3282
A8L 4 A8O1 A8O2 A8Q3 A8O4	9100-3282 1854-0019 1854-0071 1855-0081 1855-0081	3	INDUCTOR:POT CORE TSTR:SI NPN TSTR:SI NPN(SELECTED FROM 2N3704) TSTR:SI FET TSTR:SI FET	28480 28480 28480 80131 80131	9100-3282 1854-0019 1854-0071 2N5245 2N5245
A805 A806 A807 A808 A809	1853-0010 1853-0010 1854-0071 1854-0071 1853-0010		TSTR: SI PNP TSTR: IS PNP (SELECTED FROM 2N3251) TSTR:SI NPNISELECTED FROM 2N3704) TSTR:SI NPNISELECTED FROM 2N3704) TSTR:SI PNPISELECTED FROM 2N3251)	28480 28480 28480 28480 28480	1853-0010 1853-0010 1854-0071 1854-0071 1853-0010
A8011 A8012 A8013 A8014 A8015	1854-0071 1853-0086 1854-0071 1854-0071 1854-0019	5	TSTR:SI NPN(SELECTED FROM 2N3704) TSTR:SI PNP TSTR:SI NPN(SELECTED FROM 2N3704) TSTR:SI NPN(SELECTED FROM 2N3704) TSTR:SI NPN	28480 80131 28480 28480 28480	1854-0071 2N5087 1854-0071 1854-0071 1854-0019
A8016 A8017 A8018 A8019 A8021 A8022 A8023 A8R1 A8R2 A8R3	1853-0016 1854-0019 1854-0232 1854-0474 1205-0048 1854-0476 1853-0010 1855-0081 2100-3354 2100-3358 2100-3357	1 9 1 1	TSTR:SI PNP TSTR:SI NPN TSTR:SI NPN TSTR:SI NPN TSTR:SI NPN HEATSINK:TRANSISTOR TSTR:SI NPN TSTR:SI PNP(SELECTED FROM 2N3251) TSTR:SI FET R:VAR CERMET 50 K OHM 10% R: VAR CERMET 1 MEGOHM 20% TYPE VI 1/2W R: TRMR 500K 10%	801.31 28.480 28.480 28.480 02735 28.480 801.31 28.480 32997	2N3638 1854-0019 1854-0474 1205-0048 2N3879 1853-0010 2N5245 2100-3354 2100-3358 3386X-Y46-504
A8R4 A8R5 A8R6 A8R7 A8R8	2100-3353 0684-4721 0684-4711 0684-1821 0684-2231	5 1	R:VAR CFRMET 20K OHM 10% 1/2W R:FXD COMP 4700 OHM 10% 1/4W R:FXD COMP 470 OHM 10% 1/4W R:FXD COMP 1800 OHM 10% 1/4W R:FXD COMP 22K OHM 10% 1/4W	28480 01121 01121 01121 01121	2100-3353 C8 4721 C8 4711 C8 1821 C8 2231
A8R9 A8R11 A8R12 A8R13 A8R14	0684-3321 0684-2231 0684-2231 0698-4483 0698-4483	1	R:FXD COMP 3300 OHM 10% 1/4W R:FXD COMP 22K OHM 10% 1/4W R:FXD COMP 22K OHM 10% 1/4W R:FXD MET FLM 18.7K OHM 1% 1/8W R:FXD MET FLM 18.7K OHM 1% 1/8W	01121 01121 01121 28480 28480	CB 3321 CB 2231 CB 2231 0698-4483
A8R15 A8R16 A8R17 A8R18 A8R19	0757-0449 0757-0449 0757-0442 0757-0449 0757-0280		R:FXD FLM 20K OHM 1% 1/8W R:FXD FLM 20K OHM 1% 1/8W R:FXD MET FLM 10.0K OHM 1% 1/8W R:FXD FLM 20K OHM 1% 1/8W R:FXD MET FLM 1K OHM 1% 1/8W	28480 28480 28480 28480 28480	0757-0449 0757-0449 0757-0442 0757-0449 0757-0280
A8R 21 A8R 22 A8R 23 A8R 24 A8R 25	0698-4473 0757-0458 0757-0451 0683-1555 0683-1041	ı	R:FXD FLM 8-06K DHM 1% 1/8W R:FXD MET FLM 51-1K DHM 1% 1/8W R:FXD MET FLM 24-3K DHM 1% 1/8W R: FXD COMP 1.5 MEGOHM 5% 1/4W R: FXD COMP 100K OHM 10% 1/4W	28480 28480 28480 01121 01121	0698-4473 0757-0458 0757-0451 CB1555 CB1041

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Oty	Description	Mfr Code	Mfr Part Number
A8R 26 A8R 27 A8R 28 A8R 29 A8R 31	0757-0458 0757-0458 0757-0472 0757-0458 0698-4503	2	R:FXO MET FLM 51.1K OHM 1% 1/8W R:FXO MET FLM 51.1K OHM 1% 1/8W R:FXD MET FLM 200K OHM 1% 1/8W R:FXD MET FLM 51.1K OHM 1% 1/8W R:FXD FLM 66.5K OHM 1% 1/8W	28480 28480 28480 28480 28480 28480	0757-0458 0757-0458 0757-0472 0757-0458 0698-4503
A8R32 A8R33 A8R34 A8R35 A8R36	0698-4526 0698-4507 0684-1041 0684-5631 0757-0465	1 1 2	R:FXD FLM 191K OHM 1.0% 1/8W R:FXD MET FLM 76.8K OHM 1% 1/8W R:FXD COMP 100K OHM 10% 1/4W R:FXD COMP 56K OHM 10% 1/4W R:FXD MET FLM 100K OHM 1% 1/8W	28480 28480 01121 01121 28480	0698-4526 0698-4507 CB 1041 CB 5631 0757-0465
A8R38 A8R39 A8R41 A8R42 A8R43	0684-2221 0757-0442 0757-0477 0698-4541 0757-0483	2	R:FXD COMP 2200 OHM 10% 1/4W R:FXD MET FLM 10.0K OHM 1% 1/8W R:FXD FLM 332K OHM 1.0% 1/8W R:FXD FLM 442K OHM 1.0% 1/8W R:FXD MET FLM 562K OHM 1% 1/8W	01121 28480 28480 28480 28480	C8 2221 0757-0442 0757-0477 0698-4541 0757-0483
A8R44 A8R45 A8R46 A8R47 A8R48	0684-1831 0684-1831 0684-1831 0684-1831 0684-1841		R:FXD COMP 18K OHM 10% 1/4W R:FXD COMP 10K OHM 10% 1/4W	01121 01121 01121 01121 01121	CB 1831 CB 1831 CB 1831 CB 1831 CB 1041
A8R49 A8R50 A8R51 A8R52 A8R53	0684-5631 0698-3161 0684-4731 0684-2221 0684-4731	2	R:FXD COMP 56K OHM 10% 1/4W R:FXD MET FLM 38.3K OHM 1% 1/8W R:FXD COMP 47K OHM 10% 1/4W R:FXD COMP 200 OHM 10% 1/4W R:FXD COMP 27K OHM 10% 1/4W	01121 28480 01121 01121 01121	CB 5631 0698-3161 CB 4731 CB 2221 CB 4731
A8R 54 A8R 55 A8R 56 A8R 57 A8R 58	0757-0449 0757-0465 0684-4731 0684-4731 0698-3519	3	R:FXD FLM 20K OHM 1% 1/8W R:FXD MET FLM 100K OHM 1% 1/8W R:FXD COMP 47K OHM 10% 1/4W R:FXD COMP 47K OHM 10% 1/4W R:FXD MET FLM 12.4K OHM 1% 1/8W	28480 28480 01121 01121 28480	0757-0449 0757-0465 CB 4731 CB 4731 0698-3519
A8R 59 A8R 61 A8R 62 A8R 63 A8R 64	0698-3228 0698-3149 0698-3266 0698-4532 0698-3460	2 1 1 1	R:FXD MET FLM 49.9K OHM 1% 1/8W R:FXD FLM 255K OHM 1% 1/8W R:FXD MET FLM 237K OHM 1% 1/8W R:FXD FLM 280K OHM 1% 1/8W R:FXD MET FLM 422K OHM 1% 1/8W	28480 28480 28480 28480 28480	0698-3228 0698-3149 0698-3266 0698-4532 0698-3460
A8R65 A8R66 A8R67 A8R68 A8R69	0698-7332 0698-4505 0757-0486 0757-0469 0684-1041	1 1 3	R:FXD FLM 1 MEGOHM 1.0% 1/8W R:FXD MET FLM 71.5K OHM 1% 1/8W R:FXD MET FLM 750K OHM 1% 1/8W R:FXD FLM 150K OHM 1% 1/8W R:FXD COMP 100K OHM 10% 1/4W	28 480 28 480 28 4 80 28 4 80 01 1 2 1	0698-7332 0698-4505 0757-0486 0757-0469 CB 1041
A 8R 71 A 8R 72 A 8R 73 A 8R 74 A 8R 75 A 8R 76 A 8R 77 A 8R 78 A 8R 95 A 8R 95 A 8R 96	0684-1041 0757-0394 0757-0273 0757-0284 0757-0282 0767-0283 0698-3161 0698-3149 0757-0283	2	R:FXD COMP 100K OHM 10% 1/4W R:FXD MET FLM 51.1 OHM 1% 1/8W R:FXD MET FLM 3.01K OHM 1% 1/8W R:FXD MET FLM 150 OHM 1% 1/8W R:FXD MET FLM 221 OHM 1% 1/8W R:FXD MET FLM 2.00K OHM 1% 1/8W R:FXD MET FLM 38.3K OHM 1% 1/8W R:FXD MET FLM 2.00K OHM 1% 1/8W R:FXD MET FLM 2.00K OHM 1% 1/8W R:FXD MET FLM 2.00K OHM 1% 1/8W R:FXD MET FLM 100 OHM 1% 1/8W	01121 28480 28480 28480 28480 28480 28480 28480 28480	CB 1041 0757-0394 0757-0273 0757-0284 0757-0282 0757-0283 0698-3161 0698-3149 0757-0283
A8U1, U2 A8U3 A8U4 A8U5 A8U6 A8U7	1826-0021 1826-0043 1826-0021 1826-0043 1826-0043		IC:VOLTAGE FOLLOWER 0 TO 70 C TO-99 IC:LINEAR OPERATIONAL AMPLIFIER IC:VOLTAGE FOLLOWER 0 TO 70 C TO-99 IC:LINEAR OPERATIONAL AMPLIFIER IC:LINEAR OPERATIONAL AMPLIFIER IC:OPERATIONAL AMPLIFIER	12040 28480 12040 28480 28480 07263	LM310H 1826-0043 LM310H 1826-0043 1826-0043 SL9940
A8U8	1820-0203		IC:OPERATIONAL AMPLIFIER	07263	SL8940
A9	03580-66509	1	BOARD ASSY: INPUT (FOR STD 3580A ONLY)	28480	03580-66509
A9C1 A9C2 A9G3	0170-0042 0121-0407 0121-0407	2 10	CAPACITOR-FXD .33UF+-5% 100WVDC CAPACITOR, VAR, TRMR, PSTN, .7/3PF CAPACITOR, VAR, TRMR, PSTN, .7/3PF	99515 72982 72982	E1-334D 536-016 536-016
A9C4 A9C5 A9C6 A9C7 A9C8	0121-0407 0121-0407 0121-0407 0150-0022 0140-0162	2	CAPACITOR, VAR, TRMR, PSTN, .7/3PF CAPACITOR, VAR, TRMR, PSTN, .7/3PF CAPACITOR, VAR, TRMR, PSTN, .7/3PF CAPACITOR-FXD 3.3PF+-10% 500WVDC CAPACITOR-FXD .0047UF-10% 300WVDC	72982 72982 72982 95121 72136	536-016 536-016 536-016 Type QC DM20F472K0300WV1CR

Model 3580A

Table 6-3. Replaceable Parts

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Numbe
A9C9 A9C10 A9C11 A9C12 A9C13	0150-0011 0160-2207 0150-0022 0150-0022 0160-0356	2 3	CAPACITOR-FXD 1.5PF+-20% 500WVDC CAPACITOR-FXD 300PF+-5% 300WVDC CAPACITOR-FXD 3.3PF+-10% 500WVDC CAPACITOR-FXD 3.3PF+-10% 500WVDC CAPACITOR-FXD 18PF+-5% 300WVDC	95121 28480 95121 95121 28480	TYPE QC 0160-2207 TYPE QC TYPE QC 0160-0356
A9C14 A9C15 A9C16 A9C17 A9C18	0150-0022 0150-0022 0180-0229 0180-0229 0140-0210	4	CAPACITOR-FXD 3.3PF+-10% 500WVDC CAPACITOR-FXD 3.3PF+-10% 500WVDC CAPACITOR-FXD; 33UF+-10% 10VDC TA-SOLID CAPACITOR-FXD; 33UF+-10% 10VDC TA-SOLID CAPACITOR-FXD 270PF+-5% 300WVDC	95121 95121 56289 56289 72136	TYPE QC TYPE QC 150D336X9010B2 150D336X9010B2 DM15F271J0300WV1CR
A9C19 A9C21 A9C22 A9C23 A9C24	0160-2198 0180-0060 0160-2204 0180-0197 0180-1758	4	CAPACITOR-FXD 20PF+-5% 300WVDC CAPACITOR-FXD; 200UF+75-10% 3VDC AL CAPACITOR-FXD 100PF+-5% 300WVDC CAPACITOR-FXD; 2-2UF+-10% 20VDC TA CAPACITOR-FXD; 300UF+75-10% 3VDC AL	28480 56289 28480 56289 56289	0160-2198 30D207G003CC2 0160-2204 150D225X9020A2 30D307G003DC2
A9C25 A9C26 A9C27 A9C28 A9C29	0180-0061 0180-1758 0180-0210 0140-0210 0180-0060		CAPACITOR-FXD; 100UF+75-10% 16VDC AL CAPACITOR-FXD; 300UF+75-10% 3VDC AL CAPACITOR-FXD; 3-3UF+-20% 15VDC TA CAPACITOR-FXD 270FF+-5% 300WVDC CAPACITOR-FXD; 200UF+75-10% 3VDC AL	56289 56289 56289 72136 56289	30D107G016DC2 30D307G003DC2 150D335X0015A2 DM15F271J0300WV1CR 30D207G003CC2
49C30 49C31 49C32 49C33 49C34	0160-2204 0160-0763 0180-1758 0180-0061 0180-0137	2	CAPACITOR-FXD 100PF+-5% 300WVDC CAPACITOR-FXD 5PF+-10% 500WVDC CAPACITOR-FXD; 300UF+75-10% 3VVDC AL CAPACITOR-FXD; 100UF+75-10% 16VVDC AL CAPACITOR-FXD; 100UF+-20% 10VDC TA	28480 28480 56289 56289 56289	0160-2204 0160-0763 300307G003DC2 300107G016DC2 150D107X0010R2
49C35 49C36 49C37 49C38 49C38	0160-2724 0140-0217 0160-3269 0160-0341 0160-3269	2 4 2	CAPACITOR FXD .0036UF - 2% 500WVDC CAPACITOR FXD 140PF - 2% 300WVDC CAPACITOR FXD .0076LUF - 1% 100WVDC CAPACITOR FXD .640PF - 1% 300WVDC CAPACITOR FXD .0076LUF - 1% 100WVDC	28480 72136 28480 28480 28480	0160-2724 DM15F141G0300WV1CR 0160-3269 0160-0341 0160-3269
19C41 19C42 19C43 19C44 19C45	0140-0233 0160-2230 0180-0303 0150-0093 0180-0374	2 2 2	CAPACITOR-FXD 480PF+-1% 300WVDC CAPACITOR-FXD -0033UF+-5% 300WVDC CAPACITOR-FXD; 100UF+75-10% 3VDC AL CAPACITOR-FXD -01UF+80-20% 100WVDC CAPACITOR-FXD; 10UF+-10% 20VDC TA-SOLID	72136 28480 56289 28480 56289	DM15F481F0300WV1C 0160-2230 30D107G003C82 0150-0093 1500106X902082
49C46 49C47 49C48 49C49 49C51	0150-0093 0180-0197 0160-2605 0150-0093 0160-2035	2	CAPACITOR+FXD .01UF+80-20% 100WVDC CAPACITOR-FXD; 2.2UF+-10% 20VDC TA CAPACITOR-FXD .02UF+80-20% 25WVDC CAPACITOR-FXD .01UF+80-20% 100WVDC CAPACITOR-FXD 750PF+-5% 300WVDC	28480 56289 28480 28480 28480	0150-0093 150D225X9020A2 0160-2605 0150-0093 0160-2035
A9C52 A9C53 A9C54 A9C55 A9C56	0180-0197 0150-0093 0180-0197 0160-2009 0180-0197		CAPACITOR-FXD; 2.2UF++10% 20VDC TA CAPACITOR-FXD .01UF+80-20% 100MVDC CAPACITOR-FXD; 2.2UF+-10% 20VDC TA CAPACITOR-FXD 820FF+-5% 300MVDC CAPACITOR-FXD; 2.2UF+-10% 20VDC TA	56289 28480 56289 28480 56289	1500225X9020A2 0150-0093 1500225X9020A2 0160-2009 1500225X9020A2
A 9C 57 A 9C 58 A 9C 59 A 9C 61 A 9C 62	0150-0093 0150-0093 0180-0197 0180-0228 0180-0197		CAPACITOR-FXD .01UF+80-20% 100MVDC CAPACITOR-FXD .01UF+80-20% 100MVDC CAPACITOR-FXD; 2-2UF+-10% 20VDC TA CAPACITOR-FXD; 2-2UF+-10% 15VDC TA-SOLID CAPACITOR-FXD; 2-2UF+-10% 20VDC TA	28480 28480 56289 56289 56289	0150-0093 0150-0093 150D225X9020A2 150D226X9015B2 150D225X9020A2
49C63 49C64 49C65 49C66 49CR1	0180-0339 0180-0197 0180-0228 0180-0339 1901-0040	5	CAPACITOR-FXD; 50UF+75-10% 16VDC AL CAPACITOR-FXD; 2-2UF+-10% 20VDC TA CAPACITOR-FXD; 22UF+-10% 15VDC TA-SOLID CAPACITOR-FXD; 50UF+75-10% 16VDC AL DIODE; SWITCHING; ; 30V MAX VRM 50MA	56289 56289 56289 56289 28480	30D506G016CB2 150D225X9020A2 150D226X9015B2 30D506G016CB2 1901-0040
A9CR2 A9CR3 A9CR4 A9CR5 A9J1	1901-0040 1901-0040 1901-0040 1901-0040 1251-2969	3	DIODE; SWITCHING; ; 30V MAX VRM 50MA DIODE; SWITCHING; ; 30V MAX VRM 50MA DIODE; SWITCHING; ; 30V MAX VRM 50MA DIODE; SWITCHING; ; 30V MAX VRM 50MA CONNECTOR:PHONO, SINGLE JACK	28480 28480 28480 28480 27264	1901-0049 1901-0040 1901-0040 1901-0040 15-24-0501
1919 1919 1911 1912 1913	1251-3361 1251-3361 9100-3264 9100-3259 9100-3260	2 2 2	CCNNECTOR, 10-CONT, FEM, POST TYPE CONNECTOR, 10-CONT, FEM, POST TYPE COIL:FXD 2=34 MH 2% TRANSFORMER TRANSFORMER	27264 27264 28480 28480 28480	09-52-3102 09-52-3102 9100-3264 9100-3259 9100-3260
49L4 49L5 49MP1 49MP2 49MP3	9100-3277 9170-0894 03580-01204 03580-01205 03580-21701	2 2 2	INDUCTOR: POT CORE  CORE; MAG; SHIELDING BEAD; -138 OD -047  BRACKET: INPUT SWITCH  BRACKET: IF SWITCH  BUSHING: DIAL	28480 02114 28480 28480 28480	9100-3277 56-590-65/4A6 03580-01204 03580-01205 03580-21701
19MP4 19Q1 19Q2 19Q3	03580-23201 1855-0377 1854-0226 1853-0086 1854-0071	2 2 4	COUPLER:SHAFT TRANSISTOR; J-FET N-CHAN, D-MODE SI TRANSISTOR NPN 2N4384 SI PD=500MW TRANSISTOR NPN SI CHIP PD=310MW TRANSISTOR NPN SI PD=300MW FT=200MHZ	28480 28480 28480 28480 28480	03580-23201 1855-0377 1854-0226 1853-0086 1854-0071

Table 6-3. Replaceable Parts

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A 5Q 5 A 9Q 6 A 9Q 7 A 9Q 8 A 9Q 9	1854-0971 1854-0226 1853-0086 1854-0071 1854-0071		TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN 2N4384 SI PD=500MW TRANSISTOR PNP SI CHIP PD=310MW TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ	28480 28480 28480 28480 28480	1854-0071 1854-0226 1853-0086 1854-0071
A9Q11 A9Q12 A9Q13 A9Q14 A9Q15	1854-0071 1854-0071 1854-0071 1854-0226 1853-0010		TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN 2N4384 SI PD=500MW TRANSISTOR PNP SI CHIP PD=360MW	28480 28480 28480 28480 28480	1854-0071 1854-0071 1854-0071 1854-0226 1853-0010
ASQ16 ASQ17 ASQ18 ASQ19 ASR1	1854-0071 1854-0071 1854-0071 1853-0010 2100-0580	2	TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI CHIP PD=360MW RESISTOR, VAR, TRMR, 500K0HM 10% C	28480 28480 28480 28480 73138	1854-0071 1854-0071 1854-0071 1853-0010 72PR500K
A9R2 A9R3 A9R4 A9R5 A9R6	2100-0640 0698-5159 0698-4055 0698-5132 0757-0271	2 4 4 4	RESISTOR; VAR; 5K 10% SPST SW RESISTOR-FXD 1M -5% _25W F TUBULAR RESISTOR-FXD 1K _25% -125W F TUBULAR RESISTOR-FXD 990K -5% _25W F TUBULAR RESISTOR-FXD 124K 1% _125W F TUBULAR	28480 19701 03888 19701 24546	2100-0640 MF52C1/4-T0-1004-D PME55-1/8-T0-1001-C MF52C1/4-T0-9903-D C4-1/8-T0-1243-F
A9R7 A9R8 A9R9 A9R11 A9R12	0698-6661 0698-5132 0698-5131 0698-6659 0698-5131	2 4 2	RESISTOR-FXD 11.11K .25% .125M F RESISTOR-FXD 990K .5% .25M F TUBULAR RESISTOR-FXD 900K .5% .25M F TUBULAR RESISTOR-FXD 127K .25% .125M F TUBULAR RESISTOR-FXD 900K .5% .25M F TUBULAR	19701 19701 19701 19701 19701	MF4C1/8-T0-11111-C MF52C1/4-T0-9903-D MF52C1/4-T0-9003-D MF4C1/8-T0-1273-C MF52C1/4-T0-9003-D
A9R13 A9R14 A9R15 A9R16 A9R17	0757-0430 0698-3150 0698-5159 0757-0824 0684-1041	1 2	RESISTOR-FXD 2-21K 1% -125W F TUBULAR RESISTOR-FXD 2-37K 1% -125W F TUBULAR RESISTOR-FXD 1M -5% -25W F TUBULAR RESISTOR-FXD 2K 1% -5W F TUBULAR RESISTOR-FXD 100K 10% -25W CC TUBULAR	24546 16299 19701 30983 01121	C4-1/8-T0-2211-F C4-1/8-T0-2371-F MF52C1/4-T0-1004-D MF7C1/2-T0-2001-F C81041
A9R 18, A9R 19 A9R 20 A9R 21 A9R 22 A9R 23	C698-3581 0684-1021 C698-4473 0757-0442 0698-4421	4	RESISTOR-FXD 13.7K 1% .125W F TUBULAR RESISTOR-FXD 1K 10% .25W CC TUBULAR RESISTOR-FXD 8.06K 1% .125W F TUBULAR RESISTOR-FXD 10K 1% .125W F TUBULAR RESISTOR-FXD 249 0HM 1% .125W F TUBULAR	16299 01121 24546 24546 16299	C4-1/8-T0-1372-F CB1021 C4-1/8-T0-8061-F C4-1/8-T0-1002-F C4-1/8-T0-249R-F
A9R 24 A9R 25 A9R 26 A9R 27 A9R 28	0698-3193 0698-6362 0698-4486 0698-3382 0757-0407	2	RESISTOR-FXD 10K .25% .125W F TUBULAR RESISTOR-FXD 1.153K .25% .125W F RESISTOR-FXD 24.9K 1% .125W F TUBULAR RESISTOR-FXD 5.49K 1% .125W F TUBULAR RESISTOR-FXD 200 DHM 1% .125W F TUBULAR	19701 19701 24546 16299 24546	MF4C1/8-C-1002-C MF4C1/8-T2-1153R-C C4-1/8-T0-2492-F C4-1/8-T0-5491-F C4-1/8-T0-201-F
A9R 29 A9R 31 A9R 32 A9R 33 A9R 34	0698-4464 0684-1041 0757-0448 0684-4701 0757-0407	2	RESISTOR-FXD 887 DHM 1% .125W F TUBULAR RESISTOR-FXD 100K 10% .25W CC TUBULAR RESISTOR-FXD 18.2K 1% .125W F TUBULAR RESISTOR-FXD 47 OHM 10% .25W CC TUBULAR RESISTOR-FXD 200 DHM 1% .125W F TUBULAR	24546 01121 24546 01121 24546	C4-1/8-T0-887R-F C81041 C4-1/8-T0-1822-F C84701 C4-1/8-T0-201-F
A9R 35 A9R 36 A9R 37 A9R 38 A9R 39	0698-3488 0684-1041 0757-0442 0757-0278 0698-6780	4 2	RESISTOR-FXD 442 DHM 1% .125W F TUBULAR RESISTOR-FXD 100K 10% .25W CC TUBULAR RESISTOR-FXD 10K 1% .125W F TUBULAR RESISTOR-FXD 1.78K 1% .125W F TUBULAR RESISTOR-FXD 5.62K .25% .125W F TUBULAR	16299 01121 24546 24546 19701	C4-1/8-T0-422R-F CB1041 C4-1/8-T0-1002-F C4-1/8-T0-1781-F MF4C1/8-T2-5621-C
A9R40 A9R41 A9R42 A9R43 A9R44	0684-1041 0698-6823 0698-4473 0698-3495 0757-0424	2	RESISTOR-FXD 100K 10% .25W CC TUBULAR RESISTOR-FXD 2.61K .25% .125W F TUBULAR RESISTOR-FXD 8.06K 1% .125W F TUBULAR RESISTOR-FXD 8.66 DHM 1% .125W F TUBULAR RESISTOR-FXD 1.1K 1% .125W F TUBULAR	01121 19701 24546 16299 24546	C81041 MF4C1/8-T0-2611-C C4-1/8-T0-8061-F C4-1/8-T0-866R-F C4-1/8-T0-1101-F
A9R45 A9R46 A9R47 A9R48 A9R49	0757-0442 0757-0442 0698-3154 0757-0407 0698-4483	1	RESISTOR-FXD 10K 1% -125W F TUBULAR RESISTOR-FXD 10K 1% -125W F TUBULAR RESISTOR-FXD 4-22K 1% -125W F TUBULAR RESISTOR-FXD 200 OHM 1% -125W F TUBULAR RESISTOR-FXD 18-7K 1% -125W F TUBULAR	24546 24546 16299 24546 24546	C4-1/8-T0-1002-F C4-1/8-T0-1002-F C4-1/8-T0-4221-F C4-1/8-T0-201-F C4-1/8-T0-1872-F
A9R 50 A9R 51 A9R 52 A9R 53 A9R 54	0684-1021 0698-4421 0684-1041 0757-0278 0757-0407		RESISTOR-FXD 1K 10% .25W CC TUBULAR RESISTOR-FXD 249 OHM 1% .125W F TUBULAR RESISTOR-FXD 100K 10% .25W CC TUBULAR RESISTOR-FXD 1.78K 1% .125W F TUBULAR RESISTOR-FXD 200 OHM 1% .125W F TUBULAR	01121 16299 01121 24546 24546	CB1021 C4-1/3-T0-249R-F CB1041 C4-1/8-T0-1781-F C4-1/8-T0-201-F
A9R55 A9R56 A9R57 A9R58 A9R58	0698-3327 0698-4518 0698-4492 0698-4055 0698-3497	2 3	RESISTOR-FXD 3-92K -5% -125W F TUBULAR RESISTOR-FXD 137K 1% -125W F TUBULAR RESISTOR-FXD 32-4K 1% -125W F TUBULAR RESISTOR-FXD 1K -25% -125W F TUBULAR RESISTOR-FXD 6-04K 1% -125W F TUBULAR	03888 24546 24546 03888 16299	PME55-1/8-T0-3921-D C4-1/8-T0-1373-F C4-1/8-T0-3242-F PME55-1/8-T0-1001-C C4-1/8-T0-604R-F
A9R61 A9R62 A9R63 A9R64 A9R65	069 E-4488 0698-7417 0757-0407 0757-0442 0757-0161	2	RESISTOR-FXD 26.7K 1% .125W F TUBULAR RESISTOR-FXD 69.8K .25% .125W F TUBULAR RESISTOR-FXD 200 0HM 1% .125W F TUBULAR RESISTOR-FXD 10K 1% .125W F TUBULAR RESISTOR-FXD 604 0HM 1% .125W F TUBULAR	24546 30983 24546 24546 24546	C4-1/8-T0-2672-F MF4C1/8-T0-6982-C C4-1/8-T0-201-F C4-1/8-T0-1002-F C4-1/8-T0-604R-F

Table 6-3. Replaceable Parts

Reference Designation	HP Part Number	Oty	Description	Mfr Code	Mfr Part Number
A9R66 A9R67 A9R68 A9R69 A9R71	0698-4422 0757-0283 0757-0976 0698-4202 0757-0438	2 4 4	RESISTOR-FXD 1.27K 1% .125W F TUBULAR RESISTOR-FXD 2K 1% .125W F TUBULAR RESISTOR-FXD 150K 2% .125W F TUBULAR RESISTOR-FXD 8.07K 1% .125W F TUBULAR RESISTOR-FXD 5.11K 1% .125W F TUBULAR	16299 24546 24546 16299 24546	C4-1/8-T0-1271-F C4-1/8-T0-2001-F C4-1/8-T0-1502-G C4-1/8-T0-8871-F C4-1/8-T0-5111-F
A9R72 A9R73 A9R74 A9R75 A9R76	0757-0283 0698-4202 0757-0976 0757-0453 0757-0438		RESISTOR-FXD 2K 1% .125W F TUBULAR RESISTOR-FXD 8.87K 1% .125W F TUBULAR RESISTOR-FXD 150K 2% .125W F TUBULAR RESISTOR-FXD 30-1K 1% .125W F TUBULAR RESISTOR-FXD 5.11K 1% .125W F TUBULAR	24546 16299 24546 24546 24546	C4-1/8-T0-2001-F C4-1/8-T0-8871-F C4-1/8-T0-1502-G C4-1/8-T0-3012-F C4-1/8-T0-5111-F
A9R77 A9R78 A9R79 A9R81 A9R82	0757-0438 0684-1021 0757-0434 0698-3437 0698-3437	6	RESISTOR-FXD 5.11K 1% .125W F TUBULAR RESISTOR-FXD 1K 10% .25W CC TUBULAR RESISTOR-FXD 3.05K 1% .125W F TUBULAR RESISTOR-FXD 133 OHM 1% .125W F TUBULAR RESISTOR-FXD 133 OHM 1% .125W F TUBULAR	24546 01121 24546 16299 16299	C4-1/8-T0-5111-F CB1021 C4-1/8-T0-3651-F C4-1/8-T0-133R-F C4-1/8-T0-133R-F
A 9R 83 A 9R 84 A 9R 85 A 9R 86 A 9R 87	0698-3437 0757-0404 0757-0404 0757-0404 0757-0404	8	RESISTOR-FXD 133 OHM 1% .125W F TUBULAR RESISTOR-FXD 130 OHM 1% .125W F TUBULAR	16299 24546 24546 24546 24546	C4-1/8-T0-133R-F C4-1/8-T0-131-F C4-1/8-T0-131-F C4-1/8-T0-131-F C4-1/8-T0-131-F
A 9R 88 A 9R 89 A 9R 91 A 9R 92 A 9R 93	0698-3446 0757-0438 0757-0161 0698-4441 0698-4020	2	RESISTOR-FXD 383 OHM 1% .125W F TUBULAR RESISTOR-FXD 5.11K 1% .125W F TUBULAR RESISTOR-FXD 604 OHM 1% .125W F TUBULAR RESISTOR-FXD 3.74K 1% .125W F TUBULAR RESISTOR-FXD 9.53K 1% .125W F TUBULAR	16299 24546 24546 16299 16299	C4-1/8-T0-383R-F C4-1/8-T0-5111-F C4-1/8-T0-604R-F C4-1/8-T0-3741-F C4-1/8-T0-9531-F
A 9R 94 A 9R 95 A 9R 96 A 9R 97 A 9R 98	0757-0435 0757-0161 0757-0435 0757-0280 0698-4486	6	RESISTOR-FXD 3.92K 1% .125M F TUBULAR RESISTOR-FXD 604 OHM 1% .125M F TUBULAR RESISTOR-FXD 3.92K 1% .125M F TUBULAR RESISTOR-FXD 1K 1% .125M F TUBULAR RESISTOR-FXD 24.9K 1% .125M F TUBULAR	24546 24546 24546 24546 24546	C4-1/8-T0-3921-F C4-1/8-T0-604R-F C4-1/8-T0-3921-F C4-1/8-T0-1001-F C4-1/8-T0-2492-F
A9R 99 A 9R 101 A 9R 102 A 9R 103 A 9R 104	0757-0280 0698-4486 0757-0271 0757-0161 0757-0401		RESISTOR-FXD 1K 1% -125W F TUBULAR RESISTOR-FXD 24.9K 1% -125W F TUBULAR RESISTOR-FXD 124K 1% -125W F TUBULAR RESISTOR-FXD 604 0HM 1% -125W F TUBULAR RESISTOR-FXD 100 0HM 1% -125W F TUBULAR	24546 24546 24546 24546 24546	C4-1/8-T0-1001-F C4-1/8-T0-2492-F C4-1/8-T0-1243-F C4-1/8-T0-004R-F C4-1/8-T0-101-F
A 9R 105 A 9R 106 A 9R 107 A 9R 108 A 9R 109	0757-0438 0757-0435 0698-3158 0684-1021 0757-0422	2	RESISTOR-FXD 5-11K 1% -125M F TUBULAR RESISTOR-FXD 3-92K 1% -125M F TUBULAR RESISTOR-FXD 23-7K 1% -125M F TUBULAR RESISTOR-FXD 1K 10% -25M CC TUBULAR RESISTOR-FXD 909 OHM 1% -125M F TUBULAR	24546 24546 16299 01121 24546	C4-1/8-T0-5111-F C4-1/8-T0-3921-F C4-1/8-T0-2372-F C81021 C4-1/8-T0-909R-F
A9R111 A9R112 A9R113 A9R114 A9R115	0698-4441 0757-0413 0683-2045 0684-1041 0684-1031	2 4	RESISTOR-FXD 3-74K 1% .125W F TUBULAR RESISTOR-FXD 392 OHM 1% -125W F TUBULAR RESISTOR-FXD 100K 10% .25W CC TUBULAR RESISTOR-FXD 100K 10% .25W CC TUBULAR RESISTOR-FXD 10K 10% .25W CC TUBULAR	16299 24546 01121 01121 01121	C4-1/8-T0-3741-F C4-1/8-T0-392R-F CB2045 CB1041 CB1031
A9R116 A9R117 A9R118 ASR119 A9R121	0698-3557 0683-2045 0684-1041 0684-1031 0698-3153	2	RESISTOR-FXD 806 OHM 1% .125W F TUBULAR RESISTOR-FXD 200K 5% .25W CC TUBULAR RESISTOR-FXD 100K 10% .25W CC TUBULAR RESISTOR-FXD 10K 10% .25W CC TUBULAR RESISTOR-FXD 3.83K 1% .125W F TUBULAR	16299 01121 01121 01121 16290	C4-1/8-T0-806R-F CB2045 CB1041 CB1031 C4-1/8-T0-3831-F
A9R 122 A9R 123 A9R 124 A9R 125 A9R 126	0684-1011 0684-4701 0684-1021 0757-0438 0684-4711	4	RESISTOR-FXO 100 DHM 10% .25W CC RESISTOR-FXD 47 OHM 10% .25W CC TUBULAR RESISTOR-FXD 1k 10% .25W CC TUBULAR RESISTOR-FXD 5.11k 1% .125W F TUBULAR RESISTOR-FXD 470 OHM 10% .25W CC	01121 01121 01121 24546 01121	CB1011 CB4701 CB1021 C4-1/8-T0-5111-F CB4711
ASR 127 A 9R 128 A SR 129 A 9R 131 A 9R 132	0757-0462 0684-1021 0684-4701 0757-0438 0684-4711	4	RESISTOR-FXD 75K 1% -125W F TUBULAR RESISTOR-FXD 1K 10% -25W CC TUBULAR RESISTOR-FXD 47 OHM 10% -25W CC TUBULAR RESISTOR-FXD 5-11K 1% -125W F TUBULAR RESISTOR-FXD 470 OHM 10% -25W CC	24546 01121 01121 24546 01121	C4-1/8-T0-7502-F CB1021 CB4701 C4-1/8-T0-5111-F CB4711
A <sup>9</sup> R133 A9S1	0757-0462 03580-61905	2	RESISTOR-FXD 75K 1% -125W F TUBULAR SWITCH ASSY	24546 28480	C4-1/8-T0-7502-F 03580-61905
A952 A9U1	3100-2738 1826-0044	2 2	(INCLUDES R2) SWITCH:RUTARY IC;LIN;OPERATIONAL AMPLIFIER	28480 07263	3100-2738 739DC
A9U2	1820-0427		IC;LIN;BALANCED MODULATOR	04713	MC1496G
A 9	03580-66519	1	BOARD ASSY: INPUT (FOR OPTION 002 ONLY)	28480	03580-66519
A9 C1 A9 C2 A9 C3	0170-0042 0121-0407 0121-0407		CAPACITOR-FXD ~33UF+-5% 100WVDC CAPACITOR, VAR, TRMR, PSTN, ~7/3PF CAPACITOR, VAR, TRMR, PSTN, ~7/3PF	99515 72982 72982	E1-3340 536-016 536-016

Table 6-3. Replaceable Parts

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A9 C4 A9 C5 A9 C6 A9 C7 A9 C8	0121-0407 0121-0407 0121-0407 0150-0022 0140-0162		CAPACITOR, VAR, TRMR, PSTN, .7/3PF CAPACITOR, VAR, TRMR, PSTN, .7/3PF CAPACITOR, VAR, TRMR, PSTN, .7/3PF CAPACITOR-FXD 3.3PF+10% 500WVDC CAPACITOR+FXD .0047UF+10% 300WVDC	72982 72982 72982 72982 95121 72136	536-016 536-016 536-016 Type QC DM20F472K03JUWV1CR
A9 C9 A9 C10 A9 C11 A9 C12 A9 C13	0150-0011 0160-2207 0150-0022 0150-0022 0160-0356		CAPACITOR-FXD 1.5PF+-20% 500WVDC CAPACITOR-FXD 300PF+-5% 300WVDC CAPACITOR-FXD 3.3PF+-10% 500WVDC CAPACITOR-FXD 3.3PF+-10% 500WVDC CAPACITOR-FXD 18PF+-5% 300WVDC	95121 28480 95121 95121 28480	TYPE QC 0160-2207 TYPE QC TYPE QC 0160-0356
A9 C14 A9 C15 A9 C16 A9 C17 A9 C18	C150-0022 0150-0022 0180-0229 0180-0229 0140-0210		CAPACITOR-FXD 3.3PF+-10% 500WVDC CAPACITOR-FXD 3.3PF+-10% 500WVDC CAPACITOR-FXD; 33UF+-10% 10VDC TA-SOLID CAPACITOR-FXD; 33UF+-10% 10VDC TA-SOLID CAPACITOR-FXD 270PF+-5% 300WVDC	95121 95121 56289 56289 72136	TYPE QC TYPE QC 150D336X9010B2 150D336X9010B2 DM15F271J0300WV1CR
A9 C19 A9 C21 A9 C22 A9 C23 A9 C24	0160-2198 0180-0060 0166-2204 0180-0197 0180-1758		CAPACITOR-FXD 20PF+-5% 300WVDC CAPACITOR-FXD; 200UF+75-10% 3VDC AL CAPACITOR-FXD 100PF+-5% 300WVDC CAPACITOR-FXD; 2.2UF+-10% 20VDC TA CAPACITOR+FXD; 300UF+75-10% 3VDC AL	28480 56289 28480 56289 56289	0160-2198 30D207G003CC2 0160-2204 150D225X9020A2 30D307G003DC2
A9 C25 A9 C26 A9 C27 A9 C28 A9 C29	0180-0061 0180-1758 0180-0210 0140-0210 0180-0060		CAPACITOR-FXD; 100UF+75-10% 16VDC AL CAPACITOR-FXD; 300UF+75-10% 3VDC AL CAPACITOR-FXD; 3.3UF+-20% 15VDC TA CAPACITOR-FXD; 270PF4-5% 300WVDC CAPACITOR-FXD; 200UF+75-10% 3VDC AL	56289 56289 56289 72136 56289	30D107G016DC2 30D307G003DC2 150D335X0015A2 DM15F271J0300WV1CR 30D207G003CC2
A9 C30 A9 C31 A9 C32 A9 C33 A9 C34	016C-22O4 0160-0763 0180-1758 0180-0061 0180-0137		CAPACITOR-FXD 100PF+-5% 300WVDC CAPACITOR-FXD 5PF+-10% 500WVDC CAPACITOR-FXD; 300UF+75-10% 3VDC AL CAPACITOR-FXD; 100UF+75-10% 16VDC AL CAPACITOR+FXD; 100UF+-20% 10VDC TA	28480 28480 56289 56289 56289	0160-2204 0160-0763 30D307G003DC2 30D107G016DC2 150D107X0019R2
A9 C35 A9 C36 A9 C37 A9 C38 A9 C39	0160-2724 0140-0217 0160-3269 0160-0341 0160-3269		CAPACITOR-FXD →0036UF←-2% 500WVDC CAPACITOR+FXD 140PF+-2% 300WVDC CAPACITOR+FXD →00761UF+-1% 100WVDC CAPACITOR-FXD →640PF+-1% 300WVDC CAPACITOR-FXD →00761UF+-1% 100WVDC	28480 72136 28480 28480 28480	0160-2724 DM15F141G0300WV1CR 0160-3269 0160-0341 0160-3269
A9 C41 A9 C42 A9 C43 A9 C44 A9 C45	0140-0233 0160-2230 0180-0303 0150-0093 0180-0374		CAPACITCR+FXD 480PF+-1% 300MVDC CAPACITCR-FXD *0033UF+-5% 300MVDC CAPACITCR-FXD; 100UF+75-10% 3VDC AL CAPACITCR-FXD *01UF+80-20% 100MVDC CAPACITOR-FXD; 10UF+-10% 20VDC TA-SOLID	72136 28480 56289 28480 56289	DM15F481F0300WV1C 0160-2230 30D107G003CB2 0150-0093 150D106X9020B2
A9 C46 A9 C47 A9 C48 A9 C49 A9 C51	015C-0093 0180-0197 0160-2605 0150-0093 0160-2035		CAPACITCR-FXD -01UF+80-20% 100HVDC CAPACITOR-FXD; 2.2UF+-10% 20VDC TA CAPACITOR-FXD -02UF+80-20% 25WVDC CAPACITOR-FXD -01UF+80-20% 100HVDC CAPACITOR-FXD 750PF4-5% 300WVDC	28480 56289 28480 28480 28480	0150-0093 150D225X9020A2 0160-2605 0150-0093 0160-2035
A9 C52 A9 C53 A9 C54 A9 C55 A9 C56	0180-0197 0150-0093 0180-0197 0160-2009 0180-0197		CAPACITOR-FXC; 2.2UF++10% 20VDC TA CAPACITOR-FXD .01UF+80-20% 100WVDC CAPACITOR-FXD; 2.2UF+-10% 20VDC TA CAPACITOR-FXD 820FF+-5% 300WVDC CAPACITOR-FXC; 2.2UF+-10% 20VDC TA	56289 28480 56289 28480 56289	150D225X9020A2 0150-0093 150D225X9020A2 0160-2009 150D225X9020A2
A9 C57 A9 C58 A9 C59 A9 C61 A9 C62	0150-0093 0150-0093 0180-0197 0180-0228 0180-0197		CAPACITOR-FXD .01UF+80-20% 100WVDC CAPACITOR-FXD .01UF+80-20% 100WVDC CAPACITOR-FXD; 2.2UF+-10% 20VDC TA CAPACITOR-FXD; 2.2UF+-10% 15VDC TA-SOLID CAPACITOR-FXD; 2.2UF+-10% 20VDC TA	28480 28480 56289 56289 56289	0150-0093 0150-0093 1500225X9920A2 1500226X9015B2 1500225X9020A2
A9 C63 A9 C64 A9 C65 A9 C66 A9 CR1	0180-0339 0180-0197 0180-0228 0180-0339 1901-0040		CAPACITOR+FXD; 50UF+75-10% 16VDC AL CAPACITOR-FXD; 2.2UF+-10% 20VDC TA CAPACITOR-FXD; 22UF+-10% 15VDC TA-SOLID CAPACITOR-FXD; 50UF+75-10% 16VDC AL DIDDE; SWITCHING; ; 30V MAX VRM 50MA	56289 56289 56289 56289 28480	30D506G016CB2 150D225X902DA2 150D226X9015B2 30D506G016CB2 1901-0040
A9 CR2 A9 CR3 A9 CR4 A9 CR5 A9 CR7	1901-0040 1901-0040 1901-0040 1901-0040 1901-0040		DIODE; SWITCHING; ; 30V MAX VRM 50MA	28480 28480 28480 28480 28480	1901-0040 1901-0040 1901-0040 1901-0040 1901-0040
A9 J1 A9 J9 A9 J9 A9 L1 A9 L2	1251-2969 1251-3361 1251-3361 9100-3264 9100-3259		CONNECTOR: PHONG, SINGLE JACK CONNECTOR: 10-CONT, FEM, POST TYPE CONNECTOR: 10-CONT, FEM, POST TYPE COIL:FXD 2-34 MH 2% TRANSFORMER	27264 27264 27264 28480 28480	15-24-0501 09-52-3102 09-52-3102 9100-3264 9100-3259
A9 L3 A9 L4 A9 L5 A9 MP1 A9 MP2	9100-3260 9100-3277 9170-0894 03580-01204 03580-01205		TRANSFORMER INDUCTOR: POT CORE CORE: MAG; SHIELDING BEAD; -138 OD -047 BRACKET: INPUT SWITCH BRACKET: IF SWITCH	28480 28480 02114 28480 28480	9100-3260 9100-3277 56-590-65/4A6 03580-01204 03580-01205

Model 3580A Section VI

Table 6-3. Replaceable Parts

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A9 MP3 A9 MP4 A9 Q1 A9 Q2 A9 Q3	C3580-21701 0358C-23201 1855-0377 1854-0226 1853-0086		BUSHING:DIAL COUPLER:SHAFT TRANSISTOR; J-FET N-CHAN, D-MODE SI TRANSISTOR NPN 2N4384 SI PD=500MW TRANSISTOR PNP SI CHIP PD≈310MW	28480 28480 28480 28480 28480	03580-21701 03580-23201 1855-0377 1854-0226 1853-0086
A9 Q4 A5 Q5 A9 Q6 A9 Q7 A9 Q8	1854-0071 1854-0071 1854-0226 1853-0086 1854-0071		TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN 2N4384 SI PD=500MW TRANSISTOR NPN SI CHIP PD=310MW TRANSISTOR NPN SI PD=300MW FT=200MHZ	28480 28480 28480 28480 28480	1854-0071 1854-0071 1854-0226 1853-0086 1854-0071
A9 Q9 A9 Q11 A9 Q12 A9 Q13 A9 Q14	1854-0071 1854-0071 1854-0071 1854-0071 1854-0226		TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN 2N4384 SI PD=500MW	28480 28480 28480 28480 28480	1854-0071 1854-0071 1854-0071 1854-0071 1854-0226
A9 Q15 A9 Q16 A9 Q17 A9 Q18 A9 Q19	1853-0010 1854-0071 1854-0071 1854-0071 1853-0010		TRANSISTOR PNP SI CHIP PD=360MW TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR PNP SI CHIP PD=360MW	28480 28480 28480 28480 28480	1853-0010 1854-0071 1854-0071 1854-0071 1853-0010
A9 R1 A9 R2 A9 R3 A9 R4 A9 R5	2100-0580 2100-0640 0698-5159 0698-4055 0698-5132		RESISTOR, VAR, TRMR, 500KOHM 10% C RESISTOR; VAR; 5K 10% SPST SW RESISTOR-FXD 1M -5% -25W F TUBULAR RESISTOR-FXD 1K -25% -125W F TUBULAR RESISTOR-FXD 990K -5% -25W F TUBULAR	73138 28480 19701 03888 19701	72PR530K 2100-0640 MF52C1/4-T0-1004-D PME55-1/8-T0-1001-C MF52C1/4-T0-9903-D
A5 R6 A9 R7 A9 R8 A9 R9 A9 R10	0757-0271 C698-6661 0698-5132 C698-5131 0698-3359	1	RESISTOR-FXD 124K 1% -125W F TUBULAR RESISTOR-FXD 11-11K -25% -125W F RESISTOR-FXD 990K -5% -25W F TUBULAR RESISTOR-FXD 900K -5% -25W F TUBULAR RESISTOR-FXD 12-7K 1% -125W F TUBULAR	24546 19701 19701 19701 16299	C4-1/8-T0-1243-F MF4C1/8-T0-11111-C MF52C1/4-T0-9903-D MF52C1/4-T0-9003-D C4-1/8-T0-1272-F
A9 R11 A9 R12 A9 R13 A9 R14 A9 R15	C698-6659 C698-5131 0757-0430 0698-4437 0698-5159	1	RESISTOR-FX0 127K -25% -125W F TUBULAR RESISTOR-FXD 900K -5% -25W F TUBULAR RESISTOR-FXD 2-21K 1% -125W F TUBULAR RESISTOR-FXD 2-94K 1% -125W F TUBULAR RESISTOR-FXD 1M -5% -25W F TUBULAR	19701 19701 24546 16299 19701	MF4C1/8-T0-1273-C MF52C1/4-T0-9003-D C4-1/8-T0-2211-F C4-1/8-T0-2941-F MF52C1/4-T0-1004-D
A9 R16 A9 R17 A9 R18, A9 R19 A9 R20 A9 R21	0757-0824 0684-1041 0698-3581 0684-1021 0698-4473		RESISTOR-FXD 2K 1% -5M F TUBULAR RESISTOR-FXD 100K 10% -25M CC TUBULAR RESISTOR-FXD 13-7K 1% -125M F TUBULAR RESISTOR-FXD 1K 10% .25W CC TUBULAR RESISTOR-FXD 8-06K 1% -125M F TUBULAR	30983 01121 16299 01121 24546	MF7C1/2-T0-2001-F C81041 C4-1/8-T0-1372-F CB1021 C4-1/8-T0-8061-F
A9 R22 A9 R23 A9 R24 A9 R25 A9 R26	0757-0442 C698-4421 0698-3193 0698-6862 0698-4486		RESISTOR-FXD 10K 1% -125W F TUBULAR RESISTOR-FXD 249 OHM 1% -125W F TUBULAR RESISTOR-FXD 10K -25% -125W F TUBULAR RESISTOR-FXD 1-155K -25% -125W F RESISTOR-FXD 24-9K 1% -125W F TUBULAK	24546 16299 19701 19701 24546	C4-1/8-T0-1002-F C4-1/8-T0-249R-F MF4C1/8-C-1002-C MF4C1/8-T2-1153R-C C4-1/8-T0-2492-F
A9 R27 A9 R28 A9 R29 A9 R31 A9 R32	C698-3382 0757-0407 C698-4464 0684-1041 0757-0448		RESISTOR-FXD 5.49K 1% .125M F TUBULAR RESISTOR-FXD 200 OHM 1% .125M F TUBULAR RESISTOR-FXD 887 OHM 1% .125M F TUBULAR RESISTOR-FXD 100K 10% .25M CC TUBULAR RESISTOR-FXD 18.2K 1% .125M F TUBULAR	16299 24546 24546 01121 24546	C4-1/8-T0-5491-F C4-1/8-T0-201-F C4-1/8-T0-887R-F C81041 C4-1/8-T0-1822-F
A9 R33 A9 R34 A9 R35 A9 R36 A9 R36	0684-4701 0757-0407 0698-3488 0684-1041 0757-0442		RESISTOR-FXO 47 OHM 10% -25W CC TUBULAR RESISTOR-FXD 200 OHM 1% -125W F TUBULAR RESISTOR-FXD 442 OHM 1% -125W F TUBULAR RESISTOR-FXD 100K 10% -25W CC TUBULAR RESISTOR-FXD 10K 1% -125W F TUBULAR	01121 24546 16299 01121 24546	C84701 C4-1/8-T0-201-F C4-1/8-T0-422R-F C81041 C4-1/8-T0-1002-F
A9 R38 A9 R39 A9 R40 A9 R41 A9 R42	0757-0278 0698-6780 0684-1041 0698-6823 0698-4473		RESISTOR-FXD 1.78K 1% .125M F TUBULAR RESISTOR-FXD 5.62K .25% .125M F TUBULAR RESISTOR-FXD 100K 10% .25M CC TUBULAR RESISTOR-FXD 2.61K .25% .125M F TUBULAR RESISTOR-FXD 8.06K 1% .125M F TUBULAR	24546 19701 01121 19701 24546	C4-1/8-T0-1781-F MF4C1/8-T2-5621-C CB1041 MF4C1/8-T0-2611-C C4-1/3-T0-8061-F
A9 R43 A9 R44 A9 R45 A9 R46 A9 R47	0698-3495 0757-0424 0757-0442 0757-0442 0698-3382		RESISTOR-FXD 866 OHM 1% al25W F TUBULAR RESISTOR-FXD 1a1K 1% al25W F TUBULAR RESISTOR-FXD 10K 1% al25W F TUBULAR RESISTOR-FXD 10K 1% al25W F TUBULAR RESISTOR-FXD 5a49K 1% al25W F TUBULAR	16299 24546 24546 24546 16299	C4-1/8-T0-866R-F C4-1/8-T0-1101-F C4-1/8-T0-1002-F C4-1/8-T0-1002-F C4-1/8-T0-5491-F
A9 R48 A9 R49 A9 R50 A9 R51 A9 R52	0757-0467 0698-4483 0684-1021 0698-4421 0684-1041		RESISTOR-FXD 200 0HM 1% .125W F TUBULAR RESISTOR-FXD 18.7K 1% .125W F TUBULAR RESISTOR-FXD 1K 10% .25W CC TUBULAR RESISTOR-FXD 249 0HM 1% .125W F TUBULAR RESISTOR-FXD 100K 10% .25W CC TUBULAR	24546 24546 01121 16299 01121	C4-1/8-T0-201-F C4-1/8-T0-1872-F CB1021 C4-1/8-T0-249R-F CB1041
A9 R53 A9 R54 A9 R55 A9 R56 A9 R56	0757-0278 0757-0407 0698-3327 0698-4518 0698-4492		RESISTOR-FXD 1.78K 1% .125W F TUBULAR RESISTOR-FXD 200 OHM 1% .125W F TUBULAR RESISTOR-FXD 3.92K .5% .125W F TUBULAR RESISTOR-FXD 137K 1% .125W F TUBULAR RESISTOR-FXD 32.4K 1% .125W F TUBULAR	24546 24546 03888 24546 24546	C4-1/8-T0-1781-F C4-1/8-T0-201-F PME55-1/8-T0-3921-D C4-1/8-T0-1373-F C4-1/8-T0-3242-F

Table 6-3. Replaceable Parts

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A9 R58 A9 R59 A9 R61 A9 K62 A9 R63	C698-4055 C698-3497 C698-4488 C698-7417 0757-0407		RESISTOR-FXD 1K .25% .125W F TUBULAR RESISTOR-FXD 6.04K 1% .125W F TUBULAR RESISTOR-FXD 26.7K 1% .125W F TUBULAR RESISTOR-FXD 69.8K .25% .125W F TUBULAR RESISTOR-FXD 200 DHM 1% .125W F TUBULAR	03888 16299 24546 30983 24546	PME55-1/8-T0-1001-C C4-1/8-T0-604R-F C4-1/8-T0-2672-F MF4C1/8-T0-6982-C C4-1/8-T0-201-F
A9 R64 A9 k65 A9 R66 A9 R67 A9 R67	0757-0442 0757-0161 0698-4422 0698-4202 0757-0283		RESISTOR-FXD 10K 1% -125W F TUBULAR RESISTOR-FXD 604 OHM 1% -125W F TUBULAR RESISTOR-FXD 1-27K 1% -125W F TUBULAR RESISTOR-FXD 8-87K 1% -125W F TUBULAR RESISTOR-FXD 2K 1% -125W F TUBULAR	24546 24546 16299 16299 24546	C4-1/8-T0-1002-F C4-1/8-T0-604R-F C4-1/8-T0-1271-F C4-1/8-T0-8871-F C4-1/8-T0-2001-F
A9 R68 A9 R71 A9 R72 A9 R73 A9 R74	C757-C976 0757-0438 C757-0283 C698-4202 C757-0976		RESISTOR-FXD 150K 2% .125W F TUBULAR RESISTOR-FXD 5.11K 1% .125W F TUBULAR RESISTOR-FXD 2K 1% .125W F TUBULAR RESISTOR-FXD 8.87K 1% .125W F TUBULAR RESISTOR-FXD 150K 2% .125W F TUBULAR	24546 24546 24546 16299 24546	C4-1/8-T0-1502-G C4-1/8-T0-5111-F C4-1/8-T0-2001-F C4-1/8-T0-8871-F C4-1/8-T0-1502-G
A9 R75 A9 R76 A9 R77 A9 R78 A9 R79	0757-0453 0757-0438 0757-0438 0684-1021 0757-0434		RESISTOR-FXO 30-1K 1% -125W F TUBULAR RESISTOR-FXO 5-11K 1% -125W F TUBULAR RESISTOR-FXO 5-11K 1% -125W F TUBULAR RESISTOR-FXO 1K 10% -25W CC TUBULAR RESISTOR-FXO 3-65K 1% -125W F TUBULAR	24546 24546 24546 01121 24546	C4-1/8-T0-3012-F C4-1/8-T0-5111-F C4-1/8-T0-5111-F C81021 C4-1/8-T0-3651-F
A9 R81 A9 R82 A9 R83 A9 R84 A9 R85	0698-3437 0698-3437 0698-3437 0757-0404		RESISTOR-FXD 133 OHM 1% .125W F TUBULAR RESISTOR-FXD 133 OHM 1% .125W F TUBULAR RESISTOR-FXD 133 OHM 1% .125W F TUBULAR RESISTOR-FXD 130 OHM 1% .125W F TUBULAR RESISTOR-FXD 130 OHM 1% .125W F TUBULAR	16299 16299 16299 24546 24546	C4-1/8-T0-133R-F C4-1/8-T0-133R-F C4-1/8-T0-133R-F C4-1/8-T0-131-F C4-1/8-T0-131-F
AS R86 A9 R87 A9 R88 A9 R89 A9 R91	0757-0484 0757-0404 0698-3446 0757-0438 0757-0161		RESISTOR-FXD 130 OHM 1% -125W F TUBULAR RESISTOR-FXD 130 OHM 1% -125W F TUBULAR RESISTOR-FXD 383 OHM 1% -125W F TUBULAR RESISTOR-FXD 5-11K 1% -125W F TUBULAR RESISTOR-FXD 5-04 OHM 1% -125W F TUBULAR	24546 24546 16299 24546 24546	C4-1/8-T0-131-F C4-1/8-T0-131-F C4-1/8-T0-383R-F C4-1/8-T0-5111-F C4-1/8-T0-604R-F
A9 R92 A9 R93 A9 R94 A9 R95 A9 R96	0698-4441 0698-4020 0757-0435 0757-0161 0757-0435		RESISTOR-FXD 3.74K 1% .125W F TUBULAR RESISTOR-FXD 9.53K 1% .125W F TUBULAR RESISTOR-FXD 3.92K 1% .125W F TUBULAR RESISTOR-FXD 604 OHM 1% .125W F TUBULAR RESISTOR-FXD 3.92K 1% .125W F TUBULAR	16299 16299 24546 24546 24546	C4-1/8-T0-3741-F C4-1/8-T0-9531-F C4-1/8-T0-3921-F C4-1/8-T0-604R-F C4-1/8-T3-3921-F
A9 R97 A9 R98 A5 R99 A5 R101 A9 R102	0757-0280 0698-4486 0757-0280 0698-4486 0757-0271		RESISTOR-FXD 1K 1% -125W F TUBULAR RESISTOR-FXD 24.9K 1% -125W F TUBULAR RESISTOR-FXD 1K 1% -125W F TUBULAR RESISTOR-FXD 24.9K 1% -125W F TUBULAR RESISTOR-FXD 124K 1% -125W F TUBULAR	24546 24546 24546 24546 24546	C4-1/8-T0-1001-F C4-1/8-T0-2492-F C4-1/8-T0-1001-F C4-1/8-T0-2492-F C4-1/8-T0-1243-F
A9 R103 A9 R104 A9 R105 A9 R106 A9 R107	0757-0161 0757-0401 0757-0438 0757-0435 0698-3158		RESISTOR-FXO 604 OHM 1% -125W F TUBULAR RESISTOR-FXD 100 OHM 1% -125W F TUBULAR RESISTOR-FXO 5-11K 1% -125W F TUBULAR RESISTOR-FXO 3-92K 1% -125W F TUBULAR RESISTOR-FXO 23-7K 1% -125W F TUBULAR	24546 24546 24546 24546 16299	C4-1/8-T0-604R-F C4-1/8-T0-101-F C4-1/8-T0-5111-F C4-1/8-T0-3921-F C4-1/8-T0-2372-F
A9 R108 A9 R109 A9 R111 A9 R112 A9 R113	0684-1021 0757-0422 0698-4441 0757-0413 0683-2045		RESISTOR-FXD 1K 10% -25W CC TUBULAR RESISTOR-FXD 909 OHM 1% -125W F TUBULAR RESISTOR-FXD 3.74K 1% -125W F TUBULAR RESISTOR-FXD 392 OHM 1% -125W F TUBULAR RESISTOR-FXD 200K 5% -25W CC TUBULAR	01121 24546 16299 24546 01121	C81021 C4-1/8-T0-909R-F C4-1/8-T0-3741-F C4-1/8-T0-392R-F C82045
A9 R114 A9 R115 A9 R116 A9 R117 A9 R118	0684-1041 0684-1031 0698-3557 0683-2045 0684-1041		RESISTOR-FXD 100K 10% .25W CC TUBULAR RESISTOR-FXD 10K 10% .25W CC TUBULAR RESISTOR-FXD 806 0HM 1% .125W F TUBULAR RESISTOR-FXD 200K 5% .25W CC TUBULAR RESISTOR-FXD 100K 10% .25W CC TUBULAR	01121 01121 16299 01121 01121	CB1041 CB1031 C4-1/8-T0-806R-F CB2045 CB1041
A9 R119 A9 R121 A9 R122 A9 R123 A9 R124	0684-1031 0698-3153 0684-1011 0684-4701 0684-1021		RESISTOR-FXD 10K 10% .25W CC TUBULAR RESISTOR-FXD 3.83K 1% .125W F TUBULAR RESISTOR-FXD 100 0HM 10% .25W CC RESISTOR-FXD 47 0HM 10% .25W CC TUBULAR RESISTOR-FXD 1K 10% .25W CC TUBULAR	01121 16299 01121 01121 01121	CB1031 C4-1/8-T0-3831-F CB1011 CB4701 CB1021
A9 R125 A9 R126 A9 R127 A9 R128 A9 R129	0757-0438 0684-4711 0757-0462 C684-1021 0684-4701		RESISTOR-FXD 5.11K 1% .125W F TUBULAR RESISTOR-FXD 470 OHM 10% .25W CC RESISTOR-FXD 75K 1% .125W F TUBULAR RESISTOR-FXD 1K 10% .25W CC TUBULAR RESISTOR-FXD 47 OHM 10% .25W CC TUBULAR	24546 01121 24546 01121 01121	C4-1/8-T0-5111-F C84711 C4-1/8-T0-7502-F C81021 C84701
A9 R131 A9 R132 A9 R133 A9 S1 A9 S1	0757-0438 0684-4711 0757-0462 03586-61905		RESISTOR-FXD 5.11K 1% .125W F TUBULAR RESISTOR-FXD 470 OHM 10% .25W CC RESISTOR-FXD 75K 1% .125W F TUBULAR SWITCH ASSY (INCLUDES R2)	24546 01121 24546 28480	C4-1/8-T0-5111-F C84711 C4-1/8-T0-7502-F 03580-61905
Ag S2 A9 U1 A9 U2	3100-2738 1826-0044 1820-0427		SWITCH:ROTARY IC;LIN;OPERATIONAL AMPLIFIER IC;LIN;BALANCED MODULATOR	28480 07263 04713	3100-2738 739DC MC1496G

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Oty	Description	Mfr Code	Mfr Part Number
AlO	03580-61906	1	SWITCH ASSY	28480	03580-61906
A1051	3100-2739	1	SWITCH: ROTARY	28480	3100-2739
A1052 A1053	3101-1240 3101-1240 03581-26510 03580-01203	1 1 1	SWITCH  BOARD:BLANK PC  BRACKET:SWITCH	28480 28480 28480	3101-1240 03581-26510 03580-01203
	03580-04106 03580-24708 2260-0001 3030-0007 3050-0295 5040-7038	1 1 1 1 1 1 1	INSULATOR:POWER  SPACER:POWER SWITCH NUT:HEX SSTL 4-40X1/4X3/32 SCREW:SET SST 4-40 X 1/8* WASHER:FLAT 0.313*ID X 0.500* OD CAM:POWER SWITCH	28480 28480 80120 00000 28480	03580-04106 03580-24708 080# 080 080 5040-7038
A11Δ <sub>2</sub>	03580-64211		P.S. HI VOLTAGE	28480	03580–64211
Α11Α1 Δ2	0358066531	1	PC ASSY: HVPS	28480	03580-66531
A11A1C1 A11A1C2, C3 A11A1C4-C6	0150-0050 0150-0012 0160-3008		C:FXD CER 1000 PF +80-20% 1000VDCW C:FXD CER 0.01 UF 20% 1000 VDCW C:FXD CER 4700 PF 20% 4K VDCW	56289 56289 72982	C067B102E102ZS26-CD 29C214A3 3888-024-Y5S0-472M
A11A1CR1-4 A11A1CR5	1901-0033 1901-0341		DIODE:SI 100 MZ 180 WV DIODE:SI 7000 PIV 50 MA	07263 28480	FD3369 19010341
A11A1R1 A11A1R2 A11A1R3 A11A1R4 A11A1R5	0757-0465 2100-3358 0757-0472 0683-1535 0683-3345	R	R:FXD MET FLM 100K OHM 1% 1/8W R:VAR CER 1M OHM 20% 1/2W R:FXD MET FLM 200K OHM 1% 1/8W R:FXD COMP 15K OHM 5% 1/4W R:FXD COMP 330K OHM 5% 1/4W	28480 73138 28480 01121 01121	07570465 72XR105 07570472 CB 1535 CB 3345
A11A2 Δ <sub>2</sub>	0358066532	2	PC ASSY:HVPS (DOES NOT INCLUDE A11A2T1)	28480	03580—66532
A11A2C1,C2 A11A2C3 A11A2C4 A11A2C5	0160-3859 0150-0012 0160-3007 0160-2544		C:FXD CER 560 PF 20% 6000 VDCW C:FXD CER 0.01 UF 20% 1000 VDCW C:FXD CER 4700 PF 20% 4K VDCW C:FXD CER 270 PF 10% 1000 VDCW	56289 56289 72982 56289	29C614 29C214A3 3888-024—Y5S0—472M C016B102E271KS27—CDH
A11A2CR1 A11A2CR2,CR3	1902-3237 1902-3428		DIODE:BKDN 20.0 V 5% DIODE:BKDN SI 100 V 5%	28480 28480	1902—3237 1902—3428
A11A2R1 A11A2R2 A11A2R3 A11A2R4 A11A2R5	0683-4725 0683-1065 0683-1055 0683-4725 0687-2751		R:FXD COMP 4700 OHM 5% 1/4W R:FXD COMP 10M OHM 5% 1/4W R:FXD COMP 1M OHM 5% 1/4 W R:FXD COMP 4700 OHM 5% 1/4W R:FXD COMP 2.7M OHM 10% 1/2W	01121 01121 01121 01121 01121	CB 4725 CB 1065 CB 1055 CB 4725 EB 2751
A11A2R6	0698-8427		R:FXD MET FLM 29M OHM 10%	28480	0698-8427
A11A2T1	9100–3440		TRANSFORMER:HIGH VOLTAGE(INCLUDES 03580-66537)	28480	9100–3440

Table 6-1. Replaceable Parts(Cont'd)

Reference	HP Part Number	Otv	Description	Mfr	Mfu Dout Number
Designation	HP Part Number	Qty	Description	Code	Mfr Part Number
A12			NOT ASSIGNED		
A13	03590_44513	,			
A13	03580-66513	1	BOARD ASSY: DEFLECTION	28480	03580-66513
A13C1	0160-0168		C:FXD MY 0-1 UF 10% 200VDCW	56289	192P10492-PTS
A13C2	0180-0291		C:FXD FLECT 1-0 UF 10% 35VDCW	56289	150D105X9035A2-DYS
A13C3	0180-0291		C:FXD ELECT 1-0 UF 10% 35VDCW	56289	150D105X9035A2-DYS
A13CR1	1901-0040		DIODE:SILICON 50 MA 30 WV	07263	FDG1988
A13CR2	1901-0040		DIODE:SILICON 50 MA 30 WV	07263	FDG1088
A13CR3	1901-0040		DIODE:SILICON 50 MA 30 WV	07263	FDG1088
A13CR4	1901-0040		DIODE:SILICON 50 MA 30 WV	07263	FDG1088
A13Q1	1854-0474		TSTR:SI NPN	28480	1854-0474
A1302	1854-0474		TSTR:SI NPN	28480	1854-0474
A1303	1854-0474		TSTR:SI NPN	28480	1854-0474
A1304	1854-0474		TSTR:SI NPN	28480	1854-0474
A1305	1854-0071		TSTR:SI NPN(SELECTED FROM 2N3704)	28480	1854-0071
A1306	1854-0071		TSTR:SI NPN(SELECTED FROM 2N3704)	28480	1854-0071
A1307	1854-0474		TSTR:SI NPN	28480	1854-0474
A1308	1854-0474		TSTR:SI NPN	28480	1854-0474
A1309	1854-0474		TSTR:SI NPN	28480	1854-0474
A13011	1854-0474		TSTR:SI NPN	28480	1854-0474
A13012	1854-0474		TSTR:SI NPN(SELECTED FROM 2N3704)	28480	1854-0071
A13013 A13R1 A13R2 A13R3 A13R4	1854-0071 2100-0558 2100-3252 2100-3253 2100-3252.	3 2	TSTR:SI NPN(SELECTED FROM 2N3704) R:VAR TRIMMER 20K (HM 10% LIN 1/2W R:VAR CERMET 5K (HM 10% TYPF H 1/2W R:VAR TRIMMER 50K (HM 10% LIN 1/2W R:VAR TRIMMER 50K (HM 10% LIN 1/2W R:VAR CERMET 5K (HM 10% TYPE H 1/2W	28480 28480 28480 28480 28480	1854-0071 2100-0558 2100-3252 2100-3253 2100-3252
A13R5	2100-0558		R:VAR TRIMMFR 20K OHM 10% LIN 1/2W	28480	2100-0558
A13R6	0757-0469		R:FXD FLM 150K OHM 1% 1/8W	28480	0757-0469
A13R7	0757-0469		R:FXD FLM 150K OHM 1% 1/8W	28480	0757-0469
A13R8	0757-0465		R:FXD MET FLM 100K OHM 1% 1/8W	28480	0757-0465
A13R9	0757-0440		R:FXD MET FLM 7.50K OHM 1% 1/8W	28480	0757-0440
A13R11	0757-0442	1	R:FXD MET FLM 10.0K OHM 1% 1/8W	28480	0757-0442
A13R12	0757-0440		R:FXD MET FLM 7.50K OHM 1% 1/8W	28480	0757-0440
A13R13	0757-0469		R:FXD FLM 150K OHM 1% 1/8W	28480	0757-0469
A13R14	0757-0430		R:FXD MET FLM 2.21K OHM 1% 1/8W	28480	0757-0430
A13R15	0757-0429		R:FXD MET FLM 1.82K OHM 1% 1/8W	28480	0757-0429
A13R1 6	0757-0469		R:FXD FLM 150K OHM 1% 1/8W	78480	0757-0469
A13R1 7	0698-4481		R:FXD FLM 16.5K OHM 1% 1/8W	28480	0698-4481
A13R1 8	0698-4435		R:FXD FLM 2.49K OHM 1% 1/8W	28480	0698-4435
A13R1 9	0698-4435		R:FXD FLM 2.49K OHM 1% 1/8W	28480	0698-4435
A13R2 1	0757-0469		R:FXD FLM 2.49K OHM 1% 1/8W	28480	0757-0469
A13R22	0757-0469		R:FXD FLM 150K OHM 1% 1/8W	28480	0757-0469
A13R23	0757-0465		R:FXD MFT FLM 100K OHM 1% 1/8W	28480	0757-0465
A13R24	0757-0440		R:FXD MFT FLM 7,50K OHM 1% 1/8W	28480	0757-0440
A13R25	0757-0442		R:FXD MET FLM 10,0K OHM 1% 1/8W	28480	0757-0442
A13R26	0757-0440		R:FXD MET FLM 7,50K OHM 1% 1/8W	28480	0757-0440
A13k27	0757-0469	1	R:FXD FLM 150K OHM 1% 1/8W	28480	0757-0469
A13k28	0757-0469		R:FXD FLM 150K OHM 1% 1/8W	28480	0757-0469
A13k29	0757-0449		R:FXD FLM 20K OHM 1% 1/8W	28480	0757-0469
A13k31	0698-3484		R:FXD FLM 6650 OHM 1% 1/8W	28480	0698-3484
A13k32	0698-4481		R:FXD FLM 16.5K OHM 1% 1/8W	28480	0698-4481

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A13R33 A13R34 A13R35 A13R36 A13S1	0698-4435 0698-4435 0757-0467 0757-0476 3101-1162	2 1 1	R:FXD FLM 2.49K DHM 1% 1/8W R:FXD FLM 2.49K DHM 1% 1/8W R:FXD MET FLM 121K DHM 1% 1/8W R:FXD MET FLM 301K DHM 1% 1/8W SWITCH:SLIDE MINIATURE. SPDT	28480 28480 28480 28480 79727	0698-4435 0698-4435 0757-0467 0757-0476 GF124-0008
A14	03580-66514	1	BOARD ASSY:BANDWIDTH SWITCH	28480	03580-66514
A14C4 A14C5 A14C6 A14C7 A14C8	0180-0197 0180-0373 0180-1735 0180-2050 0180-1701	1	C:FXD ELECT 2.2 UF 10% 20VDCW C:FXD ELECT 0.68 UF 10% 35VDCW C:FXD ELECT 0.22 UF 10% 35VDCW C:FXD TANT. 0.082 UF 10% 35VDCW C:FXD ELECT 6.8 UF 20% 6VDCW	56289 56289 28480 56289 28480	150D 225X9020A2-DYS 150D684X9035A2-DYS 0180-1735 150D823X9035A2-DYS 0180-1701
A14C9 A14C12 A14C15	0160-0162 0180-0106 0180-0339		C:FXD MY 0.022 UF 10% 200VDCW C:FXD ELECT 60 UF 20% 6VDCW C:FXD AL ELECT 50 UF +75-10% 15VDCW	56289 28480 56289	192P22392-PTS 0180-0106 30D506G015C82-DSM
A14CR1	1902-0777		DIODE:BREAKDOWN 6.2V 5%	04713	1N825
A14R1 A14R2 A14R3 A14R4 A14R5	0698-3453 0698-4488 0698-3558 0698-3519 0698-3228	1	R:FXD MET FLM 196K OHM 1% 1/8W R:FXD FLM 26.7K OHM 1% 1/8W R:FXD MET FLM 4.02K OHM 1% 1/8W R:FXD MET FLM 12.4K OHM 1% 1/8W R:FXD MET FLM 49.9K OHM 1% 1/8W	28480 28480 28480 28480 28480	0698-3453 0698-4488 0698-3558 0698-3519 0698-3228
A14R6 A14R7 A14R8 A14R9 A14R10	0757-0473 0684-1051 0684-2251 0684-1041 0684-3941	1	R:FXD MET FLM 221K OHM 1% 1/8W R:FXD COMP 1MEGOHM 1% 1/4W R:FXD COMP 2-2 MEGOHM 10% 1/4W R:FXD COMP 100K OHM 10% 1/4W R:FXD COMP 390K OHM 10% 1/4W	28480 - 01121 01121 01121 01121	0757-0473 CB 1051 CB 2251 CB 1041 CB 3941
A14R11 A14R12 A14R13 A14R14 A14R15	0698-5102 0698-4443 0757-0454 0698-4506 0698-3459	1 1 1 1	R:FXD COMP 1.2 MEGOHN 10% 1/4W R:FXD FLM 4.53% OHM 1% 1/8W R:FXD MET FLM 33.2% OHM 1% 1/8W R:FXD FLM 73.2K OHM 1% 1/8W R:FXD FLM 78.2K OHM 1% 1/8W	01121 28480 28480 28480 28480 28480	C8 1251 0698-4443 0757-0454 0698-4506 0698-3459
A14R16 A14R17 A14R18 A14R19 A14R20	0698-4524 0757-0442 0698-4441 0698-4427 0698-4511	3 1 3	R:FXD FLM 174K OHM 1% 1/8W R:FXD MET FLM 10.0K OHM 1% 1/8W R:FXD MET FLM 3.74K OHM 1% 1/8W R:FXD FLM 1650 OHM 1% 1/8W R:FXD FLM 86.6K OHM 1% 1/8W	28480 28480 28480 28480 28480	0698-4524 0757-0442 0698-4441 0698-4427 0698-4511
A14R21 A14R22 A14R23 A14R24 A14R25	0757-0456 0757-0446 0757-0415 0757-0407 2100-3123	3 1 1	R:FXD MET FLM 43.2K OHM 1% 1/8W R:FXD MET FLM 15.0K OHM 1% 1/8W R:FXD MET FLM 475 OHM 1% 1/8W R:FXD MET FLM 200 OHM 1% 1/8W R:VAR CFRMET 500 OHM 10% TYPE P 3/4W	28480 28480 28480 28480 28480	0757-0456 0757-0446 0757-0415 0757-0407 2100-3123
A14R26 A14R27 A14R28 A14R31 A14R32	0698-5673 2100-3161 0698-3279 0698-4511 0698-4500	1 1 2	R:FXD MET FLM 3.9K OHM 1% 1/8W R:VAR CERMET 20K OHM 10% TYPE P 3/4W R:FXD MET FLM 4990 OHM 1% 1/8W R:FXD FLM 86.6K OHM 1.0% 1/8W R:FXD FLM 57.6K OHM 1% 1/8W	28480 28480 28480 28480 28480	0698-5673 2100-3161 0698-3279 0698-4511 0698-4500
A14R33 A14R34 A14R35 A14R36 A14R37	0757-0456 0757-0123 0698-3455 0757-0468 0698-7802	1 2 1 2	R:FXO MET FLM 43.2X OHM 1% 1/8W R:FXO MET FLM 34.8K OHM 1% 1/8W R:FXD MET FLM 261K OHM 1% 1/8W R:FXD FLM 130K OHM 1% 1/8W R:FXD FLM 130K OHM 1% 1/8W	28480 28480 28480 28480 28480	0757-0456 0757-0123 0698-3455 0757-0468 0698-7802
A14R38 A14R39 A14R40 A14R41 A14R42	0757-0272 0698-4502 0698-3228 0698-3215 0698-3228	1	R:FXD FLM 52.3K OHM 1% 1/8W R:FXF FLM 64.9K OHM 1% 1/8W R:FXD MET FLM 49.9K OHM 1% 1/8W R:FXD FLM 499K OHM 1.0% 1/8W R:FXD MET FLM 49.9K OHM 1% 1/8W	28480 28480 28480 28480 28480	0757-0272 0698-4502 0698-3228 0698-3215 0698-3228
A14R43 A14R44 A14R45 A14R46* A14R101	0698-3279 0698-4524 0698-4542 0698-3540 0757-0446	1	R:FXD MET FLM 4990 OHM 1% 1/8W R:FXD FLM 174K OHM 1% 1/8W R:FXD FLM 453K OHM 1% 1/8W R: FXD MET FLM 15.4 K OHM 1% 1/8W R: FXD MET FLM 15.0 K OHM 1% 1/8W	28480 28480 28480 16299 28480	0698-3279 0698-4524 0698-4542 C4-1/8-TO-1542-F 0757-0446
A14R102 A14R103 A14R104 A14R105 A14R106	0698-3572 0698-4518 0698-3456 0757-0486 0698-5904	1	R:FXD FLM 60.4K OHM 1% 1/8W R:FXD FLM 137K OHM 1% 1/8W R:FXD MET FLM 287K OHM 1% 1/8W R:FXD MET FLM 750K OHM 1% 1/8W R:FXD FLM 1.58 MEGOHM 1.0% 1/2W	28480 28480 28480 28480 28480	0698-3572 0698-4518 0698-3456 0757-0486 0698-5904
A14R107 A14R108 A14R109 A14R110 A14S1	0698-7094 0698-7091 0698-5675 0698-5675 03580-61901	1 1 2	R:FXD MET FLM 3.32 MEGOHM 1% 1/4W R:FXD MET FLM 10 MEGOHM 1% 1/2W R:FXD MET FLM 30 MEGOHM 1% 1W R:FXD MET FLM 30 MEGOHM 1% 1W SWITCH ASSY	28480 28480 28480 28480 28480	0698-7094 0698-7091 0698-5675 0698-5675 03580-61901

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A1451 A1451 A1452 A14U1	3100-2740 3100-2736 3100-2740 1826-0304	1 1 1	SWITCH:ROTARY SWITCH:ROTARY SWITCH:BANDWIDTH IC LF 355 OP AMP	28480 28480 28480 27014	3100-2740 3100-2736 3100-2740 LF355H
A15	03580-66515	1	BOARD ASSY:SWEEP SWITCH	28480	03580-66515
A15R1	0698-7802		R:FXD FLM 523K DHM 1.0% 1/8W	28480	0698-7802
A15R2 A15R3 A15R4 A15R5 A15R6	0757-0403 0757-0410 0757-0161 0757-0274 0757-0273		R:FXD MET FLM 121 OHM 1% 1/8W R:FXD MET FLM 301 OHM 1% 1/8W R:FXD FLM 604 OHM 1% 1/8W R:FXD MET FLM 1.21K OHM 1% 1/8W R:FXD MET FLM 3.01K OHM 1% 1/8W	28480 28480 28480 28480 28480	0757-0403 0757-0410 0757-0161 0757-0274 0757-0273
A15R7 A15R8 A15R9 A15R10 A15R11	0698-3497 0757-0444 0757-0453 0698-3572 0757-0467	2	R:FXD FLM 6.04K OHM 1% 1/8W R:FXD MET FLM 12.1K OHM 1% 1/8W R:FXD MET FLM 30.1K OHM 1% 1/8W R:FXD FLM 60.4K OHM 1% 1/8W R:FXD MET FLM 121K OHM 1% 1/8W	28480 28480 28480 28480 28480	0698-3497 0757-0444 0757-0453 0698-3572 0757-0467
A15R12 A15R13 A15R14 A15R15 A15R16 A15R17 A15R18 A15R19 A15R20 A15R21 A15R21	0698-3499 0698-3497 0757-0442 0757-0444 0698-5572 0698-5572 0757-0442 2100-0668 0698-3519 0698-6758	2	R:FXD FLM 40.2K OHM 1% 1/8W R:FXD FLM 6.04K OHM 1% 1/8W R:FXD MET FLM 10.0K OHM 1% 1/8W R:FXD MET FLM 12.1K OHM 1% 1/8W R:FXD FLM 12.5K OHM 0.5% 1/8W R:FXD FLM 12.5K OHM 0.5% 1/8W R:FXD MET FLM 10.0K OHM 1% 1/8W R:VAR 10 K 10% R:FXD MET FLM 12.4K OHM 1% 1/8W R:FXD FLM 12.5K OHM 0.5% 1/8W R:FXD FLM 12.5K OHM 0.5% 1/8W R:FXD FLM 12.5K OHM 0.5% 1/8W	28480 28480 28480 28480 28480 28480 28480 12697 28480 28480 28480	0698-3499 0698-3497 0757-0442 0757-0444 0698-5572 0757-0442 381 0698-3519 0698-6758 0698-5580
A15R23 A15R24 A15R25 A15R26 A15R27	0698-5573 0698-6292 0698-5581 0757-0015 0698-5916	1 1 1	R:FXD FLM 50K OHM 0.5% 1/8W R:FXD FLM 125K OHM 0.5% 1/8W R:FXD FLM 250K OHM 0.5% 1/8W R:FXD MET FLM 500K OHM 1/2% 1/2W R:FXD MET FLM 1.25 MEGOHM 1.0% 1/2W	28480 28480 28480 28480 28480	0698-5573 0698-6292 0698-5581 0757-0015 0698-5916
A15R28 A15R29 A15R3C A15R31 A15R32	0698-5987 0698-3587 0757-0486 0698-4489 0684-3351	1 1	R:FXD MET FLM 2.5 MEGGHM 1.0% 1/2W R:FXD MET FLM 5.00 MEGGHM 1% 1W R:FXD MET FLM 750K DHM 1% 1/8W R:FXD FLM 28K DHM 1% 1/8W R:FXD 3.3 MEGGHM 10%	28480 28480 28480 28480 01121	0698-5987 0698-3587 0757-0486 0698-4489 CB 3351
A15R41 A15R42 A15R43 A15R44 A15S1	0698-4524 0698-3455 0698-4500 0698-4511 03580-61903	1	R:FXD FLM 174K OHM 1% 1/8W R:FXD MET FLM 261K OHM 1% 1/8W R:FXD FLM 57.6K OHM 1% 1/8W R:FXD FLM 86.6K OHM 1.0% 1/8W SWITCH ASSY:SPAN	28 4 8 0 28 4 8 0 28 4 8 0 28 4 8 0 28 4 8 0	0698-4524 0698-3455 0698-4500 0698-4511 03580-61903
A15S1 A15S2 A15U1 A15U2	3100-2742 03580-61904 1826-0043 1826-0043	1	SWITCH:ROTARY SWITCH ASSY:MODE IC:LINFAR OPERATIONAL AMPLIFIER IC:LINEAR OPERATIONAL AMPLIFIER	28480 28480 28480 28480	3100-2742 03580-61904 1826-0043 1826-0043
A16	03580-66516	1	BOARD ASSY:FCM	28480	03580-66516
A16C1	0180-1743		C:FXD ELECT 0.1 UF 10% 35VDCW	56289	1500104X9035A2-DYS
A16C2 A16C3 A16C4 A16C12 A16CR1	0160-2207 0150-0093 0180-0376 0150-0093 1901-0040		C:FXD MICA 300 PF 5% C:FXD CER 0.01 UF +80-20% 100VDCW C:FXD ELECT 0.47 UF 10% 35VDCW C:FXD CER 0.01 UF +80-20% 100VDCW DIODE:SILICON 50 MA 30 WV	28480 72982 56289 72982 07263	0160-2207 801-K800011 1500474×9035A2-DYS 801-K800011 FDG1088
A16CR2 A16CR3 A16CR4 A16CR6 A16CR7	1901-0040 1901-0040 1901-0040 1902-0025 1901-0040		DIGDE:SILICON 50 MA 30 WV DIGDE:SILICON 50 MA 30 WV DIGDE:SILICON 50 MA 30 WV DIGDE-BREAKOOWN:10.0V 5% 400 MW DIGDE:SILICON 50 MA 30 WV	07263 07263 07263 28480 07263	FDG1088 FDG1088 FDG1088 FDG1088 1902-0025 FDG1088
A16CR 8, CR9 A16J1, J2 A16L1 A16O1 A16O2	1901-0040 1251-2035 9100-1644 1854-0354 1853-0010	1	DIODE:SILICON 50 MA 30 WV CONN:PC EDGE (2 x 15) 30 CONTACT COIL/CHOKE 330 UH 5% TSTR:SI NPN TSTR:SI PNP(SELECTED FROM 2N3251)	07263 71785 28480 28480 28480	FDG1088 252-15-30-300 9100-1644 1854-0354 1853-0010
A1603 A1604 A1606 A1607 A1608	1854-0071 1853-0010 1854-0071 1854-0475 1855-0386	1	TSTR:SI NPN(SELECTED FROM 2N3704) TSTR:SI PNP(SELECTED FROM 2N3251) TSTR:SI NPN(SELECTED FROM 2N3704) TSTR:SI NPN TSTR:FET N-CHANNEL	28480 28480 28480 28480 80131	1854-0071 1853-0010 1854-0071 1854-0475 2N4392

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A16R1 A16R2 A16R3 A16R4 A16R5	0757-0270 0757-0270 0757-0426 0698-4499 0698-3162	2 1 2	R:FXD MET FLM 249K OHM 1% 1/8W R:FXD MET FLM 249K OHM 1% 1/8W R:FXD FLM 1.3K OHM 1% 1/8W R:FXD FLM 54.9K OHM 1% 1/8W R:FXD MET FLM 46.4K OHM 1% 1/8W	28480 28480 28480 28480 28480	0757-0270 0757-0270 0757-0426 0698-4499 0698-3162
A16R6 A16R7 A16R8 A16R9 A16R10	0698-4503 0684-2231 0757-0282 0684-1031 0698-3228		R:FXD FLM 66.5K OHM 1% 1/8W R:FXD COMP 22K OHM 10% 1/4W R:FXD MET FLM 221 OHM 1% 1/8W R:FXD COMP 10K OHM 10% 1/4W R:FXD MET FLM 49.9K OHM 1% 1/8W	28480 01121 28480 01121 28480	0698-4503 CB 2231 0757-0282 CB 1031 0698-3228
A16R11 A16R12 A16R13 A16R14 A16R15	0757-0456 0698-3228 0698-3228 0684-1041 0684-2251		R:FXD MET FLM 43-2K OHM 1% 1/8W R:FXD MET FLM 49-9K OHM 1% 1/8W R:FXD MET FLM 49-9K OHM 1% 1/8W R:FXD COMP 100K OHM 10% 1/4W R:FXD COMP 2-2 MEGOHM 10% 1/4W	28480 28480 28480 01121 01121	0757-0456 0698-3228 0698-3228 GB 1041 CB 2251
A16R1 & A16R1 7 A16R1 8 A16R1 9 A16R2 2	0757-0440 0757-0460 0698-3557 0698-3228 0684-2231		R:FXD MET FLM 7.50K OHM 1% 1/8W R:FXD MET FLM 61.9K OHM 1% 1/8W R:FXD FLM 806 OHM 1% 1/8W R:FXD MET FLM 49.9K OHM 1% 1/8W R:FXD COMP 22K OHM 10% 1/4W	28480 28480 28480 28480 01121	0757-0440 0757-0460 0698-3557 0698-3228 CB 2231
A16R24 A16R25 A16R2& A16R27 A16R28	0757-0479 0757-0273 0698-3162 0698-3228 0757-0463	1	R:FXD MET FLM 392K OHM 1% 1/8W R:FXD MET FLM 3.01K OHM 1% 1/8W R:FXD MET FLM 46.4K OHM 1% 1/8W R:FXD MET FLM 49.0K OHM 1% 1/8W R:FXD MET FLM 82.5K OHM 1% 1/8W	28 4 8 0 2 8 4 8 0 2 8 4 8 0 2 8 4 8 0 2 8 4 8 0	0757-0479 0757-0273 0698-3162 0698-3228 0757-0463
A16R2S A16R3C A16R31 A16W1 A16W1	0698-3557 0684-6831 0684-1041 1826-0043 1826-0043		R:FXD FLM 806 OHM 1% 1/8W R:FXD COMP 66K OHM 10% 1/4W R:FXD COMP 100K OHM 10% 1/4W IC:LINEAR OPERATIONAL AMPLIFIER IC:LINEAR OPERATIONAL AMPLIFIER	28480 01121 01121 28480 28480	0698-3557 C8 6831 C8 1041 1826-0043
A16U3 A16U4 A17	1820-0223 1826-0111	1	INTEGRATED CIRCUIT:OPERATIONAL AMPL. IC NOT ASSIGNED	28480 04713	1820-0223 MC1458C
Α18 Δ <sub>1</sub>	03581-66518	1	BOARD ASSY: INPUT, BALANCED (FOR OPTION 002 ONLY)	28480	03580–66518
A18C1 A18C4 A18C5 A18C6	0180-0091 0180-0091 0160-2206 0140-0204	2	C:FXD 10UF+50 -10% 100VDC AL C:FXD 10UF+50 -10% 100VDC AL C:FXD 160PF 5% 300VDCW C:FXD 47PF 5% 500VDCW	56289 56289 28480 72136	30D106F100DC2 30D106F100DC2 0160-2206 DM15E470J0500WV1CR
A18J1 A18J2	1251–2969 1251–3638	1	CONN:PHONO, SINGLE JACK CONN:POST TYPE	27264 27264	15240501 09651061
A18R1 A18R2 A18R3*	0698-4882 0698-5874 0757-0284	1 1 1	R:FXD 976 OHM 1% .5 W F TUBULAR R:FXD 639 OHM 1% .5W F TUBULAR R:FXD 150 OHM 1% .125W F TUBULAR FACTORY SELECTED PART	24546 24546 24546	NA6 -F NA6 3-F C4-1/8-TO-151-F
A18R4 A18R5	0757-0472 0698-4308	1	R:FXD 200K 1% .125W F TUBULAR R:FXD 16.9K 1% .125W F TUBULAR	24546 16299	C4-1/8-TO-2003-F C4-1/8-TO-1692-F
A18T1	91001460	1	TRANSFORMER AUDIO	28480	9100–1460
A19			NOT ASSIGNED		
A20	0960-0444	1	POWER INPUT MODULE	28480	0960-0444

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
			CHASSIS MOUNTED COMPONENTS		
	02500 04100	2	PLASTIC BATTERY END GUARD	28480	03580-04108
BT1-BT4	03580-04108 14200203	4	BATTERY PACK (4 CELLS) (OPT 001 ONLY)	05397	Y-6114
BT5	14200202	1	BATTERY PACK (4 CELLS CENTER TAP) (OPT 001 ONLY)	05397	Y-5505
	10101B	1 .	COVER: PROTECTIVE FRONT (OPT 001 ONLY)	28480	10101B
C1	0160-2050	1	C:FXD 10 UF 30V 10%	56289	127P1069R3S4
DS1	21400380	1	LAMP: INCAND. (POWER	17537	86
	14500153	1	LAMP HOLDER (FOR DS1)	08717	102SR
	14500157	1	LENS (FOR DS1)	08717	102XX-W
DS2 -DS4	1990-0450	3	DIODE:LIGHT EMITTING	28480	19900450
551	5040-7626	3	CLAMP LED (FOR DS2-4)	28480	50407626
F1	2110-0012	1	FUSE:0.5A 250V NB	75915	312.500
J1	1510-0084	1	BINDING POST:J-GRAY/RED	28480	1510-0084
J2	1510-0087	1	BINDING POST:J-GRAY/BLK	28480	15100087
J3,J4	1510-0076	2	BINDING POST:J-GRAY	28480	1510-0076
J5-J10	1250-0083	6	CONN:BNC	02660	31-221-1020
J11,J12	1510-0076	2	BINDING POST:J-GRAY (OPT 002 ONLY)	28480	1510-0076
	2190-0027 2950-0006		WASHER-INTERNAL LOCK   NUT-HEX	78189 73734	1914-00 9000
K1	0490: -0499	1	RELAY:SPDT 2A 12VDC PEN LIFT	12300	RS5D-12VDC
L1	01200-44703	1	COIL: TRACE ALIGN	28480	01200-44703
R1	21000573	1	R:VAR LINEAR 200K OHM (INTENSITY) 20% 1/2W	01121	WA4N040S204MZ
R2	2100-0572	1	R:VAR C COMP 100K OHM (ADAPTIVE SWEEP-INCLUDES S1)	12697	381
R3	2100-0571	1	R:VAR 5M OHM (FOCUS) 20%	12697	381
R4	2100-1714	1	R:VAR C COMP LINEAR 1K OHM 20% 1/2W (CAL 10KHZ)	01121	TYPE W
R5	2100-2843	1	R:VAR COMP LINEAR 5K OHM 10% 1/2W (LEVEL)	28480	21002843
R6	2100-0564	1	R:VAR, 100 K 20*	28480	2100-0564
R7	2100-0574	1	R:VAR 10 TURN 5 K - 10%	28480	2100-0574
S1	21000572		SWITCH:SPST (P/O R2)	12697	381
S2	03580-01901	1	SWITCH:PUSHBUTTON (DISPLAY)	28480	03580-01901
S3	3101-0548	1	SWITCH:PUSHBUTTON (AMPLITUDE MODE)	28480	3101-0548
S4 S5	3101–0199 3101–0199	1	SWITCH:SLIDE DPDT (dBV/dBm) 0.5A 125V SWITCH:SLIDE DPDT (EXT REF/NORMAL)	79727 79727	G1260012 G1260012
S6	3101-0575	1	SWITCH:SLIDE (BAL, BRIDGED, TERMINATED) (OPT 002 ONLY)	79727	G168S-0000
S7	3101-0199	1	SWITCH:SLIDE	79727 28480	3101-0199
T1	9100-3425	1	TRANSFORMER:POWER	28480	9100-3425
T2	9100–3883	1	TRANSFORMER:OUTPUT (BALANCED TRACKING OSC OUT) (OPT 002 ONLY)	28480	9100-3883
V1	5083-1871	1	TUBE:CATHODE RAY	28480	5083-1871
W1	8120-1348	1	CORD:POWER, DETACHABLE	70903	KHS-7041
W2	03580-61606	1	CABLE ASSY:POWER	28480	03580-61606
W3	03580-61604	1	CABLE ASSY:DIGITAL STORAGE	28480	03580-61604
W4	03580-61603	1	CABLE ASSY:POT (INCLUDES FOCUS POT, R3)	28480	03580-61603
W5	03580-61601	1	CABLE ASSY:dBV/dBm SWITCH	28480	0358061601
W6	0358061602	1	CABLE ASSY:INPUT (OPT 002 ONLY)	28480	03580-61602
XA1	1200-0037	1	SOCKET:CRT	72825	97097
			MISCELLANEOUS MECHANICAL PARTS		
	5020-0476	1	BEZEL: CRT (METAL)	28480	5020-0476
	03580-04102 03580-00608	1	COVER: BOTTOM	28480	03580-04102
	03580-00608	2	COVER: CARD NEST COVER: SIDE RAIL	28480 28480	03580-00608 03580-04104
	03580-04103	1	COVER: TOP	28480	03580-04103
	01200-44701 1390-0084	1	CRT NECK-CLAMP FASTENER-PANEL:RECEPTACLE, QUARTER TURN	28480 94222	01200-44701 82-47-101-15
	1390-0339		FASTENER-PANEL: SCREW, QUARTER TURN	28480	1390-0339
	1390-0088		FASTENERPANEL: RETAINER (FOR SCREW)	28480	1390-0088
	03580-60121 5060-0548	1	DECK:MAIN FACE PLACE:CRT (BLUE)	28480 28480	03580-60121 5060-0548
	5040-5862	4	FOOT:REAR PANEL BASE:FOOT	28480	5040-5862
	50405861	4	CAP:END	28480	5040-5862
	03580-20001	1	FRAME:FRONT	28480	03580-20001
	03580-20012 1510-0038	1	FRAME:REAR BINDING POST-SINGLE	28480 28480	03580-20012 1510-0038
	7120-4609	1	WARNING LABEL	28480	7120-4609
	03580-23702	1	FRAME:LEFT SIDE RAIL	28480	03580-23702
	03580-23701 1440-0103	1 2	FRAME:RIGHT SIDE RAIL HANDLE:STRAP	28480 28480	03580-23701 1440-0103
	5040-0508	1	LIGHT SHIELD:CRT (PLASTIC)	28480	5040-0508

Table 6-1. Replaceable Parts(Cont'd)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
	03580-04104 5040-7042 03580-24706 03580-26001 3050-0456 5040-0508 03580-00203	2 4 4 4 4 1	MISCELLANEOUS MECHANICAL PARTS (CONT'D) COVER:SIDE RAIL CAP:END (FOR HANDLE) RETAINER (FOR HANDLE) SCREW (FOR HANDLE) WASHER (FOR HANDLE) LIGHT SHIELD:CRT (PLASTIC) PANEL:FREQUENCY CONTROL MODULE PANEL:FRONT	28480 28480 28480 28480 86928 28480 28480	03580-04104 5040-7042 03580-24706 03580-26001 5808-16-15 5040-0508 03580-00203
	0358000211 Δ 0358000214 Δ	1 1	STANDARD 3580A OPTION 002 PANEL: REAR	28480 28480	03580-00201 03580-00204
	03580-00212 03580-00205	1 1	STANDARD 3580A OPTION 002	28480 28480	03580-00202 03580-00205
	1460—1341 5060—7440	1 1	STAND:TILT WINDOW:FREQUENCY KNOBS	28480 28480	1460—1341 5060—7440
	0370-1005 0370-2182 0370-2186 0370-2188 0370-1005		ADAPTIVE SWEEP AMPLITUDE REF LEVEL BANDWIDTH DISPLAY SMOOTHING FOCUS	28480 28480 28480 28480 28480	0370-1005 0370-2182 0370-2186 0370-2188 0370-1005
	03701115 03702185 0358067401 71204008 03701005		FREQUENCY (CRANK) FREQUENCY SPAN INPUT SENSITIVITY DECAL INTENSITY	28480 28480 28480 28480 28480	0370-1115 0370-2185 03580-67401 7120-33115 0370-1005
	0370-2188 0370-2473 0370-2187 0370-2184 0370-2188		MANUAL VERNIER POWER SWEEP MODE SWEEP TIME CONCENTRIC KNOB	28480 28480 28480 28480 28480	0370-2188 0370-2473 0370-2187 0370-2184 0370-2188
	0370–2189 0370–1019 0370–0906 0370–0934 0370–0914	6 6 6	VERNIER ZERO CAL PUSHBUTTON-BASE PUSHBUTTON-CAP PUSHBUTTON-BEZEL	28480 28480 28480 28480 28480	0370-2189 0370-1019 0370-0906 0370-0934 0370-0914
	0350-0137 0350-0136 0350-0135 0350-0138	1 1 1 3	LABEL:PUSHBUTTON, 1 DB LABEL:PUSHBUTTON, 10 DB LABEL:PUSHBUTTON, LIN LABEL:PUSHBUTTON, PLAIN	28480 28480 28480 28480	0350-0137 0350-0136 0350-0135 0350-0138
			MECHANICAL PARTS (SEE FIGURE 6-1)		
MP1 MP2 MP3 MP4 MP5	1140-0059 03580-24302 03580-24303 1430-0777 03580-24304	1 1 1 1	COUNTER:MECH PLATE:COUNTER PLATE:POT GEAR:SPUR PLATE:REAR	28480 28480 28480 28480 28480	1140-0059 03580-24302 03580-24303 1430-0777 03580-24304
MP6 MP7 MP8 MP9 MP10	03580-20801 1430-0778 1430-0775 03580-24704 1460-0563	1 1 1 4	HSG:DETENT GEAR:SPUR GEAR:SPUR HSG:SPACER SPRING:CLUTCH	28480 28480 28480 28480 28480	03580-20801 1430-0778 1430-0775 03580-24704 1460-0563
MP11 MP12 MP13 MP14 MP15	03580-24705 03580-23704 03580-21401 03580-21204 3050-0587	2 1 2 1 1	SPACER:RATIO DRIVE SHAFT:COUNTER RATIO DRIVE ADAPTER:CLUTCH WASH:NEOPRENE	28480 28480 28480 28480 28480	03580-24705 03580-23704 03580-21401 03580-21204 3050-0587
MP16 MP17 MP18 MP19 MP20	03580-23703 1430-0713 03580-22402 03580-24301 03580-24702	1 1 1 1 3	SHAFT:RATIO DRIVE GEAR:MITER GEAR:BEVEL, MOD PLATE:FRONT (THIS INCLUDES SWITCH, 3101-0199) SPACER:HSG	28480 28480 28480 28480 28480	03580-23703 1430-0713 03580-22402 03580-24301 03580-24702
MP21 MP22 MP23 MP24 MP25	03580-01216 5040-7532 03580-24703 03580-62401 03580-22401	1 1 2 1	PLATE:CLUTCH CLUTCH SPACER:HSG GEAR:ANTI-BACKLASH GEAR:STOP-MOD	28480 28480 28480 28480 28480	03580-01216 5040-7532 03580-24703 03580-62401 03580-22401
MP26 MP27	00692-247 03580-23705	1	GEAR:STOP SHAFT:LIMIT	28480 28480	00692-247 0358023705
R6 R7	2100-0564 2100-0574	1 1	R:VAR, 100 K 20° R:VAR 10 TURN 5 K - 10%	28480 28480	2100-0564 2100-0574
S7	3101-0199	1	SWITCH SLIDE	28480	31010199

Δ For S/N 1312A-00365 and below: order 03580-00201 (Std) or 03580-00204 (Opt. 002).

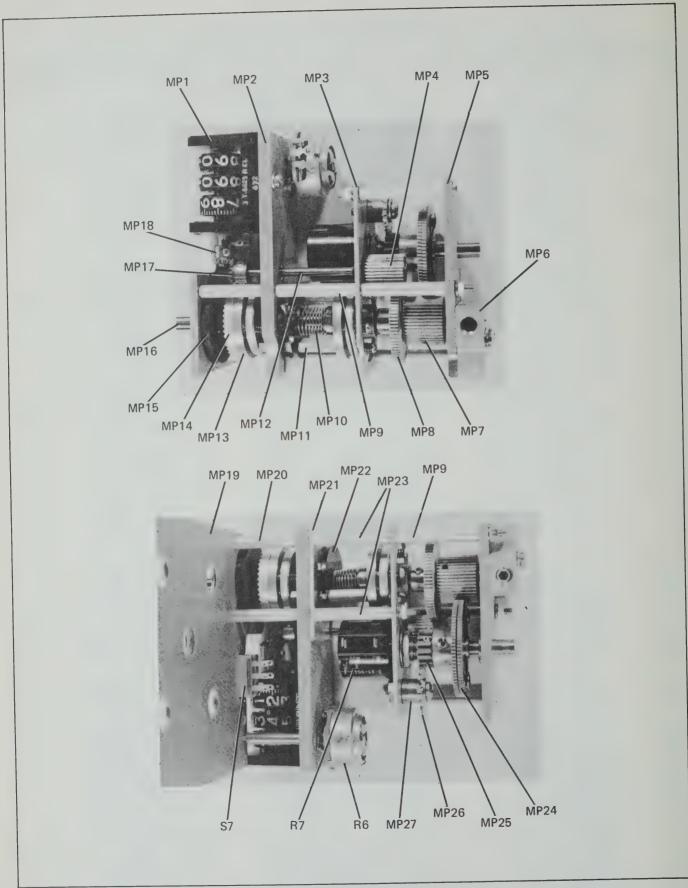


Figure 6-1. Frequency Control Component Locator.

Model 3580A

# SECTION VII TROUBLESHOOTING AND CIRCUIT DIAGRAMS

#### 7-1. INTRODUCTION.

7-2. This section of the manual contains troubleshooting information and circuit diagrams for the Model 3580A Spectrum Analyzer. Included are troubleshooting information, information on factory selected components, functional block diagrams, schematic diagrams and component location diagrams.

## 7-3. TROUBLESHOOTING AND PREVENTIVE MAINTENANCE.

#### 7-4. General Troubleshooting Procedures.

- 7-5. Troubleshooting information for the 3580A can be found in the functional block diagrams and circuit diagrams at the end of Section VII. An extensive set of notes, waveforms, and tables has been provided to help narrow the problems down from the functional block, to a board, and finally to a component.
- 7-6. Use the Overall Functional Block Diagram (Figure 7-1) to narrow the 3580A problem down into one of the four major functional blocks:
  - 1) Input Section
  - 2) Frequency and Sweep Section
  - 3) IF Section
  - 4) Display Section.

This diagram gives a good overall look at the 3580A operation. Once the diagram is understood, the failure symptoms alone may be adequate to lead you to the proper block. Other times, the output signals from the 3580A will suffice. For instance, the RECORDER X-AXIS and Y-AXIS outputs give an indication of proper instrument operation up to, but not including, the A7 Logic Board. The TRACKING OSC OUTPUT indicates if the Frequency and Sweep Section is working properly.

7-7. If the external control signals and front panel failure symptoms are not adequate to localize a problem to a particular block, remove the 3580A outer covers and check the appropriate input and output lines of each block. This will localize the problem to a block. The Analog Block Diagram (Figure 7-2), circuit schematics and associated notes can then be used to isolate the problem to the component.

#### 7-8. A2 Board VTO Troubleshooting.

7-9. The A2 VTO is part of a complex feedback loop. If the VTO circuitry is not working properly, the feedback loop can be broken by applying approximately - 1.6 V dc to A2TP4. A 0 to +9 V dc signal supplied to the VTO ERROR AMP on the RED jumper lead to the A2 board should then cause the oscillator frequency to vary from 1.0 to 1.5 MHz (0 to 50 kHz Input Frequency). This signal can then be followed around the feedback loop to find the faulty components. Use the waveforms supplied with the A2 board to aid in this process.

#### 7-10. A3 Board Troubleshooting.

7-11. This part of Section VII contains test procedures for the digital control circuitry of the A3 Sweep Board (Schematic 4). If the previous troubleshooting procedures indicate problems with the normal or adaptive sweep circuitry, perform these test procedures.

a. Position the 3580A front panel controls to:

SWEEP MODE . . . . . . . . . . . . . . . . . . REP

Short A3TP1 to the gray jumper wire connected near the center of the A3 board (Don't remove the gray jumper).

- b. Adjust A3R54 (INTEGRATOR BALANCE) to verify that the output of the Ramp Integrator (A3TP1) can be adjusted from a positive to negative dc voltage. Readjust A3R54 for 0.000 volts ± .001 volts.
- c. Measure Vsg on the dual FET, A3Q1. Both FET's should have  $Vsg \le 3 V dc$ .
- d. Set switch S1 to the test position (UP position). Verify that CLOCK OUTPUT (A3U8 pin 11) is a TTL HIGH (≥ 2.0 V dc). Return S1 to the normal position.
- e. Remove the clock test jumper between Q18 and S1. Reposition:

f. Connect a logic clip to A3U5. Turn the 3580A POWER switch OFF then back to ON. The instrument should come up in state 000 or 100, where the C, B, and A state outputs are located on pins 13, 14 and 15 respectively of A3U5. If the instrument comes up in state 000, clock it to state 100 by momentarily switching A3S1 into, and then out of the test position. (This process will be called "clocking S1" from now on.)

g. Reposition the following front panel controls:

ADAPTIVE SWEEP	. OFF
RESOLUTION BANDWIDTH 1	00 Hz
FREQ. SPAN/DIV	2 KHz
SWEEP TIME/DIV	

- h. Check the following:
  - 1. Collector of A3O4: 10 volts ± .1 volts
  - 2. Collector of A3Q16: 0.0 volts ± .1 volts
  - 3. A3U5 pin 5: TTL LOW (as measured by logic clip).
  - 4. A3U5 pins 2, 3, 4, 6 and 9: TTL HIGH (as measured by logic clip).
  - 5. A3TP2: -.25 volts ± .02 volts.
  - 6. A3TP3:  $\pm .175$  volts  $\pm .02$  volts.
  - 7. A3U8 pin 6: TTL HIGH (> 2.0 volts).
- i. Manually "clock" S1 once and verify that the state does not change from 100.
- j. Short A3TP3 to A3TP4. Verify that the voltage at A3TP11 can be changed from a negative to positive voltage by rotating A3R14. Readjust A3R14 so the voltage at TP11 is at the 0 V transition point. (In some cases it will alternate between positive and negative.)
- k. Check for proper source voltage on A3Q14.  $(.1 < V_s < +4)$ .
  - 1. Readjust A3R14\* fully CCW. Reposition:

m. (L)RESP (A3U7 pin 5) should be a TTL HIGH. Verify that any one of the following will cause (L)RESP to go LOW.

ADAPTIVE SWEEP	٠						CCW
SWEEP TIME/DIV						.05	SEC/DIV
							or faster

If (L)RESP doesn't function properly, check the A8 board.

- n. In the following tests, the proper next state qualifiers are set up and the control logic is manually stepped to the next state by "clocking" S1 once. In each case the control logic should go to the next state only when all qualifiers are met and S1 is clocked.
- o. If the control logic fails to clock to the proper state, reset the logic to state 000 or 001 by selecting:

SWEEP MODERI	ESE	C	۲,	1
--------------	-----	---	----	---

and momentarily turning the POWER switch OFF and then back to ON. Use Table 7-1 to reclock the control logic up to that state which will not go to the proper next state after clocking S1. Then recheck all the next state qualifiers, as given in Table 7-1 and test for proper inputs to the state flip—flops (U6 and U7). The J and K inputs to these flip—flops should correspond to the change the flip—flop will make on the next clock pulse. For instance, if a flip—flop's Q-output is to change from a 0 to a 1, its J input should be high. Likewise, if it is to change from a 1 to a 0, the K input should be high. If it is to stay at 1, the K input should be a 0. If it is to stay at 0, the J input should be a 0.

Table 7-1. Conditions for Single Stepping A3 Logic.

(Initial Setup: [Gray Jumper - TP1], [TP3 - TP4], A3R14\* fully CCW, ADAPTIVE SWEEP - OFF, 100 Hz Bandwidth, 2 kHz/DIV, .1 SEC/DIV, RESET.)

Present State	Next State	Conditions to go to next State	Next State Qualifiers
CBA 0 000	CBA 1 100	SWEEP MODE: RESET	(L)SING - HIGH and (H)GEW - HIGH or (L)RESET - LOW
1 100	2 101	SWEEP MODE: SING	(H)DLYO - HIGH (L)RESET - HIGH
2 101	3 111	ADAP. SWEEP: CW	(L)RESP - HIGH
3 111	4 110	R14: CW*	CCMP - HIGH (L)RESP - HIGH
4 110	5 010	ADAP. SWEEP: CCW	(L)RESET LOW
5 010	6 011	R14: CCW*	CCMP - LOW
6 011	7 001	(Clock after delay)	(H)DLYO - HIGH
7 001	2 101	R14: CW*	CCMP - HIGH

<sup>\*</sup>If A3R14 has a black casing, set it opposite to the setting given.

See Table 7-2. Notice also that the J and K inputs are not directly accessible on U7. All the inputs to each of the input AND gates must be high before there is a corresponding HIGH level given to one of the internal J or K inputs of the flip—flop.

Table 7-2. Excitation Table for J - K Flip-Flop.

Q <sub>t</sub> (Before Clock)	Q <sub>t</sub> + 1 (After Clock)	J	К
0	1	1	don't care
1	0	don't care	1
1	1	don't care	0
0	0	0	don't care

p. Reposition (Only those controls printed in BOLD require a change from the previous tests.)

ADAPTIVE SWEEP	OFF
RESOLUTION BANDWIDTH	. 100 Hz
FREQ. SPAN/DIV	2 KHz
SWEEP TIME/DIV	0.1 SEC
SWEEP MODE	RESET
A3R14 fu	ılly CCW*

- q. State 100
  - 1. Clock S1, observe no change of state.
  - 2. Check the voltage at A3TP5 and A3TP6, it should be 0 V dc ± .1 V.
  - 3. Short the A3 gray jumper to A3TP1 and short A3TP3 to A3TP4 if not already done.
  - 4. Reposition:

SWEEP MODE ...... SING

5. Clock S1, and the logic should go to State 101.

- r. State 101
  - 1. Clock S1 and observe no change of state.
  - 2. Check for the following levels:

Collector A3Q4 : < - 8 V dc Collector A3Q16: 0 V dc ± .1 V

A3TP5: < -7 V dcA3TP6:  $0 \text{ V dc} \pm .1 \text{ V}$ 

A3TP8: TTL LOW ( < .8 Vdc)

3. Reposition:

ADAPTIVE SWEEP ......CW

- 4. Clock S1 once, and the logic should go to State
- s. State 111

- 1. Adjust A3R14 fully CCW\*.
- 2. Clock S1 and observe no change of state.
- 3. Check for the following levels:

Collector A3Q16:  $-9.9 \text{ V} \pm .1 \text{ V}$ A3TP5: < -7 V

A3TP8: TTL LOW ( < .8 V)

4. Reposition:

ADAPTIVE SWEEP ......CCW

Clock S1 and observe no change of state.

- 5. Adjust A3R14 fully CCW. Clock S1 and observe no change of state.
- 6. Reposition:

ADAPTIVE SWEEP .....CW\*

Adjust A3R14 fully CCW\*. Clock S1 and observe no change of state.

- 7. Adjust A3R14 fully CW\*. Clock S1, and the logic should go to state 110.
- t. State 110
  - 1. Clock S1 and observe no change of state.
  - 2. Remove the test lead between A3TP3 and A3TP4.

    The voltage at A3TP3 should be -.25 V
    ± .1 V.

    Replace the jumper.
  - 3. Check the following levels:

A3TP6: < -6 VA3TP5:  $0 \text{ V dc} \pm .1 \text{ V}$ 

Collector of A3Q16:  $0 \text{ V} \pm .1 \text{ V}$ .

- 4. Adjust R14 fully CCW\*.
  Clock S1 and observe no change of state.
- 5. Reposition:

ADAPTIVE SWEEP ......CCW

Adjust R14 fully CW\*.

6. Clock S1, and the logic should go to state 010.

- u. State 010
  - 1. Clock S1 and observe no change of state.
  - 2. Check for the following levels:

A3TP6: > +6 V A3TP5: 0 V ± .1 V A3TP8: +9.5 V ± .5 V

Collector A3Q16: - 9.9 V ± .1 V

3. Reposition:

RESOLUTION BANDWIDTH ..... 1 Hz

- 4. Connect an oscilloscope to the collector of A3Q11. Wait 5 seconds. The voltage should be a TTL HIGH ( $\geq$  2 V dc).
- 5. Adjust A3R14 fully CCW\*.
- 6. While watching the oscilloscope clock S1. The oscilloscope should indicate a TTL LOW (< .8 V) for a few seconds and then return HIGH. The logic state should be 011.

#### v. State 011

1. Check the following levels:

A3TP5:  $0 \text{ V dc} \pm .1 \text{ V}$ A3TP6:  $0 \text{ V dc} \pm .1 \text{ V}$ 

Collector of A3Q16:  $-9.9 \text{ V} \pm .1 \text{ V}$ A3TP8: TTL LOW ( < .8 V)

2. Reposition:

RESOLUTION BANDWIDTH ... 100 Hz

- 3. Clock S1 and the control logic should go to state 001.
- w. State 001
  - 1. Clock S1 and observe no change of state.
  - 2. Check the following levels:

Collector A3Q16: - 9.9 V ± .1 V

A3TP5: <-7 V

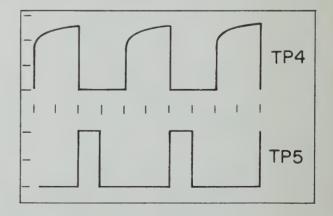
A3TP8: TTL LOW (< .8 V)

- 3. Adjust A3R14 fully CW\*. Clock S1, and the control logic should go to State 101.
- x. Adjust R14 so that the voltage at A3TP11 is at the transition between a plus and minus voltage.
- y. Remove all test leads and replace the clock jumper. The 3580A should sweep normally. The penlift relay should "click" in single sweep mode and the output of the A3 RAMP GENERATOR (A3TP1) should be +5 volts nominal for a front panel display indication at the right graticule. If the LOG SWEEP mode will not work, see the A3 schematic notes.

#### 7-12. A7 Board.

- 7-13. The A7 Board (03580-66507) is available as a rebuilt exchange board (03580-69507) through your local -hp-Sales and Service Office. Many times, however, the board can be repaired without purchasing an exchange board. The following procedure will aid in determining whether the A7 board or the analog circuits preceeding the A7 board are at fault.
- a. Connect the 3580A X-AXIS output on the rear panel to the X deflection EXT INPUT of an oscilloscope. A scope with variable persistance works best but is not absolutely necessary. Connect the 3580A Y-AXIS output to the vertical input of the scope. This procedure effectively half splits the 3580A for troubleshooting purposes.
  - If the signal seen of the scope is correct and the signal seen on the 3580A display is incorrect then the problem is in the A7, A8, or A13 boards. If the signal seen on the scope is incorrect DO NOT troubleshoot the A7 board until repairs are made to preceeding circuitry. (See Functional Block Diagram in the Operating and Service Manual.)
  - 2. If the scope presentation is good but the 3580A display is incorrect, check A7TP1. If the presentation is bad there then troubleshoot the A7 board, otherwise troubleshoot A8 or A13.
- 7-14. Troubleshooting the A7 board.
- a. Check A7Q2, Q4, Q6, Q8, and Q9. If these parts are P/N 1853-0098 replace all 5 of them with P/N 1853-0010. The new type is much more reliable and is being used in all instruments with serial numbers above 1415A01276.
- b. Check A7TP4 and A7TP5. They should look similar to the figure shown below.

Horiz 2  $\mu$  sec/div Vert .2 V/div (with 10:1 probe)



The frequency must be 55 K - 70 kHz! If the frequency is off check A8TP9. The clock frequency is determined by the A8 board.

Model 3580A Section VII

c. Clean the A7 board connector with alcohol and see if this eliminates the problem.

- d. Flex the board slightly. Occasionally the mounting screws on the A7 board apply pressure in such a way as to intermittantly open traces.
- e. If random glitches appear on the display try holding in the CLEAR WRITE button. If the glitches are still present probably a RAM is bad. Short pin 12 of each RAM to ground one at a time. When the glitches disappear replace that RAM.
- f. Depress the STORE button, and then release STORE (depress again). If the display appears to shift one or more centimeters then replace A7U58.
- g. If an unnatural phenomenon appears repedatively at the same location on the 3580A display (may appear intermittantly) try paralleling the black 99.25 K resistors with a 50 K resistor one at a time. When the 50 K resistor creates an anomaly at the same point, replace the paralleled 99.25 K resistor.
- h. Verify that the CLOCK (A7 pin 8, waveform 2) is present. Also, verify that all the internal clocks are operating as indicated by the clock waveforms supplied with the A7 schematic. If these are working properly, check the Y-AXIS A to D and X-AXIS A to D and output D to A for proper operation.
- 7-15. The two A to D converters are basically counters which count up or down until their digital output is equal to the analog input. The digital output is fed back around to the input via a D to A converter. This feedback signal is then compared with the input signal to control the count of the A to D. By verifying that the feedback signal of the A to D converter is approximately equal to the input signal, the converters can be tested. This feedback signal is available at TP2 and TP3 of the X-AXIS A to D and Y-AXIS A to D respectively. Use MANUAL SWEEP mode when checking these converters.
- 7-16. The output D to A converter (U53, U61 to U63, and associated resistors) should also be checked for proper operation. It is basically a summing device which converts the digital output from the memory into currents proportional to their digital value. U53 sums these currents into an analog signal present at A7TP1. By using a small FREQ. SPAN/DIV (5 Hz) and a wide bandwidth (300 Hz), the memory can be loaded with a constant value so that the input to the D to A is a constant. Use A4TP4 to determine the input signal level to the memory of the A7 board, and test for proper output.
- 7-17. As a last test, verify that the U56 and U57 binary counters are receiving a clock pulse at pin 15, and that they are counting.
- 7-18. If these tests fail, it is probably best to exchange your board for a rebuilt exchange board (03580-69507).

This board is available through your local -hp- Sales and Service Office. Exchange credit will be given if you return your original 03580-66507 or 03580-69507 board. Please remember the A7 board uses CMOS integrated circuits extensively and proper handling is important. DO NOT return A7 boards in a plastic bag.

#### 7-19. High Voltage Power Supply.

- 7-20. The A11A1 and A11A2 High Voltage Power Supply boards operate in conjunction with the feedback control circuitry on the A8 board to produce the regulated high voltage for the CRT. One winding of the high voltage transformer (A11A2T1) is further used to produce the + 158 V dc supply for the Deflection Amplifiers. The + 158 V dc regulator is located on the A8 board.
- 7-21. The high voltage transformer is driven by the high voltage oscillator consisting of A8Q21 and associated circuitry. Oscillation is sustained by positive ac feedback from a tertiary winding on the transformer to the base of A8Q21. Note that the 55 kHz to 65 kHz signal from the collector of A8Q21 serves as the primary clock for the Sweep Generator (A3) and Digital Storage (A7) boards.
- 7-22. The high voltage output level is determined by the drive level of the high voltage oscillator. This is controlled by dc feedback from the CRT cathode supply. The feedback voltage is fed through divider resistors A11A2 R5 and R6 and applied to the A8 board (A8J1) through a flying red lead. To prevent damage to the high voltage supply, a safety interlock disables the high voltage oscillator when the feedback lead is unplugged from A8J1. On the A8 board, the feedback voltage is processed by control amplifiers Q23 and Q22 and applied to the base of A8Q21 through the tertiary feedback winding of the high voltage transformer.
- 7-23. The voltage at the cathode of the CRT (CRT pin 2) is normally about 2,900 V and is not critical. Note, however, that the intensity grid voltage (CRT pin 3) cannot be more than 30 or 40 volts more negative than the cathode voltage. If it is, the display will be blanked.

### WARNING

Do not attempt to measure the difference between the cathode and intensity grid with a floating voltmeter. Measure the absolute voltage at each point with a high voltage probe and then calculate the difference. These voltages can cause serious injury or even death if proper care is not taken.

7-24. The A11A1 and A11A2 boards have dangerous voltages which make troubleshooting both hazardous and difficult. Generally, the safest and most efficient approach is to remove all power from the 3580A and check these boards with an ohmmeter. Note that A11A1 CR1 and CR2

each contain many diodes in series and their forward resistance (as measured with -hp- Model 412A) can be as high as 50 megohms while their leakage (reverse) resistance will generally be about 100 megohms. The primary windings of the high voltage transformer and the CRT heater windings have a dc resistance of only a few ohms. The other two secondary windings have dc resistances of 100 to 200 ohms.

#### 7-25. Crystal Replacement.

7-26. If it is found that the A5 filters or A2 crystal oscillator need a new crystal, the crystal cannot be exchanged individually but must be exchanged as a matched set of crystals and resistors. For this reason, the 03580-69505 exchange assembly, and 03580-69515 replacement assemblies are available. These assemblies consist of:

Item	Qty	Description
1	1	A5 IF Filter Board, 03580-66505 (Exchange Ass'y, 03580-69505, contains a rebuilt A5 Board; Replacement Ass'y, 03580-69515 contains a new A5 Board).
2	1	0410-0480 Crystal Set (This is a matched set of six crystals. Five of the crystals are already part of Item 1; the sixth cyrstal is for the A2 Tracking Oscillator).
3	1	A resistor matched to the sixth crystal supplied by Item 2.

7-27. If you need a new crystal, order the exchange or replacement assembly through your local -hp- Sales and Service Office. Exchange credit can only be given if you return both your old 03580-66505 board and the appropriate crystal and matching resistor from the A2 board. Always use care when removing these crystals, as undue stress on the leads can damage the glass encapsulation.

#### NOTE

This 03580-69505 exchange assembly is intended as an aid in crystal replacement. It is not intended to be used in place of repairing other components on the A5 board (03580-66505). The 03580-69515 replacement assembly is provided for those who want to purchase a new A5 Assembly and do not wish to use the exchange program.

#### 7-28. CRT Replacement.

WARNING

Use care when handling the CRT. Undue stress can cause dangerous implosion of the tube.

When shipping the CRT, follow the shipping instructions outlined in the Cathode Ray Tube Warranty information at the beginning of this manual.

7-29. If it is determined that the CRT needs replacement, fill out the Cathode-Ray Tube Failure Report supplied at the beginning of this manual. To remove the CRT, use the following procedure:

- a. Remove the front panel bezel (black hood).
- b. Remove the metal support and plastic lens (under bezel).
- c. Remove the rear protective CRT cover (on rear panel).
  - d. Remove the CRT rear tube socket.
  - e. Remove the bottom instrument cover.
- f. Through a hole in the left side, at the rear of the instrument, unscrew the CRT neck clamp using a long shaft screwdriver.
- g. Slide the CRT out. This may require moderate force. On instruments which have been used extensively, it may be necessary to cut the white CRT mounting tape to separate the CRT from the CRT tube shield. This tape is located on the top and bottom of the CRT, one inch to the rear of the CRT face.

Send the CRT and Failure Report to your local -hp- Sales and Service Office.

#### **NOTES**

- 1. If the CRT Mounting Tape is cut, replace it with a new mounting tape -hp- Part No. 0460-1115.
- 2. When reinstalling the CRT, push the CRT slightly forward while tightening the CRT neck clamp. This secures the plastic lens in front of the CRT

#### 7-30. Battery Replacement (Option 001 only).

7-31. Each of the five battery sticks can be replaced individually. Do not attempt to replace individual cells within a battery stick. When ordering a new battery stick, order either the center tapped stick (-hp- Part No. 1420-0203) or the regular stick (-hp- Part No. 1420-0202).

# ECAUTION?

Do not remove the individual battery sticks until the entire battery pack has been removed from the instrument. The battery pack can be removed by disconnecting the battery plug (P1) and removing the four screws holding the pack to the side of the instrument chassis. The individual battery sticks may short out against the sides of the instrument if the entire battery pack is not first removed.

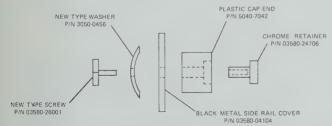
7-32. To determine which battery stick is faulty, place the 3580A on CHARGE for 14 hours and then run the 3580A on battery power until the undervoltage relays shut the battery power off. (Good batteries will run for 5 hours without a recharge). Measure the voltage across each battery stick. The nominal voltage should be approximately 5 volts per stick. Test for the stick which is lower in voltage than the other battery sticks. A bad stick will differ from the other battery sticks by .5 or more volts.

7-33. The normal warranty period on batteries is 90 days. Proper operation implys that the battery, operated under normal temperatures and load, will charge from a state of complete discharge in 14 hours, and will then power the instrument for 5 hours of continuous and normal use.

#### 7-34. Cleaning and Lubricating Rotary Switches.

7-35. Faulty switches can cause intermittent performance, spurious responses, noise, and many other annoying problems. Tests have shown that the typical operating life of a switch is 25,000 operations or more. With proper cleaning and lubrication, this life may be extended to as much as 100,000 or more operations. Freon TF cleaner (-hp- Part No. 8500-0232) is available for cleaning switches. Electrotube 2G (-hp- Part No. 5060-6086) is available for lubricating high impedance switches. Electrotube 2A (-hp- Part No. 6040-0300) is available for lubricating low impedance switches. Follow the instructions given with these cleaners, -hp- Service Note M45B (available from your local -hp-Sales and Service Office) also gives detailed information on how to use these cleaners.

**7-36.** Repairing Handles. (For S/N 1415A00975 and below) Anytime a loose or broken handle is repaired the new type screw should be used. The illustration indicates the assembly order. The screw which attaches the L shaped plastic piece remains the same.



In order to repair both handles on one instrument the following new parts are needed.

4 ea screw P/N 03580-26001 4 ea washer P/N 3050-0456

#### 7-37. FACTORY SELECTED COMPONENTS.

7-38. Certain components within the 3580A are individually selected at the factory to compensate for slightly varying circuit parameters. These components are identified by an asterisk (\*) in the parts list and schematic diagrams.

Table 7-3. Factory Selected Components.

Component	Function	Value Range
A3R88*	Controls Sweep Time/ Div. Increasing A3R88* increases sweep time Decreasing A3R88* decreases sweep time	11.8 k $\Omega$ ± 1% 1/8 W 13.7 k $\Omega$ ± 1% 1/8 W 15.4 k $\Omega$ ± 1% 1/8 W 17.4 k $\Omega$ ± 1% 1/8 W 19.6 k $\Omega$ ± 1% 1/8 W 21.5 k $\Omega$ ± 1% 1/8 W 26.1 k $\Omega$ ± 1% 1/8 W 26.1 k $\Omega$ ± 1% 1/8 W 28.0 k $\Omega$ ± 1% 1/8 W 30.9 k $\Omega$ ± 1% 1/8 W 33.2 k $\Omega$ ± 1% 1/8 W
A6R41*	Adjusts + 10 V power supply to 10 V ± ,050 V. Increasing A6R41* increases the voltage. Decreasing A6R41* decreases the voltage.	243 Ω to 1.96 kΩ 1/8 W typical: 1.05 kΩ
A7R109*	Adjusts positive pulse width at A7TP5 to 1.0 to 1.4 µsec (Rev. A) or 2.0 to 2.4 µsec (Rev. B). Increasing A7R109* increases pulse width, Decreasing A7R109* decreases pulse width.	18.2 k $\Omega$ to 63.4 k $\Omega$ 1/8 W typical: 24.3 k $\Omega$
R7R111*	Adjusts positive pulse at A7TP4 to 3,5 to 3,9 µsec, Increasing A7R111* increases the pulse width, Decreasing A7R111* decreases the pulse width.	24.9 kΩ to 41.2 kΩ 1/8 W typical: 32.4 kΩ
A11A1R2*	Gives proper intensity limit adjustment.	100 kΩ or 1 MΩ typical: 100 kΩ
A18R3*	Matches alphabetic code printed on transformer.	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$
A14R46	Adjusts range of "DIAL HI END SET" control.	11 k $\Omega$ ± 1% 1/8 W 12.1 k $\Omega$ ± 1% 1/8 W 13.3 k $\Omega$ ± 1% 1/8 W 14.7 k $\Omega$ ± 1% 1/8 W 15.4 k $\Omega$ ± 1% 1/8 W

A typical value is given for each. Table 7-3 is a list of the factory selected components, functions, and value ranges. A detailed description of selecting A3R88\* is given in Paragraph 7-39. The other components will usually not require reselection. (The crystal padding resistors are factory selected and cannot be selected in the field. See Crystal Replacement, Paragraph 7-19).

7-39. A3R88\* should be reselected if the frequency ramp integrating capacitor (C1) is changed (See Schematic 4). To select A3R88\*, select the following front panel control settings:

ADAPTIVE SWEEP					۰					OFF
SWEEP TIME/DIV								٠	1	<b>SEC</b>
SWEEP MODE						٠		۰		REP

Measure the time interval between the negative and positive voltage transition at A3TP5 with an electronic counter. For the -hp- 5326A Counter, the controls should be:



Sample Rate:

Fast

Function:

T.I. A to B .1 sec.

Multiplier: Channel A:

Slope -D.C. Atten X1

Level: set to trigger on negative

edge of pulse.

Channel B:

Slope + D.C.

Atten X1

Level: set to trigger on positive

edge of pulse.

BNC Input:

Com

The time interval should be 10.4 to 10.6 sec. The other sweep times can be easily tested at this time. The time interval should be  $10.5 \times SWEEP TIME/DIV (\pm 5\%)$ .

#### 7-40. SCHEMATIC DIAGRAMS.

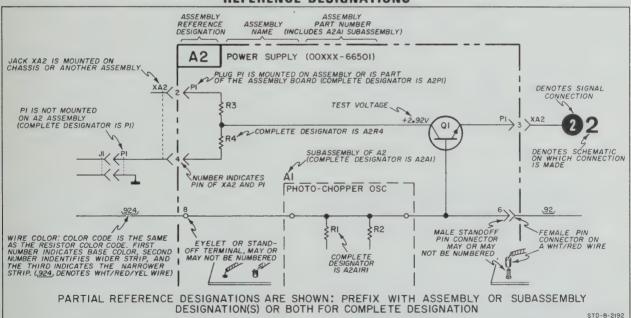
7-41. The schematic diagrams, Figure 7-3 through 7-12 show the detailed circuits of the Model 3580A. Each schematic is assigned a numerical callout (1 through 10) which is used for referencing. The schematics are arranged to provide as much signal continuity as possible and assemblies do not necessarily appear in the order of their

reference designations. Refer to Table 7-4 for a complete cross reference listing. Refer to the General Schematic Notes for further information concerning the schematic diagrams.

Table 7-4. Assembly Cross Reference.

Assembly Number	Assembly Title	Schematic Number
A2 (03581-66502)	VTO Tracking Oscillator	6
A3 (03580-66503)	Main and Log Sweep	4
A4 (03581-66504)	Detector	3
A5 (03580-66505)	IF Filter	2
A6 (03580-66506)	Low Voltage Power Supply	9
A7 (03580-66507)		
or		
(03580-69507)	Digital Storage	7
A8 (03580-66508)	Control Board	8
A9 (03580-66509)		
(Standard)	Input Circuits	1
(03580-66519)		
(Option 002)		
A10 (03580-66510	Connector Board	7
A11A1 (03580-66531)	High Voltage	8
A11A2 (03580-66532)		
and		
(03580-66537)	HV Transformer	8
A13 (03580-66513)	Deflection Amp,	8
A14 (03580-66514)	Bandwidth/Sweep Time	5
A15 (03580-66515)	Freq Span/Sweep Mode	5
A16 (03580-66516)	Combining Board	5
A18 (03581-66518, Opt.		
002 only)	Balanced Input	1

#### REFERENCE DESIGNATIONS



#### GENERAL SCHEMATIC NOTES

- PARTIAL REFERENCE DESIGNATIONS ARE SHOWN. PREFIX WITH ASSEMBLY OR SUBASSEMBLY DESIGNATION (S) OR BOTH FOR COMPLETE DESIGNATION.
- 2. COMPONENT VALUES ARE SHOWN AS FOLLOWS UN-LESS OTHERWISE NOTED.

RESISTANCE IN OHMS
CAPACITANCE IN MICROFARADS
INDUCTANCE IN MILLIHENRYS

3. DENOTES EARTH GROUND.
USED FOR TERMINALS WITH NO LESS THAN A
NO. 18 GAUGE WIRE CONNECTED BETWEEN
TERMINAL AND EARTH GROUND TERMINAL OR
AC POWER RECEPTACLE.

DENOTES FRAME GROUND.

USED FOR TERMINALS WHICH ARE PERMANENTLY CONNECTED WITHIN APPROXIMATELY

0.1 OHM OF EARTH GROUND.

5. DENOTES GROUND ON PRINTED CIRCUIT ASSEMBLY. (PERMANENTLY CONNECTED TO FRAME GROUND).

6. DENOTES ASSEMBLY.

DENOTES MAIN SIGNAL PATH.

DENOTES FEEDBACK PATH.

10. DENOTES FRONT PANEL MARKING.

11. DENOTES REAR PANEL MARKING.

12. DENOTES SCREWDRIVER ADJUST.

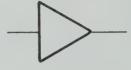
13. \*AVERAGE VALUE SHOWN, OPTIMUM VALUE SELECTED AT FACTORY. THE VALUE OF THESE
COMPONENTS MAY VARY FROM ONE INSTRUMENT TO ANOTHER. THE METHOD OF SELECTING
THESE COMPONENTS IS DESCRIBED IN SECTION V
OF THIS MANUAL.

14. DENOTES SECOND APPEARANCE OF A CONNECTOR PIN.

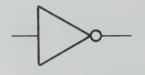
17. ALL RELAYS ARE SHOWN DEENERGIZED.

- 18. WAVEFORMS AND AC VOLTAGE MEASUREMENTS WERE MADE WITH RESPECT TO CHASSIS GROUND USING AN OSCILLOSCOPE WITH A 10:1 DIVIDER PROBE (10 MEGOHM, 10 pF). THE VOLTAGE LEVELS SHOWN ON THE WAVEFORMS ARE ACTUAL VOLTAGE LEVELS AND ARE NOT TO BE CONFUSED WITH OSCILLOSCOPE SETTING. THE VOLTAGE LEVELS SHOWN ARE NOMINAL AND MAY VARY FROM ONE INSTRUMENT TO ANOTHER. A VARIATION OF ± 10 % IN MEASUREMENTS SHOULD BE ALLOWED.
- 19. DC VOLTAGE LEVELS WERE MEASURED WITH RESPECT TO CIRCUIT GROUND USING A VTVM WITH 10 MEGOHM

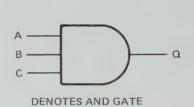
INPUT IMPEDANCE. THE VOLTAGE LEVELS SHOWN ARE NOMINAL AND MAY VARY FROM ONE INSTRUMENT TO ANOTHER DUE TO CHANGE IN TRANSISTOR CHARACTERISTICS. A VARIATION OF ± 10 % SHOULD BE ALLOWED.



**DENOTES BUFFER** 

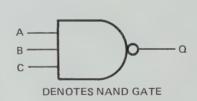


**DENOTES INVERTER** 



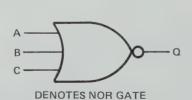
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	0
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	1

ABCQ



- (	J	0	U	1	
(	О	0	1	1	
(	0	1	0	1	
(	0	1	1	1	
	1	0	0	1	
	1	0	1	1	
	1	1	0	1	
	1	1	1	0	

в С



U	U	U		
0	0	1	0	
0	1	0	0	
0	1	1	0	
1	0	0	0	
1	0	1	0	
1	1	0	0	
1	1	1	0	

A B C Q



0 0 0 0 1 1 1 0 1 1 1 0

A B Q

DENOTES EXCLUSIVE OR GATE

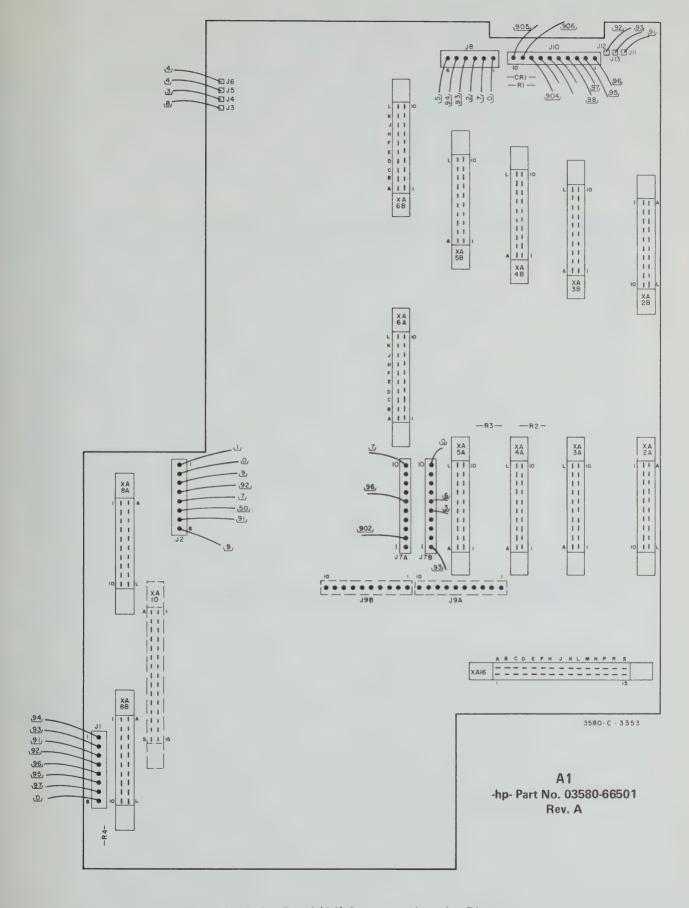


Figure 1. Mother Board (A1) Component Location Diagram.



#### GENERAL WAVEFORM INFORMATION

3580A Control Settings

☆-☆ ◎☆-☆

These woveforms were made with the 3580A INPUT SENSITIVITY which in the CAL position. The FREQUENCY and SWEEP controls are in the MANUAL mode: 300-14: Bendwidth; and adjusted to read the 10 KHz hardmone of the CAL signal. The values given are those to 10 kHz hardmone of the CAL signal. The values given are those to 10 kHz hardmone of the CAL signal. The values given are those 10 kHz hardmone of the CAL signal. The values given as which the deViLIN of the deViLIN with mode is given to the deViLIN with mode.

(For Option 002 set the dBm 900  $\Omega$ /LIN - dBm 600  $\Omega$  switch to dBm 900  $\Omega$ . Waveform  $\Omega$  is of slightly less magnitude for Option 002 instruments when using the LOG mode.)

These waveforms were made with the 3580A front panel controls adjusted as for ows

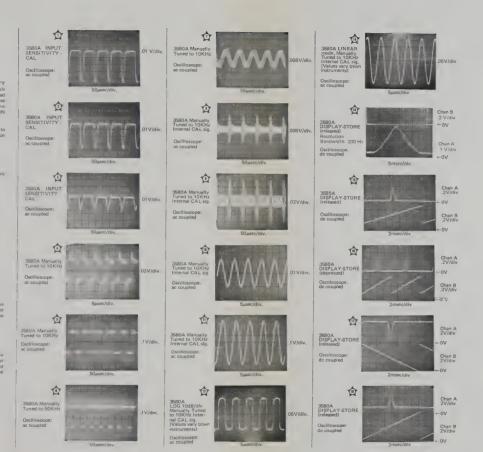
ADAPTIVE SWEEP	OF.
DISPLAY	BLANK STORE released
	STORE as indicated
AMPLITUDE MODE	LOG 10 d8v/DD
AMPLITUDE REFLEV	ELNORMA
	dBv/LIP
	(Fully CW
FREQUENCY	10.0 KH
START CTR	CTF
RESOLUTION BANDW	1DTH30 H
DISPLAY SMOOTHING	
FREQ SPAN DIV	0.5 KH
SWEEP TIME DIV	1 SE(
SWEEP MODE	

Option 002: Set dBm 900  $\Omega/\text{LIN}$  - dBm 600  $\Omega$  switch to dBm 900  $\Omega$ ; set INPUT MODE switch to UNBAL.

The front panel ZERO CAL was adjusted to give a display of the 10 KHz CAL signal in the center of the screen, it is easiest to set this adjustment in the MANUAL mode, and then switch to the REPetitive sweep mode to measure the waveforms.

#### Oscilloscope Settings

All visinforms were recorded using a 10.1 divider probe on the oscilloscope inputs. The vertical amplifue sensitivity is the actual amplifue setting and does not include the K10 multiplet introduced by the probe. All dual traces were made with the ascilloscope in the chopped mode and triggered by Channel S.



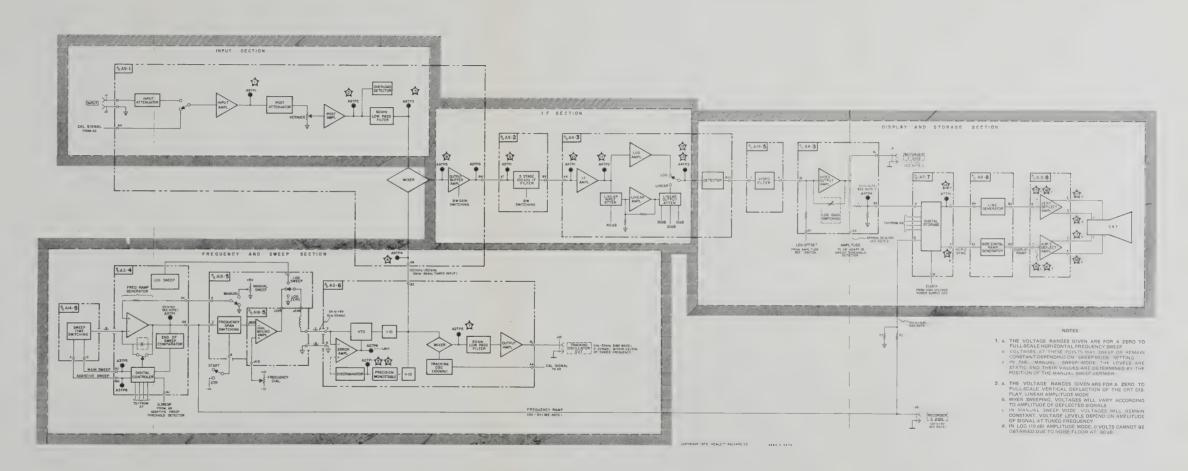




Table 1, Input Circuit Amplitude Levels and Gains for Full Scale Sine Wave Inputs. (Amplitude Ref Level - Normal). Note: All voltages in RMS.

IMPUT SENSITIVITY	+ 30 dB/20 V	+ 20 dB/10 V	+ 10 d	8/2 V	0 d8/1	V	10 d8/	02 V	20 d8/	01V	- 30 dB/20 mV	40 d8/1	DmV	- 50 dB/	Vm S	- 60 dB	Vm t	- 70 dB/I	12 11
Maximum Input Log (dBm 600 Ω) Log (dBM) Linear Log (dBm 900 Ω)*	24.5 V 31.6 V 20.0 V 30.0 V	7 75 % 10 00 % 10 00 %	3 16	V	776 1 000 1 000 949	V	245 316 200 300	V	77.5 1 1 94.9	mV V W	24.5 mV 31.6 mV 20.0 mV 30.0 mV	7 75 10 00 10 00 9.49	mV	2 45 3 16 2.00 3.00	mV mV	.775 1.000 1.000 .949	mV mV	.316	5 m 6 m 0 m
Input Attenuator Input Attenuator Out (Gate of ASO1) Log (d8m 600 Ω) Log (d8m) Linet Linet Log (d8m 900 Ω)*	- 60. d8 24 5 mV 31 6 mV 20 0 mV 30 0 mV	40 di 77.5 m\ 1 \ 1 \ 94.9 m\	24.5 31.6 20.0	mV mV mV	- 20 77.5 1 1 94.9	dB mV V V	20 24.5 31.6 20.0 30.0	dB mV mV mV	0 77.5 1 1 94.9	dB mV V V	0 d8 24.5 mV 31.6 mV 20.0 mV	77.5 10.0 10.0 9.49	mV mV	0 2 45 3 16 2 00 3.00	mV mV	1 000		200	6 m 6 m
Input Amp Gain Input Amp Out (A9TP1) Log (dBm 600 11) Log (dBv) Linear Log (dBm 900 12)*	1.8 d8 J0.1 mV 38.9 mV 24.6 mV 36.9 mV	1.8 d8 95.6 m\ 123 \ 123 \ 117 \	30 1 38 9 24 6	mV mV mV	1.8 95.6 123 123 117	V	1.8 30 1 38 9 24 6 36 9	mV mV mV mV		d8 mV 3 V 3 V	1.8 d8 30 f mV 38 9 mV 24 6 mV 36.9 mV	9.56 12.3 12.3 11.7	mV mV	1.8 3.01 3.89 2.46 3.69	mV mV	9.56 12.3 12.3 11.7	mV mV mV mV	3,01 3,89 2,46 3,69	n
Post Attenuation Log IdBm 600 (1) Log IdBm (000 (1) Linear Log IdBm 900 (1) *	28 d8 50 d8 52 d8 46 d8	- 12.8 dE 15.0 dE 15.0 dE 14.6 dE	5.0	d8 d8 d8	12 8 15 0 15 0 14 6	d8 d8 d8	28 50 52 46	d8 d8 d8	12.8 15.0 15.0 14.6	d8 d8 d8	28 d8 50 d8 52 d8 46 d8	12.8 15.0 15.0 14.6	86 68 69 68	28 50 52 46	d8 d8 d8 d8	- 12.8 - 15.0 - 15.0 - 14.5	d8 d8 d8	- 2.8 - 5.0 - 5.2 4.6	0
Post Attenuator Out IBlase A906 Log arribat	21.8 mV	21.8 m\		mV m>	21.8	mV mv	21.8 13.5	mV inv	21.8 21.8	m∀ nV	21.8 mV 13.5 mV	2 18 2 18		2 18 1 35		2 18 2 18		2 18 1 35	
Post Amp Gain Post Amp Out (ASTP?) Log Linear	13.2 d8 100 mV 61.9 mV	13.2 de 100 m\ 100 m\	100	d8 mV mV	13.2 100 100	d8 mV mV	13.2 100 61.9	d8 mV mV	13.2 100 100	d8 mV mV	13.2 dB 100 mV 61.9 mV		d8 mV mV	33.2 100 61 9	dB mV mV	33.2 100. 100.	d8 mV mV	33.2 100 61.9	-
Low Page Filter Out (ASTP3) Log United	50 mV 30.8 mV	90 m1		mV mV	50 50	mV	50 30.8	m∀ m∀	50 50	m∀ m∀	50 mV 30.8 mV		mV mV	50 30.8	mV mV	50. 50.	mV mV	60. 30.8	
Total Garn Log IdBm 600 Ω) Log IdBV? Log IdBV9 123*	47.8 d8 50 d8 7 : 8 49.6 d8	37.8 d8 -40 d8 17 d8	30	d8 d8	17.8 20 20 20	d8 d8 d8	7.8 10 10.2 9.6	dB dB dB	* 22 0 0 * 04	dB dB dB	+ 12 2 d8 + 10 d8 + 98 d8 + 10 4 d8	+ 22 2 + 20 + 20 + 20 4	d8 d8 d8	+ 32 2 + 30 + 29 8 + 30 4	dB dB dB	+ 42.2 + 40. + 40.4	d8 d8 d8	+ 52.2 + 50 + 49.8 + 50.4	

Table 4. Linear and Log Offsets.

Table 2. Approximate IF Level Changes with Bandwidth. (Full Scale Sine Wave Input, LOG Mode, Manually Tuned to Input Frequency)

Bandwidth	I.F. Input A9TP6, A5TP1	I.F. Output A5 pin B9, A4TI			
300 Hz	640 mV p-p	420 mV p-p			
100 Hz	640 mV p-p	420 mV p-p			
30 Hz	325 mV p-p	180 mV p-p			
10 Hz	325 mV p-p	140 mV p-p			
3 Hz	110 mV p-p	60 mV p-p			
1 Hz	110 mV p-p	50 mV p-p			

Full Scale	MAXINPUT (INPUT SENSITIVITY Switch)	AMPLITUDE REF LEVEL	A4TP2	Input Attenuation	Input Amp Gain	A4U6 pin 6	Output Attenuation	A4 pin 7, A9 pin 3 (LIN GAIN)	Output Amp Gain	(Appr. Value)
1 V rms .2 V rms .1 V rms .02 V rms .01 V rms 2 mV p-p 1 mV p-p .2 mV p-p	1 V 1 V 1 V 1 V 1 V 1 V 1 V	X1 X.5 X 1 X.05 X.01 X.005 X 001 X.0005	6.7 V p-p 1.32 V p-p .67 V p-p .132 V p-p .067 V p-p	- 40 dB - 40 dB - 40 dB - 40 dB - 40 dB - 0 dB - 0 dB - 0 dB	+ 40 dB + 40 dB	6.7 V p-p 1.32 V p-p .67 V p-p .132 V p-p 6.7 V p-p 1.32 V p-p .67 V p-p .132 V p-p	- 40 dB - 30 dB - 20 dB - 10 dB - 40 dB - 30 dB - 20 dB - 10 dB	28 V dc + 4.3 V dc 28 V dc + 4.3 V dc 28 V dc + 4.3 V dc 28 V dc + 4.3 V dc + 4.3 V dc	+ 34 dB (X50) + 38.2 dB (X81) + 34 dB (X50) + 38.2 dB (X81) + 34 dB (X50) + 38.2 dB (X81) + 34 dB (X50) + 38.2 dB (X81)	3.4 V p-5 3.4 V p-5 3.4 V p-5 3.4 V p-5 3.4 V p-5 3.4 V p-5 3.4 V p-5

Table 3. Approximate Full Scale Levels in Linear Amplifier (LINEAR Mode, Full Scale Input, Manually Tuned to Input Frequency).

MODE	REF LEVEL	A4 pin B7
LOG LOG LOG LOG LOG LOG LOG	0 -10 -20 -30 -40 -50 -60	- 2.50 V dc - 2.25 V dc - 2.00 V dc - 1.75 V dc - 1.50 V dc - 1.25 V dc - 1.00 V dc - 0.75 V dc
LINEAR	Any Setting	- 2.50 V dc



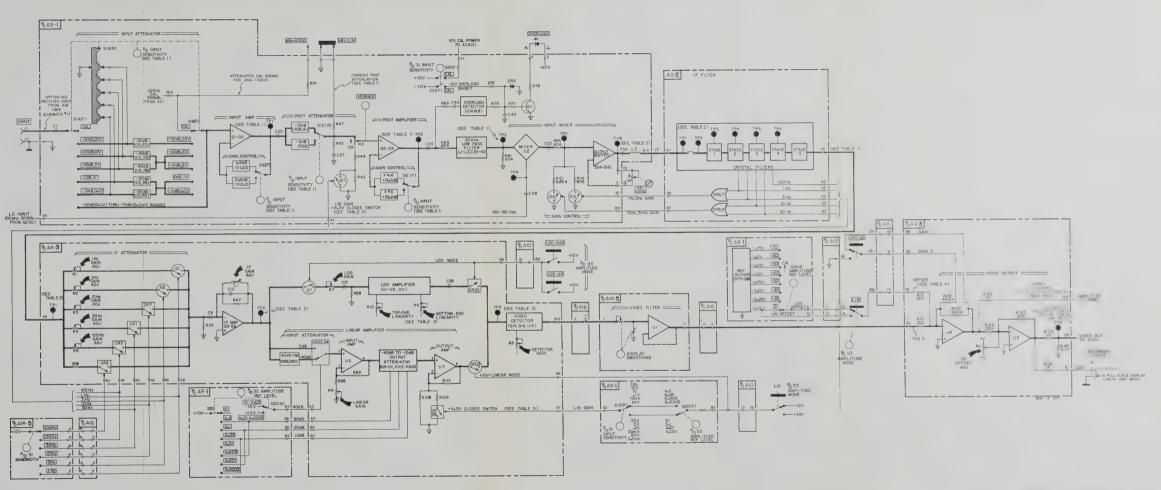
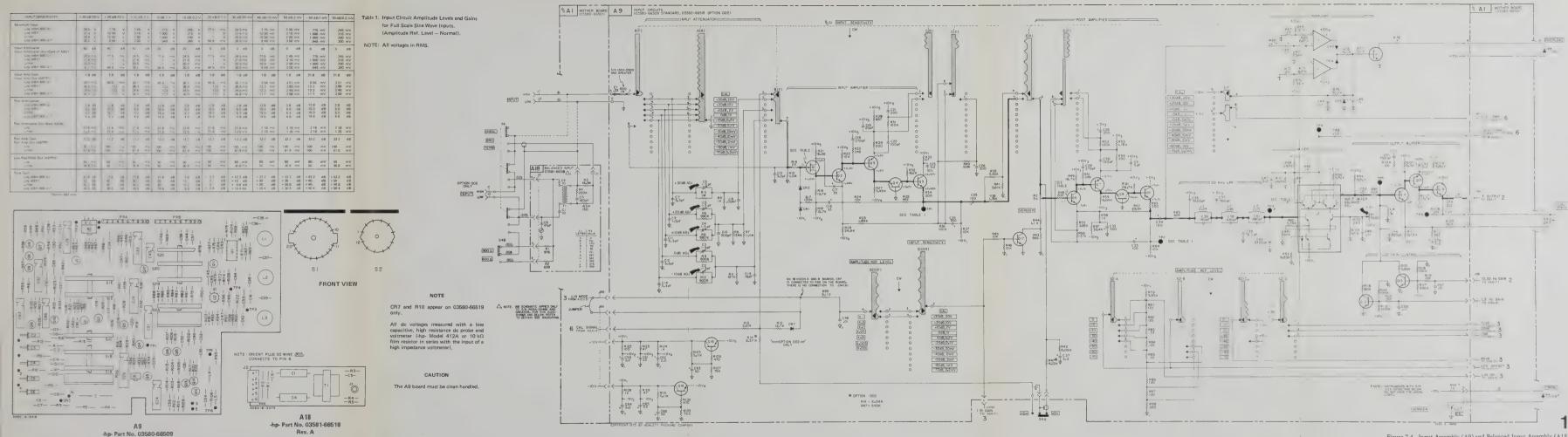


Figure 7-3. Amplitude Section Detailed Block Diagram.





Rev. C



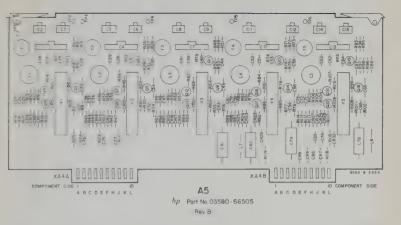
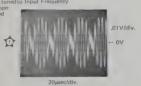


Table 1. I.F. Input Level Change With Bandwidth. (Full Scale Sine Wave Input, Log Mode, Manually Tuned to Input Frequency)

Bandwidth	A5TP1
300 Hz	640 mV p-p
100 Hz	640 mV pp
30 Hz	325 mV p-p
10 Hz	325 mV p-p
3 Hz	110 mV p-p
1 Hz	110 mV p-p

3580A Full Scale Input Log Mode, 300 Hz Bandwidth Manually tuned to Input Frequency Oscilloscope: dc coupled



A 10:1 divider probe was used on the oscilloscope input. The vertical amplitude sensitivity is the actual amplifier setting and does not include the X10 multiplier introduced by the probe.

Table 2. I.F. Output Level Change With Bandwidth. (Full Scale Input, Log Mode, Manually Tuned to Input Frequency)

Bandwidth	A5 pin B9				
300 Hz	420 mV p·p				
100 Hz	410 mV p-p				
30 Hz	180 mV p-p				
10 Hz	140 mV p-p				
3 Hz	60 mV p-p				
1 Hz	50 mV p-p				

NOTE 1: AC voltage readings were taken with an oscilloscope equipped with 10:1 divider probes. Some loading occurs during the measurement, so values are approximate. The values given correspond to a full scale input (LOG MODE, 300 Hz BANDWIDTH). The 3580A must be manually tuned to the input frequency. See Table 1 and Table 2 for level changes with Bandwidth.

NOTE 2: DC levels taken with a low capacitive, high resistance dc probe and voltmeter (-hp- 412A or 10 kΩ film resistor in series with the input of a high impedance voltmeter).

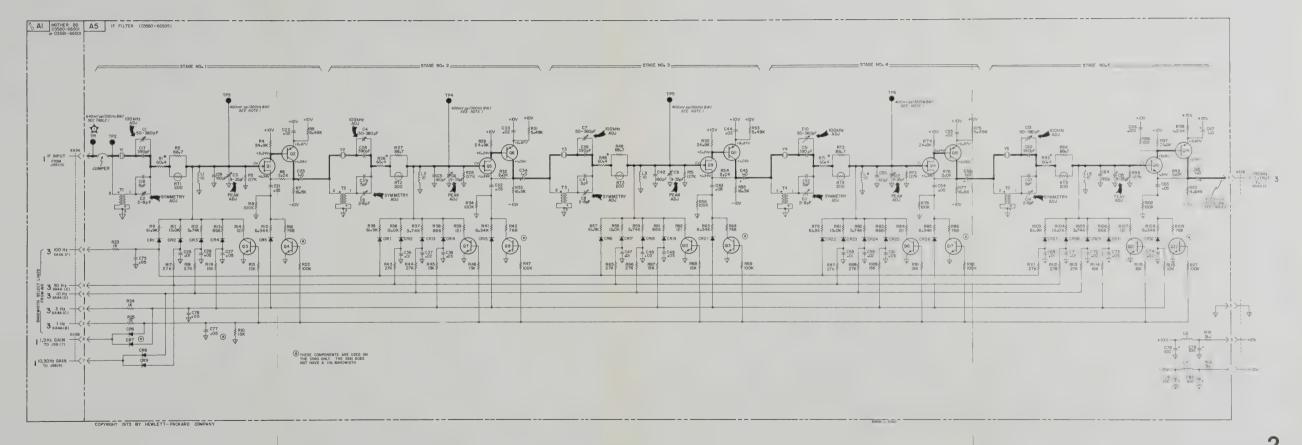


Figure 7-5. IF Filter Assembly (A5) Schematic and Component Location Diagram.

7-17/7-18



Full Scale Input	MAX INPUT (INPUT SENSITIVITY Switch)	AMPLITUDE REF LEVEL	A4TP2	Input Attenuation	Input Amp Gain	A4U6 pin 6	Output Attenuation	A4 pin 7, A9 pin 3 (LIN GAIN)	Output Amp Gain	(Appr. Value)
1 V rms	1 V	X1	6.7 V p-p	- 40 dB	+ 40 dB	6.7 V p-p	- 40 dB	28 V dc	+34 dB (X50)	3.4 V p-p
2 Virms	1 V	X.5	1,32 V p-p	- 40 dB	+ 40 dB	1.32 V p-p	- 30 dB	+4.3 V dc	+ 38.2 dB (X81)	3.4 V p-
1 V rms	1 V	X 1	.67 V p-p	- 40 dB	+ 40 dB	.67 V p-p	- 20 dB	- 28 V dc	+ 34 dB (X50)	3.4 V p-
02 V rms	1 V	X.05	.132 V p-p	- 40 dB	+ 40 dB	.132 V p-p	- 10 dB	+4.3 V dc	+ 38.2 dB (X81)	3,4 V p-1
01 V rms	1 V	X.01	.067 V p-p	0 dB	+ 40 dB	6.7 V p-p	- 40 dB	- 28 V dc	+ 34 dB (X50)	3.4 V p-
2 mVpp	1 V	X.005		0 dB	+ 40 dB	1.32 V p-p	- 30 dB	+4.3 V dc	+ 38.2 dB (X81)	3.4 V p-
1 mV p-p	1 V	X,001		0 dB	+ 40 dB	.67 V p-p	- 20 dB	- 28 V dc	+ 34 dB (X50)	3.4 V p-
2 ml pp	1 V	X.0005		0 dB	+ 40 dB	.132 V p-p	- 10 dB	+4.3 V dc	+ 38.2 dB (X81)	3,4 V p-(

Table 1. Approximate Full Scale Levels in Linear Amplifier (LINEAR Mode, Full Scale Input, Manually Tuned to Input Frequency).

AMPLITUDE MODE	AMPLITUDE REF LEVEL	A4 pin B7
LOG	0	- 2.50 V do
LOG	-10	1 - 2.25 V do
LOG	- 20	-2.00 V do
LOG	- 30	1 - 1.75 V do
LOG	- 40	- 1.50 V do
LOG	- 50	1 - 1.25 V do
LOG	- 60	- 1.00 V do
LOG	- 70	-0.75 V do
LINEAR	Any Setting	- 2.50 V do

Table 2. Linear and Log Offsets.

Bandwidth	A4TP1
300 Hz	420 mV p-p
100 Hz	420 mV p-p
30 Hz	180 mV p-p
10 Hz	140 mV p-p
3 Hz	60 mV p-p
1 Hz	50 mV p-p

Table 3. A4 Input Level with Bandwidth Change.
(LOG Mode, Full Scale Input)

NOTE 1: The values given are for a full scale input, LOG Mode. The 3580A must be manually tuned to the input frequency.

NOTE 2: DC Levels taken in LOG Mode, Full Scale Input. Use a low capacitive, high resistance dc probe and voltmeter (-hp--Model 412A or  $10~\mathrm{k}\Omega$  film resistor in series with the input of a high impedance voltmeter).

NOTE 3: R1228\* is selected to provide a full scale output of 5 V ± 50 mV at the Y AXIS output.

NOTE 4: The attenuation is not equal to the Amplitude Ref Level switch settings (see Table 1).

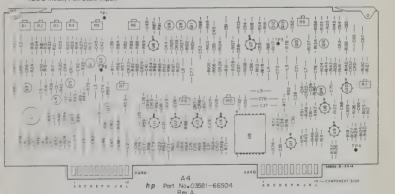
#### GENERAL WAVEFORM INFORMATION

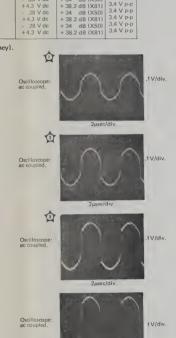
#### 3580A Control Settings

Manually tune the 3580A to the 10 KHz harmonic of the internal CAL signal. Values given are for a full scale display indication, LOG Mode.

#### Oscilloscope Settings

All waveforms were recorded using a 10:1 divider probe on the oscilloscope inputs. The vertical amplitude sensitivity is the actual amplifier setting and does not include the X10 multiplier introduced by the probe.





uc coupled, (values vary from instrument to in-

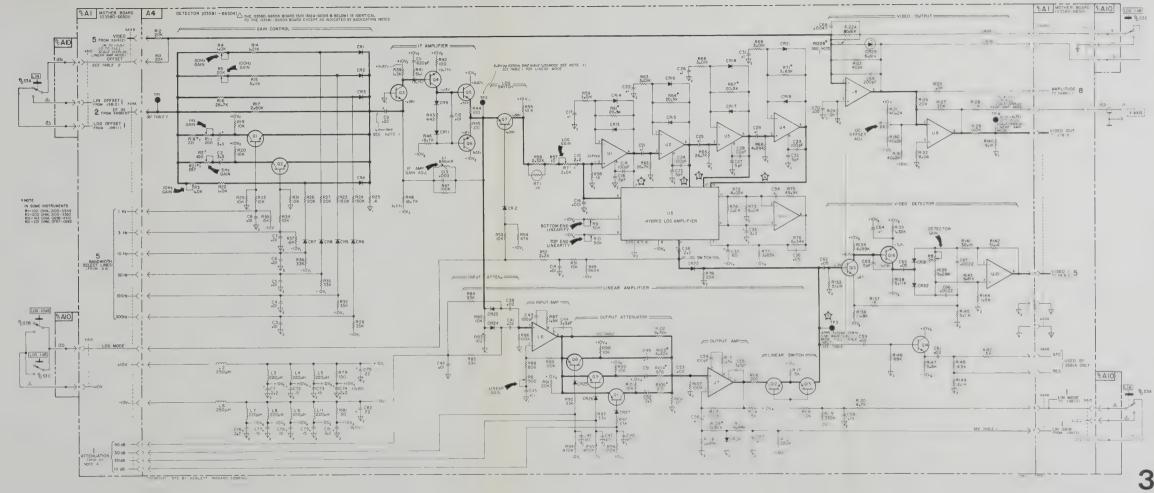
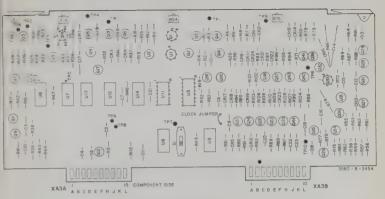


Figure 7-6. Detector Assembly (A4) Schematic and Component Location Diagram.

7-19/7-2





#### A3 -hp- Part No. 03580-66503 Rev. B

80A	
ADAPTIVE SWEEP	
DISPLAY All	pushbuttons released
FREQUENCY	. 00 0 kHz
START - CTR	START
DISPLAY SMOOTHING	MIN
RESOLUTION BANDWIDTH	
FREQ. SPAN/DIV	5 KHz
SWEEP TIME/DIV	0.01 SEC
SWEEP MODE	

#### OSCILLOSCOPE

DC coupled, dual trace (chopped), triggered by Channel B.



A 10:1 divider probe was used on the oscilloscope input. The vertical amplitude sensitivity is the actual amplifier setting and does not reflect the X10 multiplier introduced by the probe.

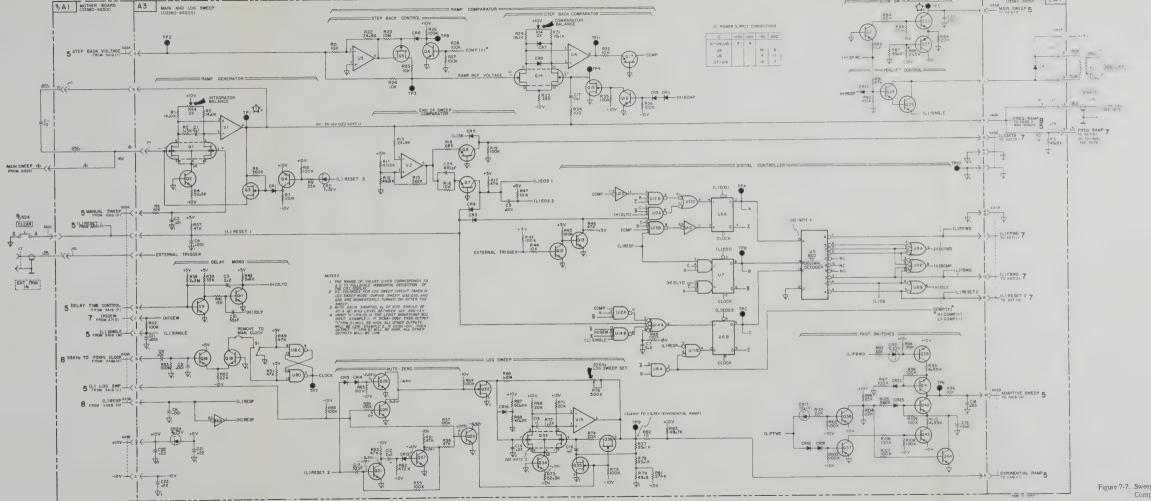


Figure 7-7. Sweep Generator (A3) Schematic and Component Location Diagram.

7 21

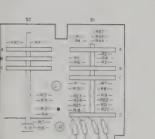




FRONT VIEW
PIN POSITIONS FOR
AI4SI



PIN POSITIONS FOR AI4S2 AI5S1



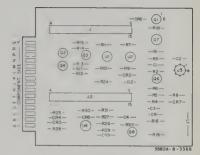
COMPONENT SIDE 15
A B C D E F H J K L M N P R S
A 15
hp Part No 03580 - 66515

NOTE 1

WHENEVER REPLACING A14U1, USE hp. PART NO. 1826-0304 AND REMOVE CAPACITOR A14C16 (IF PRESENT).





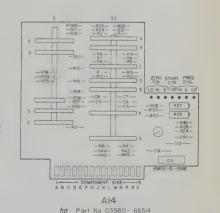


A16

hp Part No. 03580-66516

Rev A

A14 plugs into J1 of A16. A15 plugs into J2 of A16.



Rev B

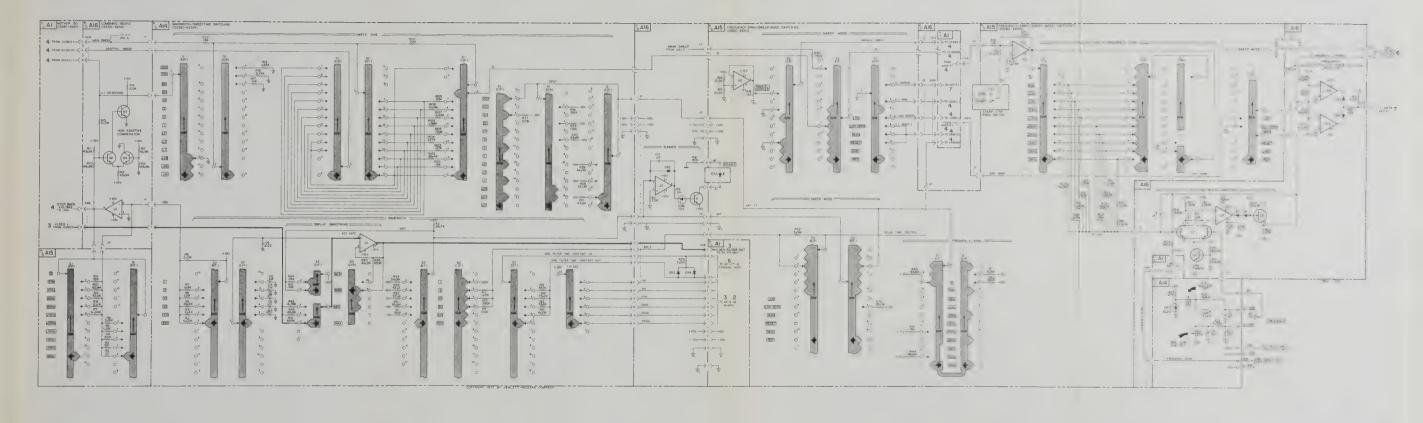
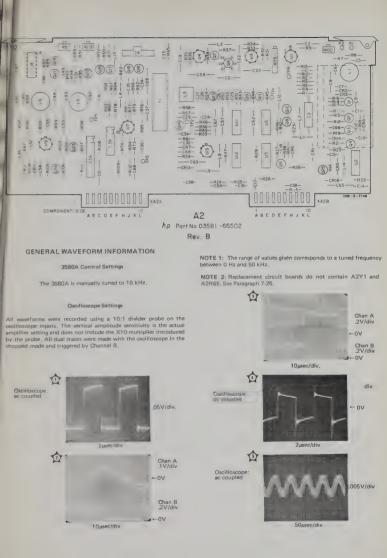


Figure 7-8. Frequency Control Circuits (A14, A15, A16) Schematic and Component Location Diagrams.





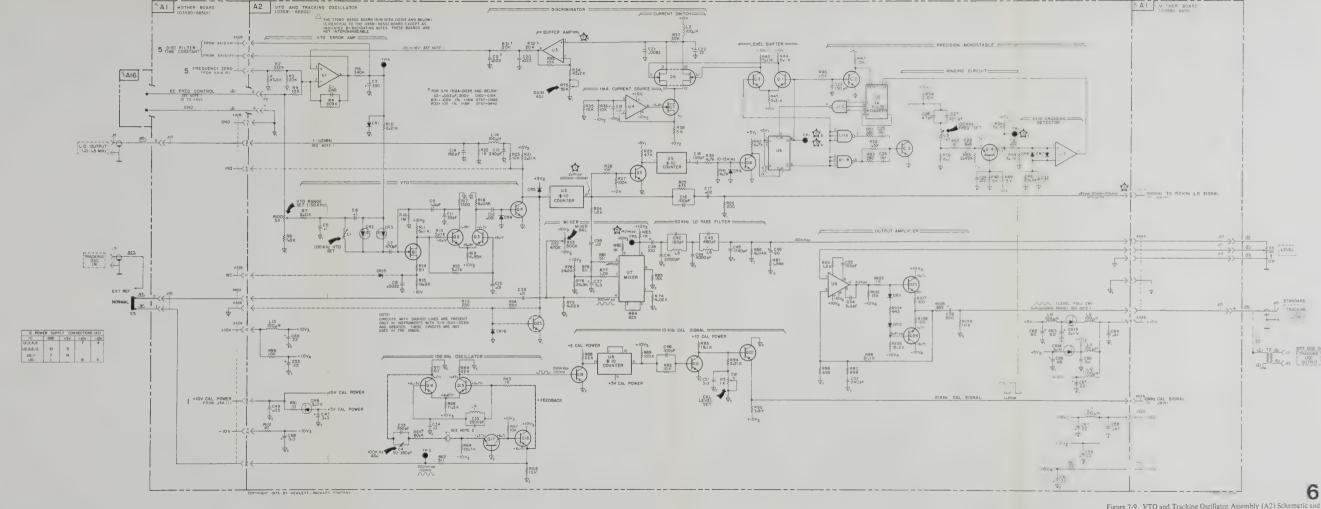
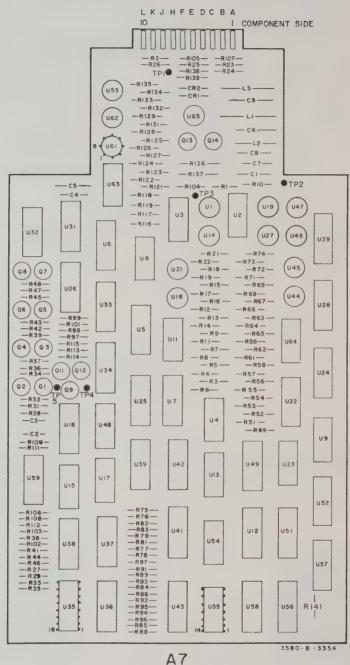


Figure 7-9. VTO and Tracking Oscillator Assembly (A2) Schematic and Component Location Diagram.



hp Part No. 03580-66507 Rev. C

#### GENERAL WAVEFORM INFORMATION

3580A	
ADAPTIVE SWEEP	
DISPLAY	All pushbuttons re
AMPLITUDE MODE	LOG 10 dB
AMPLITUDE REF L	EVEL . NO
dBv/LIN - dBm 600 s	ΩdE
INPUT SENSITIVIT	Υ
VERNIER (Amplitus	de)
	(Full
FREQUENCY	
START - CTR	
RESOLUTION BAN	DWIDTH
	ING
EREO SPAN/DIV	

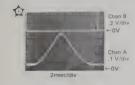
Option 002: Set dBm 900  $\Omega/LIN$  - dBm 600  $\Omega$  switch to dBm 900  $\Omega$ ; set INPUT MODE switch to UNBAL.

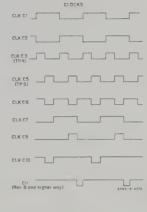
The front panel ZERO CAL and CAL 10 KHz were adjusted to give a full scale display of the 10 KHz CAL signal in the center of the screen. It is easiers to set this adjustment in the MANUAL mode, and then switch to the REPetitive sweep mode to measure the waveforms.

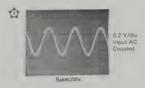
#### OSCILLOSCOPE

SWEEP TIME/DIV SWEEP MODE

10:1 Probe, Dual Trace (Chopped), triggered by Channel B. The vertical amplitude sensitivity is the actual amplifier setting and does not include the X10 multiplier introduced by the probe.







NOTE 1: The voltage range given is for a zero to full scale horizontal frequency sweep of the CRT display.

NOTE 2: The voltage range given is for a zero to full scale vertical deflection of the CRT display, LINEAR MODE. In LOG 10 dB/div Amplitude Mode, 0 volts cannot be obtained because of the -80 dB noise floor.

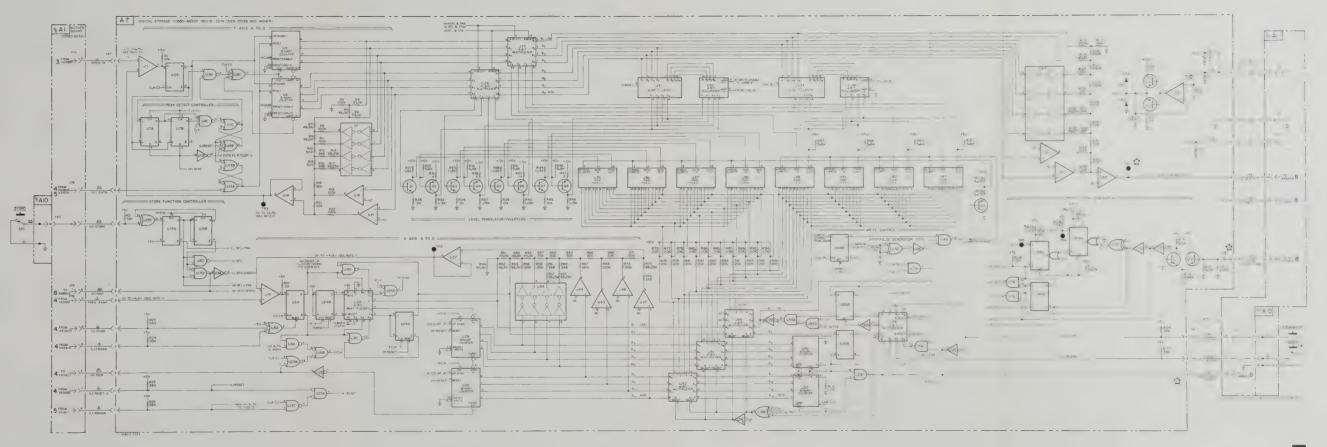
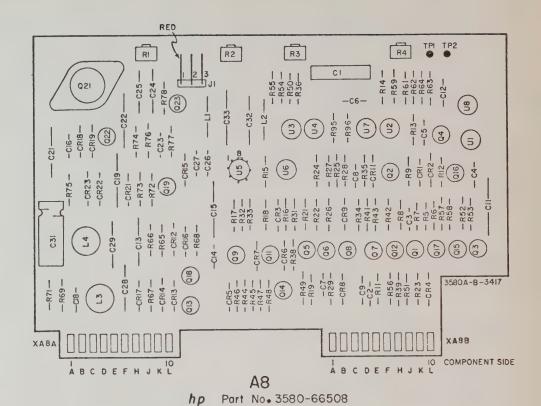
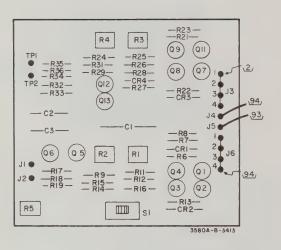


Figure 7-10. Digital Storage Assembly (A7) Schematic and



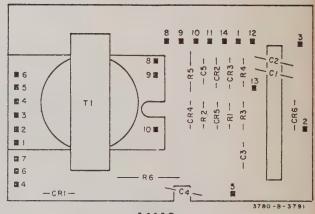
Rev B



A 13 -hp- Part No. 03580-66513 Rev. A

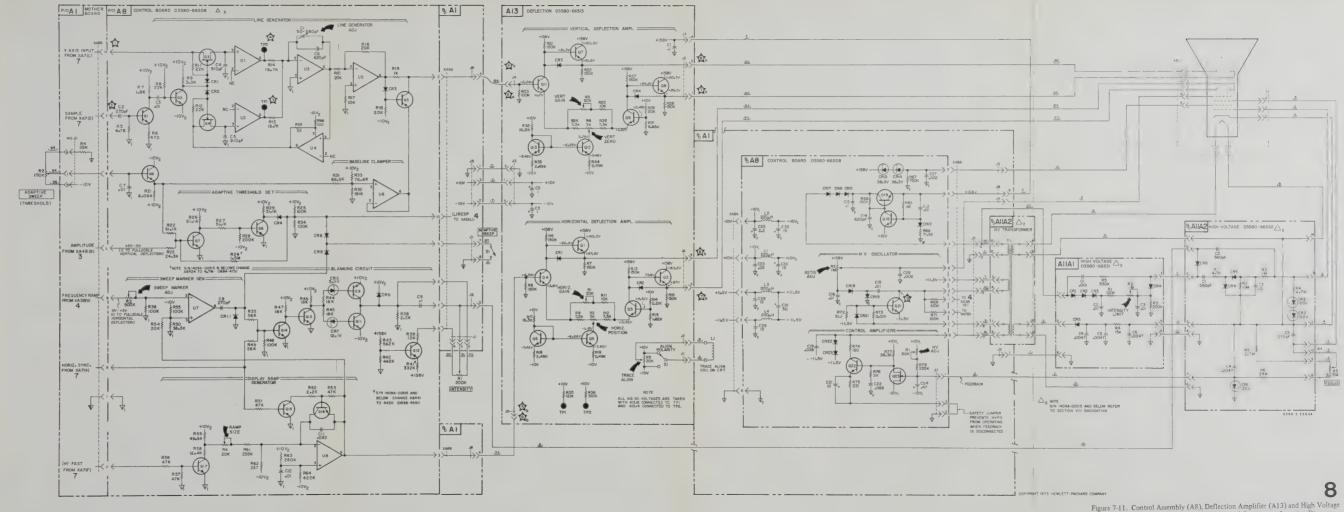


A11A1 -hp- Part No. 03580-66531 Rev. A



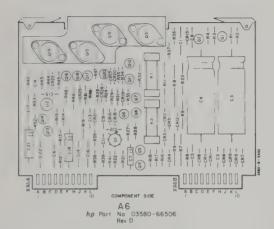
A11A2 -hp- Part No. 03580-66532 Rev. A

These waveforms were made with the 3580A front panel controls adjusted as follows: 3580A DISPLAY STORE DISPLAY .... . BLANK STORE released. STORE as indicated AMPLITUDE MODE . ...LOG 10 dBv/DIV AMPLITUDE REF LEVEL ..NORMAL dBv/LIN - dBm 600 Ω . ..dBv/LIN INPUT SENSITIVITY VERNIER (Amplitude) (Fully CW) 10.0 kHz START - CTR RESOLUTION BANDWIDTH DISPLAY SMOOTHING Chan A 2V/div 3580A DISPLAY STORE (released) SWEEP TIME/DIV SWEEP MODE Option 002: Set dBm 900 Ω/LIN - dBm 600 Ω switch to dBm 900 Ω; set INPUT MODE switch to UNBAL. front panel ZERO CAL and CAL 10 KHz were adjusted to give a full scale display of the 10 KHz CAL signal in the center of the een. It is easiest to set this adjustment in the MANUAL mode and then switch to the REPetitive sweep mode to measure the Chan A 2V/div DISPLAY STORE I waveforms were recorded using a 10:1 divider probe on the dc coupled oscilloscope inputs. The vertical amplitude sensitivity is the actual emplifier setting and does not include the X10 multiplier introduced Chan A 2V/div 3580A DISPLAY STORE (released) Oscilloscope: ac coupled Crereased)
Creatioscope
discoupled Chan A 2V/div DISPLAY STORE (released) Oscilloscope: ac coupled Chan B 2V/div Must be 55 kHz to 70 kHz 1 msec div



Power Supply (A11) Schematics and Component Location Diagrams.





#### \*NOTE

± 11.5 V Power Nominal ± 14 V Power Nominal Standard Instrument ≈ ± 20 V Opt 001 (Line Powered) ≈ ± 18 V Opt 001 (Battery Powered) ≈ ± 12 V

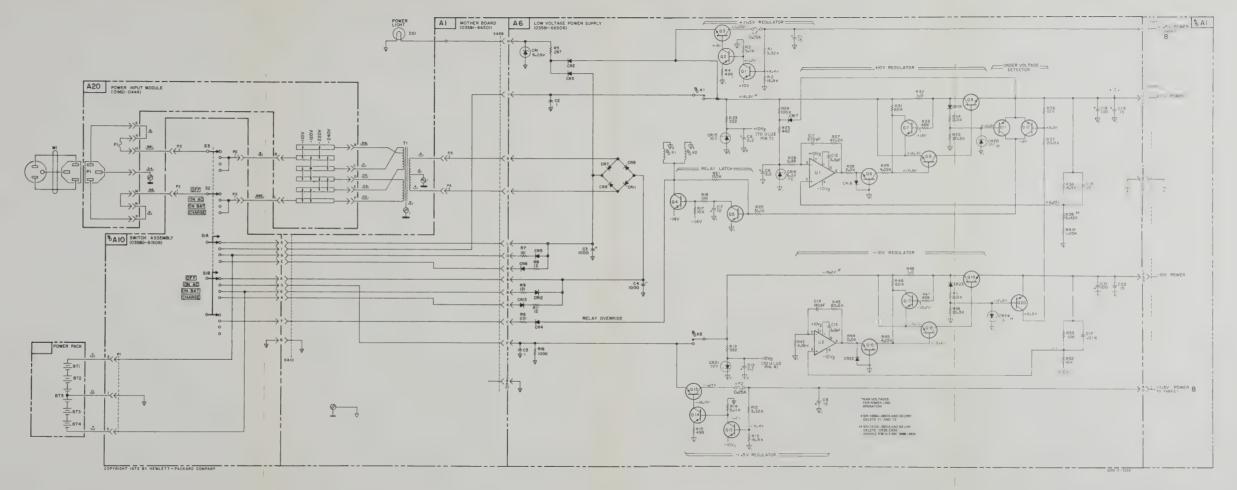
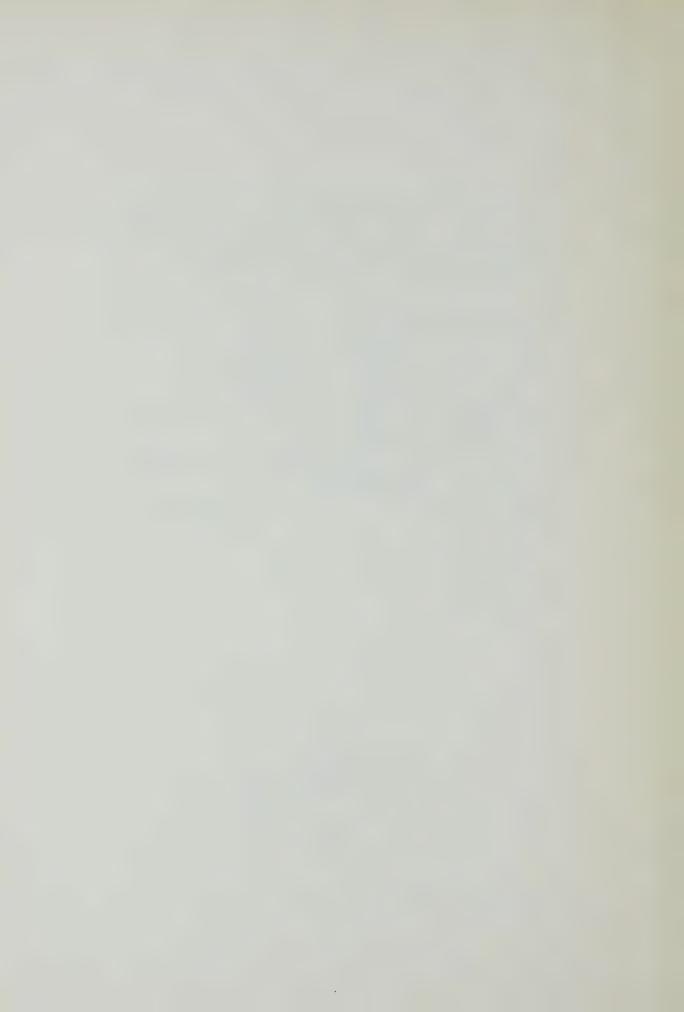


Figure 7-12. Low Voltage Power Supply (A6) Schematic and Component Location Diagram, and Power Input Module (A20) Schematic.



# SECTION VIII BACKDATING

#### 8-1. INTRODUCTION.

8-2. This section contains backdating changes which make this manual applicable to earlier instruments. Where possible, backdating changes have been integrated into the manual text, parts list and schematic diagrams. Changes that are too long or otherwise impractical to integrate into the manual are covered in this section. Backdating changes included in this section are referenced by a numbered delta  $(\Delta_1)$  which appears in the text, parts list and schematic diagrams. The number indicates the number of the corresponding backdating change. Make all backdating changes that apply to your instrument.

**CHANGE NO.**  $\Delta_1$ : Applies to Option 002 instruments with serial number 1312A-00465 and below.

Table 1-1: Change Balanced Input Frequency Response specification to ± 0.5 dB, 300 Hz to 20 kHz.

Paragraph 3-188: Change CAUTION to read as follows:

# ECAUTION 3

When using the balanced terminated input configuration, the differential input level must

not exceed  $+27 \, dBm$  or  $\pm 15 \, V$  dc. Exceeding these input levels will damage the input circuitry.

Figure 3-25(C): Change the Terminated input configuration as shown in Figure 8-1.

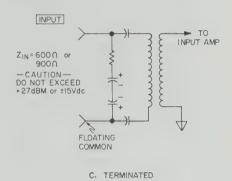


Figure 8-1. Balanced-Terminated Input Configuration.

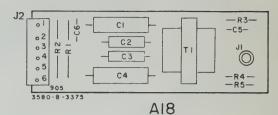
Table 5-13 (pp. 5-15): Delete 40 Hz from the Frequency Response Test.

**Table 6-1:** Change the Balanced Input Assembly (A18) parts list as follows:

A18	03580-66518	1	BOARD ASSY:INPUT.BALANCED [OPTION 002]	28480	03580-66518
A18C1 A18C2 A18C3 A18C4 A18C5	0170-0042 0180-0228 0180-0228 0170-0042 0160-2206		C:FXD MY 0.33UF 5% 100VDCW C:FXD ELECT 22 UF 10% 15VDCW C:FXD ELECT 22 UF 10% 15VDCW C:FXD MY 0.33UF 5% 103VDCW C:FXD MICA 160 PF 5%	99515 56289 56289 99515 28480	E1-334D TYPE E120 1500226X9015B2-DYS 1500226X9015B2-DYS E1-334D TYPE E120 0160-2206
A18C6	0140-0204		C:FXD MICA 47 PF 5%	14655	RDM15E470J5C
A18J1 A18J2	1251-2969 1251-3638		CONN:PHONO, SINGLE JACK CONN:POST	27264 28480	15-24-0501 1251-3638
A18R1 A18R2 A18R3 * A18R3 A18R4	0757-0819 0698-4870 0757-0284	1	R:FXD MET FLM 909 OHM 1% 1/2W R:FXD FLM 604 OHM 1.0% 1/2W R:FXD MET FLM 150 OHM 1% 1/8W FACTORY SELECTED PART R:FXD MET FLM 200K OHM 1% 1/8W	28480 28480 28480 28480	0757-0819 0698-4870 0757-0284 0757-0472
A18R5 A18T1	0698-3245 9100-1460	1	R:FXD MFT FLM 20.5K OHM 1% 1/8W TRANSFORMER AUDIO	28480 28480	0698-3245 9100-1460

Schematic No. 1: Change the Balanced Input Assembly (A18) schematic and component locator as shown in Figure 8-2.

NOTE - ORIENT PLUG SO WIRE (905)
CONNECTS TO PIN 6



hp Part No 03580-66518 Rev A

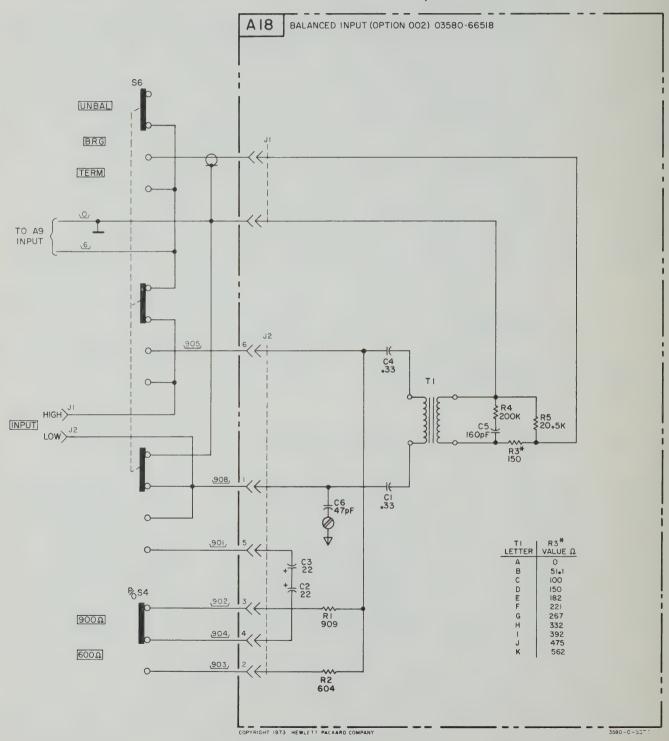


Figure 8-2. Balanced Input Assembly.

**CHANGE NO.**  $\Delta_2$ : Applies to instruments with the following serial numbers:

1312A-00399 and lower

1312A-00402

1312A-00403

1312A-00405

1312A-00408

1312A-00410

1312A-00413

1312A-00416 thru 1409A-00515

**Table 6-1:** Change the High Voltage Power Supply (A11) parts list as follows:

A11	03580-64201	1	POWER SUPPLY-HIGH VOLTAGE	28480	03580-64201
	1251-3069 1251-3201	1 1	CONNECTOR: PC 8 MALE CONTACT CONNECTOR: POST TYPE 3-CONTACT POSITION	28480 27264	1251-3069 09-50-7031
A11 A1 A11 A1C1 A11 A1C2, C3 A11 A1C4 A11 A1C5	03580-66511 0160-3007 0160-3008 0160-3007 0160-3008	1 5 4	PC ASSY:POWER SUPPLY 1, HIGH VOLTAGE C:FXD CFR 4700 PF 20% 4K VDCW C:FXD CER 4700 PF 20% 4K VDCW C:FXD CER 4700 PF 20% 4K VDCW C:FXD CER 4700 PF 20% 4K VDCW	28480 72982 72982 72982 72982	03580-66511 3888-024-Y550-472M 3888-024-Y550-472M 3888-024-Y550-472M 3888-024-Y550-472M
A11A1CR1, CR2 A11A1R1 A11A1R2 *	1901-0341 2100-3359 0687-1041 03580-66512	2 1 1	DIODE:SI 7000 PIV 50MA R:VAR CFRMET 2 MEGOHM 20% TYPE VI 1/2W R:FXD COMP 100K DHM 10% 1/2W FACTORY SELECTED PART BOARD ASSY: POWER SUPPLY 2 - DOESN'T INCLUDE A11A2T1	28480 28480 01121 28480	1901-0341 2100-3359 FB 1041 03580-66512
A11A2C1 A11A2C2 A11A2C3 A11A2C4 A11A2C5	0160-3007 0160-3008 0160-3007 0160-3007 0160-3007		C:FXD CER 4700 PF 20% 4K VDCW C:FXD CER 4700 PF 20% 4K VDCW	72 982 72 982 72 982 72 982 72 982 72 982	3888-024-Y5S0-472M 3888-024-Y5S0-472M 3888-024-Y5S0-472M 3888-024-Y5S0-472M 3888-024-Y5S0-472M
A11A2CE A11A2CR1 A11A2CR2 A11A2CR3 A11A2R1	0160-2544 1902-3428 1902-3428 1902-3428 1902-3237 0836-0001	1 1 1	C:FXD CER 270 PF 10% 1300VDCW DIODE BRFAKDOWN:SILICON 100V 5% DIODE BRFAKDOWN:SILICON 100V 5% DIODE BRFAKDOWN:SILICON 20.0V 5% R:FXD CARRON 50 MEGOHM 10% 2W	56289 28480 28480 28480 28480	C0168102E271KS27-CDH 1902-3478 1902-3428 1902-3428 1902-3237 0836-0001
A11A2R2 A11A2R3 A11A2R4 A11A2R5 A11A2R6	0687-1051 0687-1531 0687-2221 0687-2751 0698-8427	1 1 1 1	R:FXD COMP 1 MFGOHN 10% 1/2W R:FXD COMP 15K OHM 10% 1/2W R:FXD COMP 2200 OHM 10% 1/2W R:FXD COMP 2.T MFGOHN 10% 1/2W R:FXD MFT FLM 29 MFGOHM 10% 1.0W	01121 01121 01121 01121 28480	EB 1051 EB 1531 EB 2221 EB 2751 0698-8427
AllA2T1	9100-3263	1	TRANSFORMER: H.V. (INCLUDES 03580-66517)	28480	9100-3263

Schematic No. 8: Use the High Voltage Power Supply schematic (Figure 8-6) in place of the existing schematic.

**CHANGE NO.**  $\Delta_3$ : Applies to instruments with serial numbers 1415A00935 and below.

Table 6-1, Page 6-22. Delete A8R95 and A8R96 from the A8 assembly parts list.

Figure 7-11, Page 7-29/7-30. Change the A8 schematic as shown in Figure 8-3.

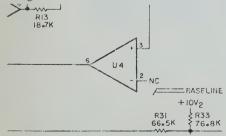


Figure 8-3, Control Board Circuit Change.

CHANGE NO.  $\Delta_4$ : Applies to instruments with serial numbers 1415AA00740 and below.

The new crystal used on the A2 board, Tracking Oscillator Assembly, differs in size from that used in the serial numbers listed above (see Figure 8-4 and 8-5). In order for the tie wrap to hold the new crystal, some new holes must be drilled in the A2 board.

Follow the Crystal Replacement procedure given in Section VII of the manual. While the A2 board crystal is removed, drill two holes in the A2 board about .120 inches (#31 drill bit) in diameter 1/4 inch above the existing tie wrap holes (see Figure 8-5). The new holes may now be used to secure the crystal to the board. The rest of the crystal replacement procedure is unchanged.

Section VIII Model 3580A

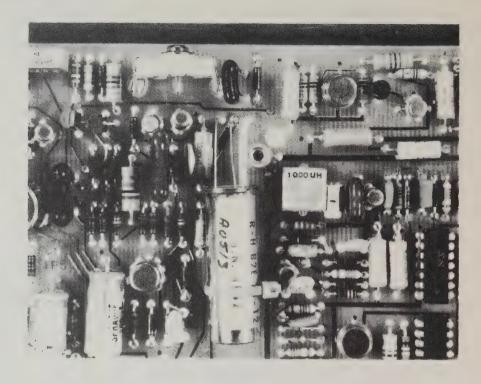


Figure 8-4. Old Style Crystal.

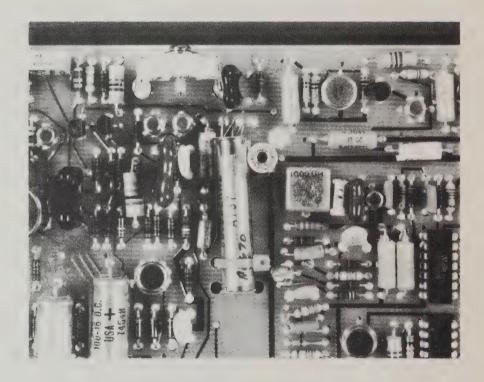


Figure 8-5. New Crystal on Modified A2 Board.

These waveforms were made with the 3580A front panel controls adjusted as follows:

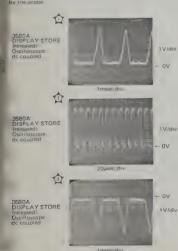
ADAPTIVE SWEEP			(	OF
DISPLAY	BLANK	STOR	E relea	158
			s indica	
AMPLITUDE MODE				
AMPLITUDE REF LEVEL				
dBv-LIN - dBm 600 Ω				
INPUT SENSITIVITY				CA
VERNIER (Amplitude)				
			(Fully	
FREQUENCY				
START - CTR .				CT
RESOLUTION BANDWIDT				0 !
DISPLAY SMOOTHING				
FREQ SPAN DIV .				
SWEEP TIME DIV				
SWEEP MODE				

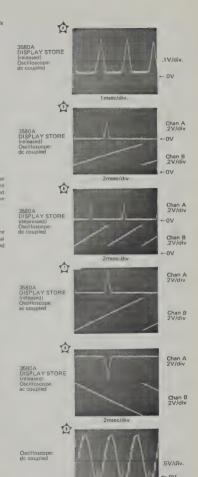
Option 002 Set dBm 900  $\Omega$  LIN dBm 600  $\Omega$  switch to dBm 900  $\Omega$  set INPUT MODE switch to UNBAL.

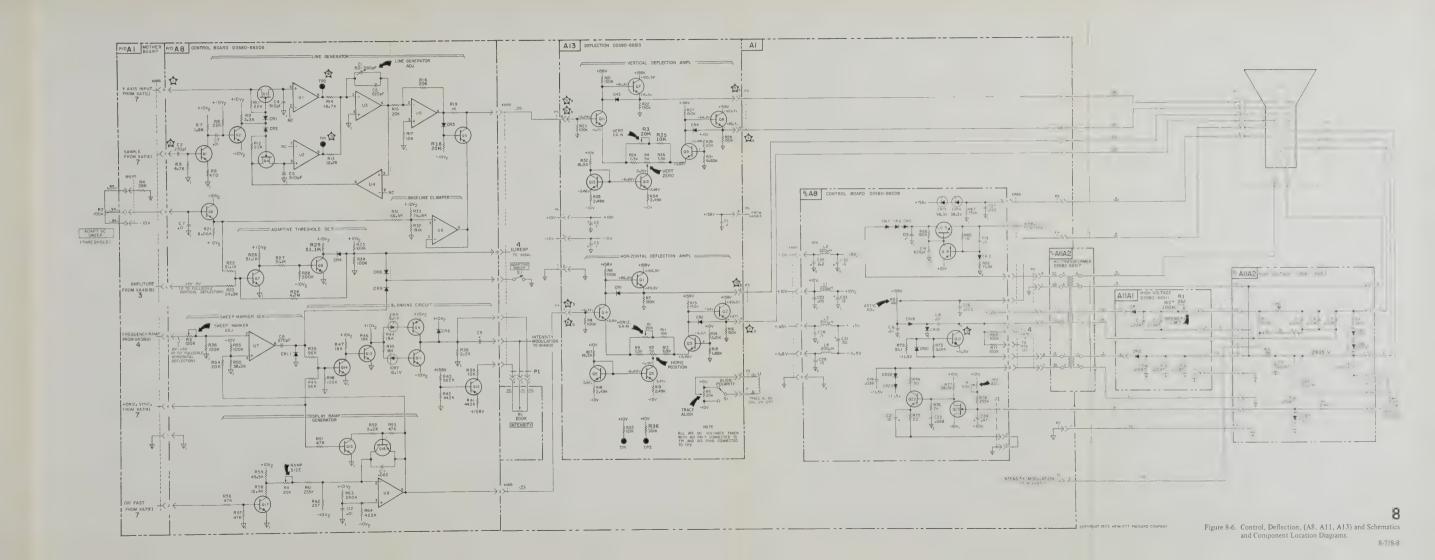
The front panel ZERO CAL and CAL 10 KHz were adjusted to give a full scale display of the 10 KHz CAL signar in the center of the screen. It is easiest to set this adjustment in the MANUAL mode and then switch to the REPetitive sweep mode to measure the waveforms.

#### Oscilloscope Settings

All waveforms were recorded using a 10:1 divider probe on the oscilloscope: inputs, The vertical amplitude sensitivity is the actual amplifier setting and does not include the X10 multiplier introduced









## CODE LIST OF MANUFACTURERS

The following code numbers are from the Federal Supply Code for Manufacturers Cataloging Handbooks H4-1 (Name to Code) and H4-2 (Code to Name) and their latest supplements. The date of revision and the date of the supplements used appear at the bottom of each page. Alphabetical codes have been arbitrarily assigned to suppliers not appearing in the H4 Handbooks.

e	Manufacturer Addres	code No.	Manufacturer	Address	Code No.	Manufacturer Address
00	U.S.A CommonAny supplier of U.S. McCoy ElectronicsMount Holly Springs, Pa	05347 05397	Ultronix, Inc			CTS of Berne, Inc Berne, Ind. Chicago Telephone of California, Inc So. Pasadena, Cal.
3	Sage Electronics Corp Rochester, N. 3 Cemco, Inc Danielson, Com	1. 05574	Div	Canoga Park, Cal.		Bay State Electronics Corp Waltham, Mass. Teledyne Inc., Microwave
14	Humidial Colton, Cali Mictron, Co., Inc. Valley Stream, N. Y	7. 05616	Icore Electro-Plastics Inc Cosmo Plastic (c/o Electrical	· · · · · · · · · · · · · · · · · · ·		Div Palo Alto, Cal. National Seal
13 13 13 16	Garlock Inc	05624	Spec. Co.)	Rockford, Ill.	11453	Precision Connector Corp Jamaica, N. Y. Duncan Electronics Inc Costa Mesa, Cal.
79 131	Amp. Inc Harrisburg, Paricraft Radio Corp Boonton, N. J	05728	Tiffen Optical Co Roslyn Heights, I	Long Island, N.Y.		General Instrument Corp., Semiconductor Division Products
)9 HL5	Croven, Ltd Whitby, Ontario, Canal Northern Engineering	05783	Metro-Tel Corp Stewart Engineering Cop	. Santa Cruz. Cal.	11717	Group Newark, N.J. Imperial Electronic, Inc. Buena Park, Cal.
53	Laboratories, Inc Burlington, Wi Sangamo Electric Co.,	06004	Wakefield Engineering Inc Bassick Co., Div. of Stewart		11870	Melabs, Inc Palo Alto, Cal. Philadelphia Handle Co Camden, N.J.
36	Pickens Div Pickens, S. Coe Engineering Co City of Industry, Ca	1. 00090	Warner Corp R	edwood City, Cal.	12361	Grove Mfg. Co., Inc Shady Grove, Pa. Gulton Ind. Inc., Data System
0 31 0 29		Γ.	Bausch and Lomb Optical	Rochester, N.Y.		Div Albuquerque, N. M. Clarostat Mfg. Co Dover, N. H.
0 )2	General Electric Co., Capacitor Dept	06402 Y.	E.T.A. Products Co. of America	Chicago, III.	12728	Elmar Filter Corp W. Haven, Conn. Nippon Electric Co., Ltd Tokyo, Japan
0 21	Alden Products Co Brockton, Mas Allen Bradley Co Milwaukee, Wi	8. 00040	Amatom Electronic Hardware Co., Inc New	w Rochelle, N. Y.	12881	Metex Electronics Corp Clark, N.J. Delta Semiconductor Inc Newport Beach, Cal.
0 55 0 81	Litton Industries, Inc Beverly Hills, Ca	1. 06555	Beede Electrical Instrument Co., Inc	Penacook, N. H.	12954	Dickson Electronics Corp Scottsdale, Arizona Airco Supply Co., Inc Witchita, Kansas
95		as 06751	General Devices Co., Inc Components Inc., Ariz. Div	Phoenix, Arizona	13061	Wilco Detroit, Mich. Thermolloy. Dallas, Texas
0 49	The Alliance Mfg. Co Alliance, Of	nio 06812 nl. 06980	Torrington Mfg. Co., West Div. Varian Assoc. Etmac Div	San Carlos, Cal.	13327	Tolefunken (GmbH)
0 89	Pacific Relays, Inc Van Nuys, Ca	1. 07088 Y. 07126	Kelvin Electric Co Digitran Co	Pasadena, Cal.	13835	Midland-Wright Div. of Pacific Industries, Inc Kansas City, Kansas
0 30	Amerock Corp Rockford, I	1. 07137	Transistor Electronics	finneapolis, Minn.	14099 14193	Sem-Tech Newbury Park, Cal. Calif. Resistor Corp Santa Monica, Cal.
14		07138 Y.	Westinghouse Electric Corp., Electronic Tube Div.	Elmira, N. Y.	14298	American Components, Inc Conshohocken, Pa. ITT Semiconductor, a Div. of
1 16	Wheelock Signals, Inc Long Branch, N.	1. 07233	Filmohm Corp Cit	y of Industry, Cal.	14400	Int. Telephone and Telegraph Corporation West Palm Beach, Fla.
60		07256 1. 07261	Silicon Transistor Corp Avnet Corp	Culver City, Cal.	14493 14655	Hewlett-Packard Company Loveland, Colo. Cornell Dublier Electric Corp Newark, N.J.
35		07263	Fairchild Camera & Inst. Corp Semiconductor Div M	ountain View, Cal.	14674 14752	Corning Glass Works Corning, N. Y. Electro Cube Inc San Gabriel, Cal.
7	Division Somerville, N.	07387	Minnesota Rubber Co Mo Birtcher Corp, The Mo	onterey Park, Cal.	14960 15106	Williams Mfg. Co. San Jose, Cal. The Sphere Co., Inc. Little Falls, N.J.
7'	Inc Old Saybrook, Con	1.	Sylvania Elect. Prod. Inc., Mt. View Operations M	ountain View, Cal.	15203 15287	Webster Electronics Co New York, N. Y. Scionics Corp Northridge, Cal.
7:	Hudson Tool & Die Newark, N.	J. 07100	Technical Wire Products Inc	Cranford, N. J.	15291 15558	Adjustable Bushing Co N. Hollywood, Cal. Micron Electronics. Garden City, Long Island, N. Y.
0		97829 Y. 07910	Bodine Elect. Co	. Hawthorne, Cal.	15566 15631	Amprobe Inst. Corp Lynbrook, N. Y. Cabletronics Costa Mesa, Cal.
10	Apex Machine & Tool Co Dayton, Oh	if. 07933	Raytheon Mfg. Co., Semi- conductor Div M	ountain View, Cal.	15772	Twentieth Century Coil Spring Co Santa Clara, Cal.
131	B Parker Seal Co Los Angeles, Ca	ss. 01980	Hewlett-Packard Co., New Jersey Division	. Rockaway, N.J.	15801 15818	Amelco Inc Mountain View, Cal.
138		J. 08145 J. 08289	U.S. Engineering Co Blinn, Delbert Co	Pomona, Cal.	16037	Omni-Spectra Inc Detroit, Ill.
195		08358 J.	Burgess Battery Co Niagara Fall	s, Ontario, Canada	16352 16554	Computer Diode Corp Loai, N.J. Electroid Co Union, N.J.
100	9 Arrow, Hart and Hegeman Elect Co Hartford, Co.	08524 n. 08664	Deutsch Fastener Corp Bristol Co., The	Waterbury, Conn.	16585	Boots Aircraft Nut Corp Pasadena, Cal.
)1 )26	Taruus Corp Lambertville, N. 2 Arco Electronic Inc Great Neck, N.	y. 08717 Y. 08718	Sloan Company			De Jur Meter Div Brooklyn, N. 1. Delco Radio Div. of G. M. Corp Kokomo, Ind.
21	2 Hi-Q Division of Aerovox. Myrtle Beach, S.	C. 08121		Paramus, N.J.	17109	Tranex Company
35 40	4 Palo Alto Division of Hewlett-		Operations, Div. of CBS Inc .	Lowell, Mass.	17745	Angstrohm Prec Inc. No. Hollywood, Cal.
65	Packard Co		Miniature Lamp Dept	Cleveland, Ohio	17056	Siliconix Inc
67		al. 09026	Babcock Relays Div	. Costa Mesa, Cal.	18042	Power Design Pacific Inc Palo Alto, Cal.
71	Prod. Div Phoenix, Arizo	09097 ona 09134 09145	Texas Capacitor Co	Houston, Texas	19476	Signetics Corp Sunnyvale, Cal.  Ty-Car Mfg. Co., Inc
73	Div Culver City, C	al.	Electro Assemblies, Inc.	Chicago, III.	18486 18565	Chamerics Plainville, Mass.
	6 Sequoia Wire Co Redwood City, C	al. 09353	C & K Components Inc	Newton, Mass.	18583	Curtis Instrument, Inc
81	70 P.M. Motor Company Westchester,		Canada, Ltd Toront	to, Ontario, Canada fton Heights, Penn.	18911	E.I. DuPont and Co., Inc Wilmington, Del. Durant Mfg. Co Milwaukee, Wis.
91	Co W. Bridgewater, Ma		Burndy Corp	Norwalk, Conn.	19315	Control Div Teterboro, N.J.
00	Inc Los Angeles, C		Corp	Los Angeles, Cal. Berkelev, Cal.		Thomas A. Edison Industries, Div. of McGraw-Edison West Orange, N. J.
2'	77 Westinghouse Electric Corp. Semiconductor Dept Youngwood,			Niagara Falls, N.Y.	19589	Concoa Baldwin Park, Cal.

0015-49 evised: May, 1970 From: Handbook Supplements H4-1 Dated January 1970

## CODE LIST OF MANUFACTURERS (Continued)

Code		Address	Code		Address	Code	Address
No.	Manufacturer	7444.00	No.	Manufacturer	Audi ess	No.	Manufacturer Address
19644 19701	LRC Electronics Horse Electra Mfg. Co Independe			C. P. Clare & Co Centralab Div. of		78452 78471	Thompson-Bremer & Co Chicago, Ill. Tilley Mfg. Co San Francisco, Cal.
20183	General Atronics Corp Phila		71616	Globe Union Inc	ilwaukee, Wis.	78488	Stackpole Carbon Co St. Marys, Pa.
21226 21355	Executone, Inc Long Island Fafnir Bearing Co., The New Bri		71700	Commercial Plastics Co	ew York, N.Y.	78493 78553	Standard Thomson Corp Waltham, Mass. Tinnerman Products, Inc Cleveland, Ohio
21520	Fansteel Metallurgical Corp N. C.	hicago, Ill.	71707	Coto Coil Co., Inc Pr	ovidence, R.I.	78790	Transformer Engineers San Gabriel, Cal.
23020 23042	General Reed Co Metu Texscan Corp Indian		71744 71785	Chicago Miniature Lamp Works. Cinch Mfg. Co.,	Chicago, Ill.	78947 79136	Ucinite Co Newtonville, Mass.
23783	British Radio Electronics Ltd Wash	ington, D.C.		Howard B. Jones Div	Chicago, Ill.	79142	Waldes Kohinoor Inc Long Island City, N. Y. Veeder Root, Inc Hartford, Conn.
24455 24655	G. E. Lamp Division, Nela Park, Cleve	eland, Ohio	71984 72136	Dow Corning Corp	Midland, Mich.	79251	Wenco Mfg. Co Chicago, Ill.
24681	General Radio Co West Con Memcor Inc., Comp. Div		12130	Electro Motive Mfg. Co., Inc.	mantic, Conn.	79727	Continental-Wirt Electronics Corp
26365	Gries Reproducer Corp New Rock	nelle, N.Y.	72619	Dialight Corp		79963	Zierick Mfg. Corp New Rochelle, N. Y.
26462 26851	Grobert File Co. of America, Inc. Carl Compac/Hollister Co Holl		12656	Indiana General Corp Electronics Div	Keashy N.I	80031	Mepco Division of Sessions Clock Co.
26992	Hamilton Watch Co Lane	caster, Pa.	72699	General Instrument Corp.,		80033	Prestole Corp Toledo, Ohio
28480 28520	Hewlett-Packard Co Palo Heyman Mfg. Co Kenilw		72.765	Cap Division	Newark, N.J.	80120	Schnitzer Alloy Products Co Elizabeth, N.J.
30817	Instrument Specialties Co.,		72825	Hugh H. Eby Inc Phi	ladelphia, Pa.	60131	Electronic Industries Association. Standard tube or semi-conductor device,
33173	Inc Little 1 G. E. Receiving Tube Dept Owen		72928 72962	Gudeman Co	.Chicago, Ill.		any manufacturer.
35434	Lectrohm Inc		72964	Robert M. Hadley Co Los		80207	Unimax Switch, Div. Maxon Electronics Corp Wallingford, Conn.
36196	Stanwyck Coil Products,		72982	Erie Technological Products, Inc.	Erie, Pa.	80223	United Transformer Corp New York, N. Y.
36287	Ltd Hawkesbury, Ontar Cunningham, W. H. & Hill,		73061 73076	Hansen Mfg. Co., Inc		80248 80294	Oxford Electric Corp Chicago, Ill. Bourns Inc Riverside, Cal.
	Ltd Toronto, Ontar	io, Canada	73138	Helipot Div. of Beckman Inst., Inc	3.		Arco Div. of Robertshaw Controls Co.
37942 39543	P.R. Mallory & Co., Inc Indiana Mechanical Industries Prod. Co A		73293	Hughes Products Division of	Fullerton, Cal.	90496	All Star Products Inc Defiance, Ohio
40920	Miniature Precision Bearings, Inc K	eene, N.H.		Hughes Aircraft Co Newpo			Avery Label Co Monrovia, Cal.
40931 42190	Muter Co			Amperex Elect. Co Hicksvil Bradley Semiconductor Corp.	le, L.I., N.Y.	80583	
43990	C. A. Norgren Co Englew		10000		Haven, Conn.	80813	Stevens, Arnold, Co., Inc Boston, Mass. Dimco Gray Co Dayton, Ohio
44655	Ohmite Mfg. Co	Skokie, Ill.		Carling Electric, Inc H		81030	International Inst. Inc Orange, Conn.
46384 47904	Penn Eng. & Mfg. Corp Doyle Polaroid Corp Cambrid			Circle F Mfg. Co	Trenton, N.J.	81073 81095	Grayhill Co LaGrange, Ill. Triad Transformer Corp Venice, Cal.
48620	Precision Thermometer &			Div. MSL Industries, Inc Phi	ladelphia, Pa.		Winchester Elec. Div. Litton Ind., Inc.
49956	Inst. Co Southa: Microwave & Power Tube Div Walth		73734 73743	Federal Screw Products, Inc Fischer Special Mfg. Co C		81349	Military Specification
52090	Rowan Controller Co Westm	inster, Md.	73793	General Industries Co., The	. Elyria, Ohio		International Rectifier Corp El Segundo, Cal.
52983 54294	HP Co., Med. Elec. Div Walth Shallcross Mfg. Co So		73846 73899	Goshen Stamping & Tool Co B JFD Electronics Corp B			Airpax Electronics, Inc Cambridge, Maryland
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00000	& System Div So. Norw	alk, Conn.	4455	J. H. Winns, and Sons Winc	hester, Mass.	02041	Sperti Faraday Inc., Copper Hewitt Electric Div Hoboken, N.J.
56137 56289	Spaulding Fibre Co., Inc Tonaw Sprague Electric Co North Ada			Industrial Condenser Corp R. F. Products Division of	.Chicago, Ill.		Electric Regulator Corp Norwalk, Conn.
58474	Superior Elect. Co Bris		1000	Amphenol-Borg Electronic Corp.		02142	Jeffers Electronics Division of Speer Carbon Co Du Bois, Pa.
59446 59730	Telex Corp To Thomas & Betts Co Eliza		74970	E. F. Johnson Co		82170	Fairchild Camera & Inst. Corp.,
60741	Triplett Electrical Inst. Co Blu	iffton, Ohio	75042	International Resistance Co Phi	ladelphia, Pa.	82209	Space & Defense Systems DivParamus, N.J. Magurie Industries, Inc Greenwich, Conn.
61775	Union Switch and Signal Div. of Westinghouse Air Brake Co Pitts	bungh Do	75263	Keystone Carbon Co., Inc S CTS Knights, Inc	t. Marys, Pa.	82219	Sylvania Electric Prod., Inc.
62119	Universal Electric Co Owo	sso, Mich.	75382	Kulka Electric Corp Mt.	Vernon, N. Y.	82376	Electronic Tube Division Emporium, Pa. Astron Corp East Newark, Harrison, N. J.
63743 64959	Ward-Leonard Electric Co Mt. Ver			Lenz Electric Mfg. Co		82389	Switchcraft, Inc Chicago, Ill.
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66295	Wittek Mfg. Co Cl	hicago, Ill.	6210	C.W. Marwedel San F	rancisco, Cal.	82768	Phillips-Advance Control CoJoliet, Ill.
66346	Minnesota Mining & Mfg. Co. Revere Mincom Div St. P		0433	General Instrument Corp., Micamold Division	Newark, N.J.	82866 82877	Research Products Corp Madison, Wis. Rolton Mfg. Co., Inc Woodstock, N. Y.
70276	Allen Mfg. Co Hartf	ord, Conn.	6487	James Millen Mfg. Co., Inc !	Malden, Mass.	82893	Vector Electronic Co Glendale, Cal.
70309 70318	Allied Control			J.W. Miller Co Los Cinch-Monadnock, Div. of United			Carr Fastener Co Cambridge, Mass. New Hampshire Ball
		City, N.Y.		Fastener Corp San	Leandro, Cal.		Bearing, Inc Peterborough, N. H.
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71002 71034	Birnbach Radio Co New Y		7221	Phaostran Instrument and	lo on dana Cal	83324	Rosan Inc Newport Beach, Cal.
71041	Bliley Electric Co., Inc	Erie, Pa.	7252	Electronic Co So. P Philadelphia Steel and	asadena, Cai.		Smith, Herman H., Inc Brooklyn, N. Y. Tech Labs Palisades Park, N. J.
71218	Murray Co. of Texas Quino Bud Radio, Inc Willow	cey, Mass.		Wire Corp	ladelphia, Pa.	83385	Central Screw Co Chicago, Ill.
71279	Cambridge Thermionics Corp. Cambrid	ige, Mass.		Potter & Brumfield Div P		83501	Gavitt Wire and Cable Co., Div. of Amerace Corp Brookfield, Mass.
71286 71313	Camloc Fastener Corp Para Cardwell Condenser Corp.			TRW Electronic Components Div. General Instrument Corp.,	Camden, N.J.	83594	Burroughs Corp., Electronic
	Lindenhurst, 1	L. I., N. Y.		Rectifier Division B		83740	Tube Div Plainfield, N.J. Union Carbide Corp., Consumer
71400	Bussmann Mfg. Div. of			Resistance Products Co Ha	rrisburg, Pa.		Prod. Div New York, N. Y.
71436	McGraw-Edison Co St. 1 Chicago Condenser Corp Ch	nicago, Ill.		Rubbercraft Corp. of Calif T Shakeproof Division of		83777 83821	Model Eng. and Mfg., Inc Huntington, Ind. Loyd Scruggs Co Festus, Mo.
71447 71450	Calif. Spring Co., Inc Pico-Ri	vera, Cal.	2277	Illinois Tool Works	Elgin, Ill.	83942	Aeronautical Inst. & Radio CoLodi, N.J.
71468	ITT Cannon Electric Inc Los Ang	geles, Cal. 7		Sigma So. Bra Signal Indicator Corp Ne			Arco Electronics Inc Great Neck, N. Y. A. J. Glesener Co., Inc San Francisco, Cal.
71471	Cinema, Div. Aerovox Corp Bur		8290	Struthers-Dunn Inc	Pitman, N.J.		TRW Capacitor Div Ogallala, Neb.

00015-49 Revised: May, 1970

From: Handbook Supplements H4-1 Dated January 1970

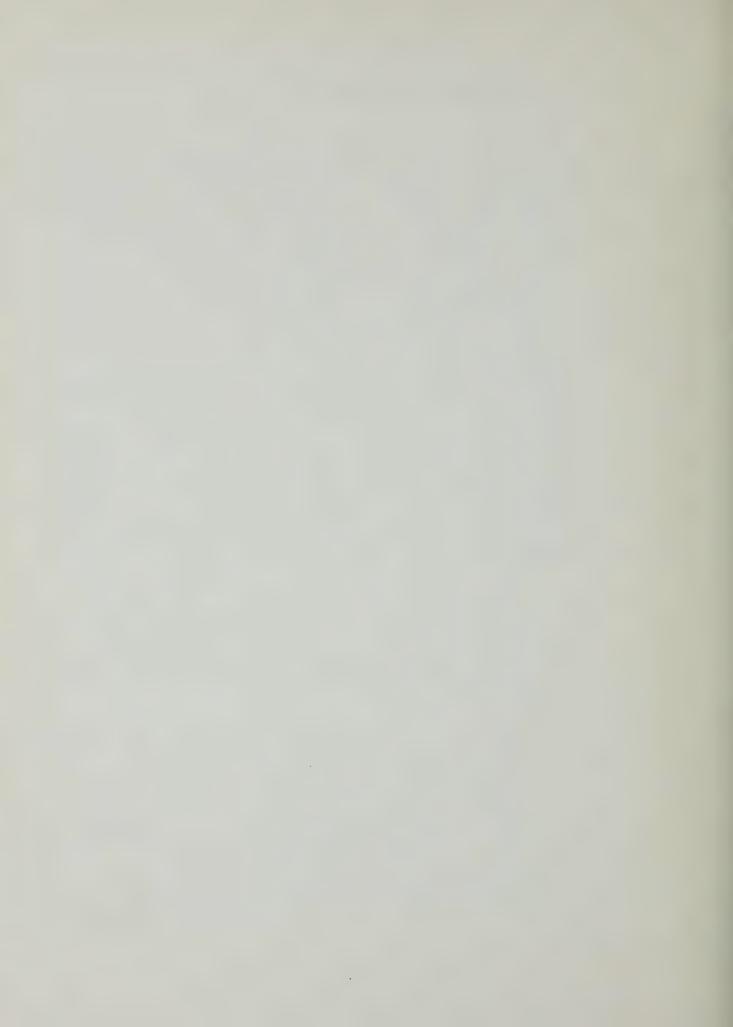
# CODE LIST OF MANUFACTURERS (Continued)

cle	Manufacturer Address	Code No.	Manufacturer	Address	Code No.	Manufacturer Address
28 64 16 77 8 79 8 84 16 16 30 1 40 12 98 8 31 1 79 165 5 763 970 146 260 345 418	Sarkes Tarzian, Inc. Bloomington, Ind. Boonton Molding Company Boonton, N. J. A. B. Boyd Co. San Francisco, Cal. R. M. Bracamonte & Co. San Francisco, Cal. R. M. Bracamonte & Co. San Francisco, Cal. Koiled Kords, Inc. Hamden, Conn. Seamless Rubber Co. Chicago, Ill. Fafnir Bearing Co. Los Angeles, Calif. Clifton Precision Products Co., Inc. Clifton Heights, Pa. Precision Rubber Products Corp. Dayton, Ohio Radio Corp. of America, Electronic Comp. & Devices Division Harrison, N. J. Seastrom Mfg. Co. Glendale, Cal. Marco Industries Anaheim, Cal. Philco Corporation (Lansdale Division)  Western Fibrous Glass Products Co. San Francisco, Cal. Van Waters & Rogers Inc. San Francisco, Cal. Tower Mfg. Corp. Providence, R. I. Cutter-Hammer, Inc. Lincoln, Ill. Gould-National Batteries, Inc. St. Paul, Minn. General Mills, Inc. Buffalo, N. Y. Graybar Electric Co. Oakland, Cal. G. E. Distributing Corp. Schenectady, N. Y. Security Co. Detroit, Mich. United Transformer Co. Chicago, Ill. United Shoe Machinery Corp. Beverly, Mass. U. S. Rubber Co., Consumer Ind. & Plastics Prod. Div. Passaic, N. J. Belleville Speciality Tool Mfg., Inc.  United Carr Fastener Corp. San Francisco, Cal. ITT Cannon Elect. Inc., Salem Div. Salem, Mass. Connor Spring Mfg. Co. San Francisco, Cal. Miller Dial & Nameplate Co. Chicago, Ill.	No.  91929 91961 92180 92367 92607 92702 92966 93332 93369 93410 93632 93492 94137 94144 94197 94222 94330 94375 94682 94682 95233 95265 95236	Honeywell Inc., Micro Switch Division Nahm-Bros. Spring Co. Cru-Connector Corp. Pee Elgeet Optical Co., Inc. Roc Transolite Insulated Wire Co., Inc. Tari IMC Magnetics Corp. Westbury Hudson Lamp Co. K Sylvania Electric Prod. Inc. Semiconductor Div. Westbury Hudson Lamp Co. K Sylvania Electric Prod. Inc. Semiconductor Div. Westbury Hudson Lamp Co. Pallisade Stemco Controls Div. of Essex Wire Corp. Magnet Go. Culva G. V. Controls Liv. General Cable Corp. Baytheon Co., Comp. Div., Ind. Comp. Operations Scientific Electronics Products, Inc. Lo. Wagner Elect. Corp., Tung-Sol Div. Curtiss-Wright Corp. Electronics Div. East Pasouth Chester Corp. Wire Cloth Products, Inc. Automatic Metal Products Co. Biworcester Pressed Aluminum Cory. Magnet Electric Co. George A. Philbrick Researchers, Allies Products Corp. Continental Connector Corp. Worlamon, Inc. Brid Corp. Bid. Gordon Corp.	ion Preeport, Ill. Pakland, Cal. abody, Mass. hester, N. Y. r, L. I., N. Y. earney, N. J. boburn, Mass. s Park, N. J. ansfield, Ohio er City, Cal. ingston, N. J. kayonne, N. J. kayonne, N. J. chester, Pa. Beliwood, Ill. rooklyn, N. Y. p. cster, Mass. Chicago, Ill. Inc. Boston, Mass. vrence, Mass. rence, Mass.	No.  96095 96256 96296 963396 96331 96501 96508 96733 96881 97464 97559 97979 98821 98731 98734 98821 98734 98928 99928	Hi-Q Div. of Aerovox Corp Olean, N. Y. Thordarson-Meissner Inc
418 506 637 662 673 737 827 886	Radio Materials Co. Chicago, III. Augat Inc. Attleboro, Mass Dale Electronics, Inc. Columbus, Nebr Elco Corp. Willow Grove, Pa Epiphone Inc. New York, N. Y Gremar Mfg. Co., Inc. Wakefield, Mass K F Development Co. Redwood City, Cal Malco Mfg., Inc. Chicago, III.	. 95348 . 95354 . 95566 . 95712 . 95984 . 95987	Gordos Corp. Blo Methode Mfg. Co. Rolling Arnold Engineering Co. Dage Electric Co., Inc. Siemon Mfg. Co. Weckesser Co. Microwave Assoc., West. Inc. S	Meadows, Ill. Marengo, Ill. Franklin, Ind. Wayne, Ill. Chicago, Ill.	99934 99942	

he following HP Vendors have no number assigned in the latest supplement to the Federal Supply Code for Manufacturers Handbook.

000Z	Willow Leather Products Corp Newark, N.J.	000MM	Hewlett-Packard Co., Colorado Springs Div Colorado Springs, Colorado Rubber Eng. & Development	000WW 000YY	CooltronOakland, Cal. California Eastern LabBurlington, Cal. S.K. Smith CoLos Angeles, Cal.
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